



(51) International Patent Classification:
C25D 5/48 (2006.01)

(21) International Application Number:
PCT/US2010/054682

(22) International Filing Date:
29 October 2010 (29.10.2010)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
12/608,326 29 October 2009 (29.10.2009) US

(71) Applicant (for all designated States except US):
SHOCKING TECHNOLOGIES, INC. [US/US]; 5870
Hellyer Avenue, San Jose, California 95138 (US).

(72) Inventor; and

(75) Inventor/Applicant (for US only): **KOSOWSKY, Lex**
[US/US]; Shocking Technologies, Inc., 5870 Hellyer Av-
enue, San Jose, California 95138 (US).

(74) Agents: **KLINE, Keith** et al.; Carr & Ferrell LLP, 120
Constitution Drive, Menlo Park, California 94025 (US).

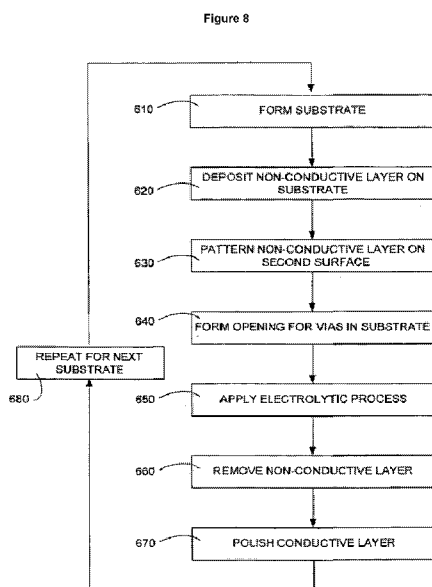
(81) Designated States (unless otherwise indicated, for every
kind of national protection available): AE, AG, AL, AM,
AO, AT, AU, AZ, BA, BB, BG, BH, BR, BW, BY, BZ,
CA, CH, CL, CN, CO, CR, CU, CZ, DE, DK, DM, DO,
DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT,
HN, HR, HU, ID, IL, IN, IS, JP, KE, KG, KM, KN, KP,
KR, KZ, LA, LC, LK, LR, LS, LT, LU, LY, MA, MD,
ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI,
NO, NZ, OM, PE, PG, PH, PL, PT, RO, RS, RU, SC, SD,
SE, SG, SK, SL, SM, ST, SV, SY, TH, TJ, TM, TN, TR,
TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

(84) Designated States (unless otherwise indicated, for every
kind of regional protection available): ARIPO (BW, GH,
GM, KE, LR, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG,
ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ,
TM), European (AL, AT, BE, BG, CH, CY, CZ, DE, DK,
EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU,
LV, MC, MK, MT, NL, NO, PL, PT, RO, RS, SE, SI, SK,
SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ,
GW, ML, MR, NE, SN, TD, TG).

Published:

— with international search report (Art. 21(3))

(54) Title: METAL DEPOSITION



(57) Abstract: Systems and methods include depositing one or more ma-
terials on a voltage switchable dielectric material. In certain aspects, a
voltage switchable dielectric material is disposed on a conductive back-
plane. In some embodiments, a voltage switchable dielectric material in-
cludes regions having different characteristic voltages associated with de-
position thereon. Some embodiments include masking, and may include
the use of a removable contact mask. Certain embodiments include elec-
trografting. Some embodiments include an intermediate layer disposed be-
tween two layers.