

FIG. 1

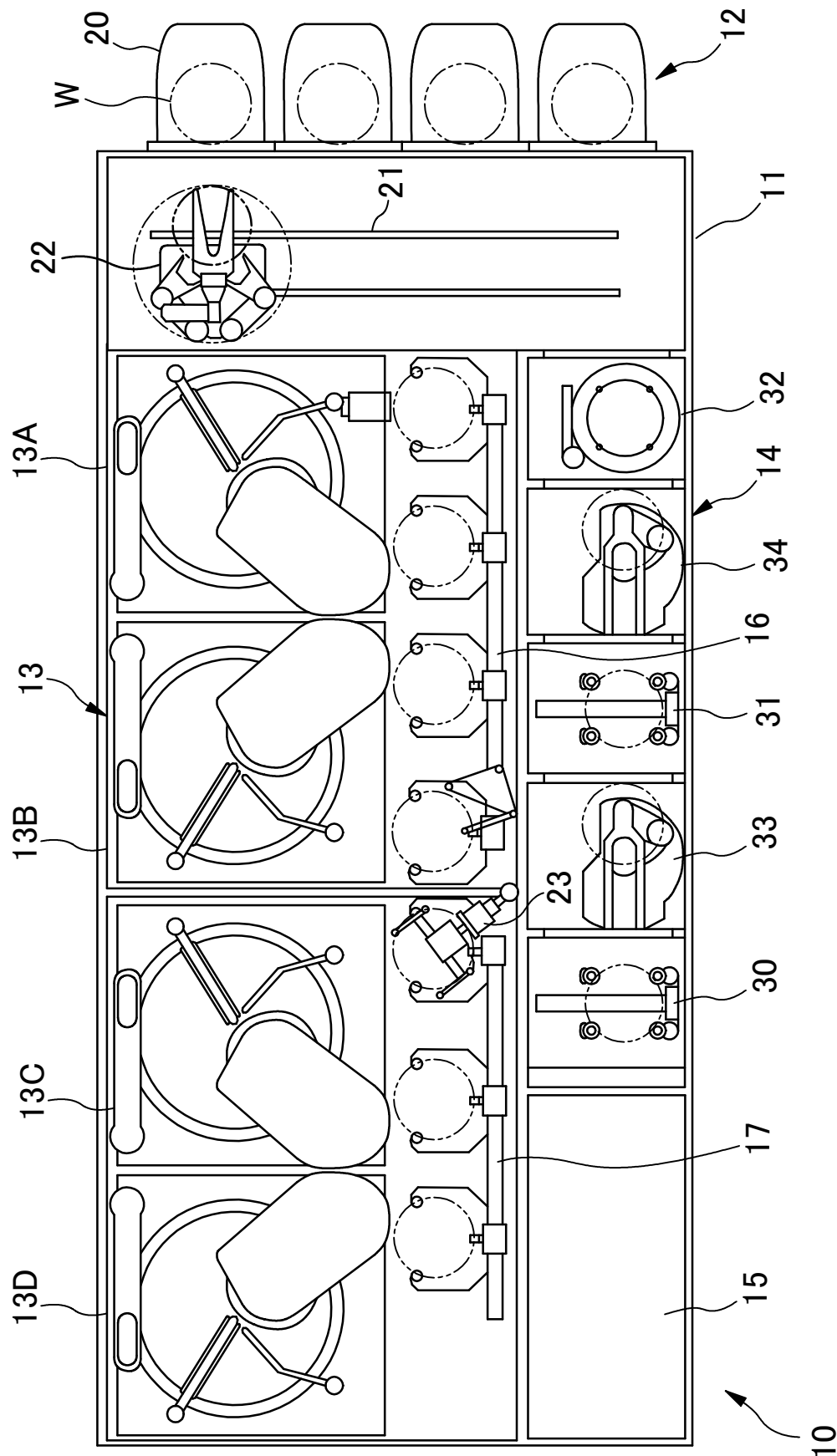


FIG. 2

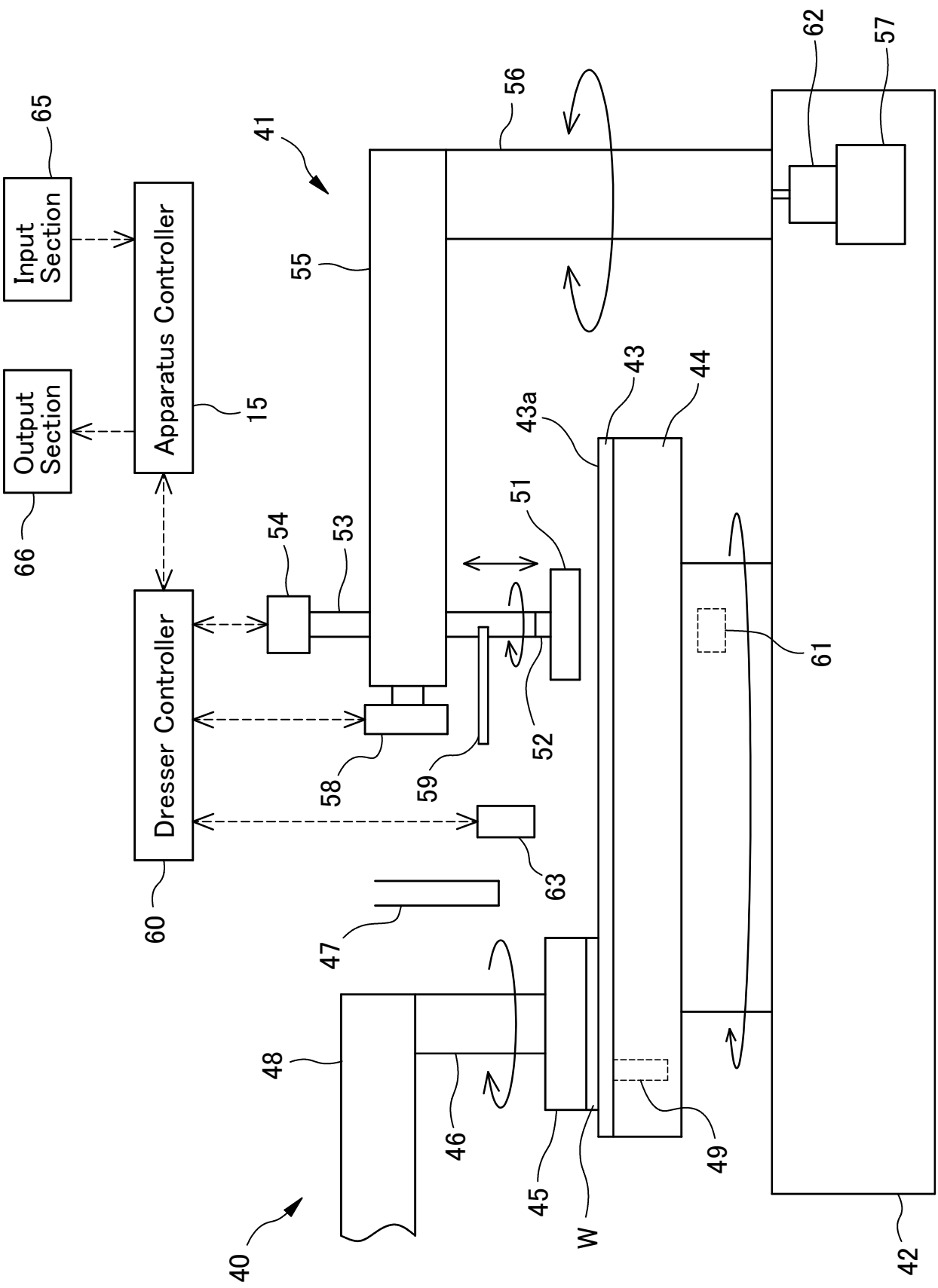




FIG. 4

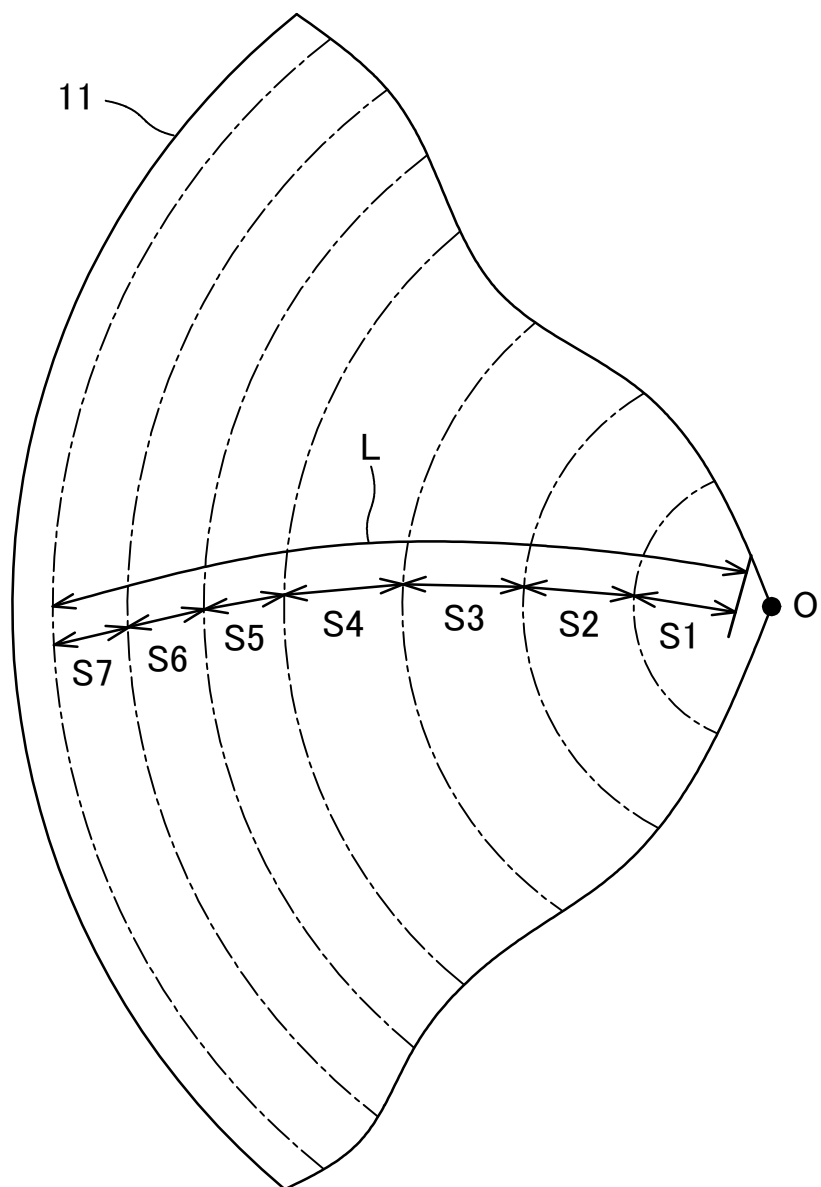


FIG. 5



FIG. 6

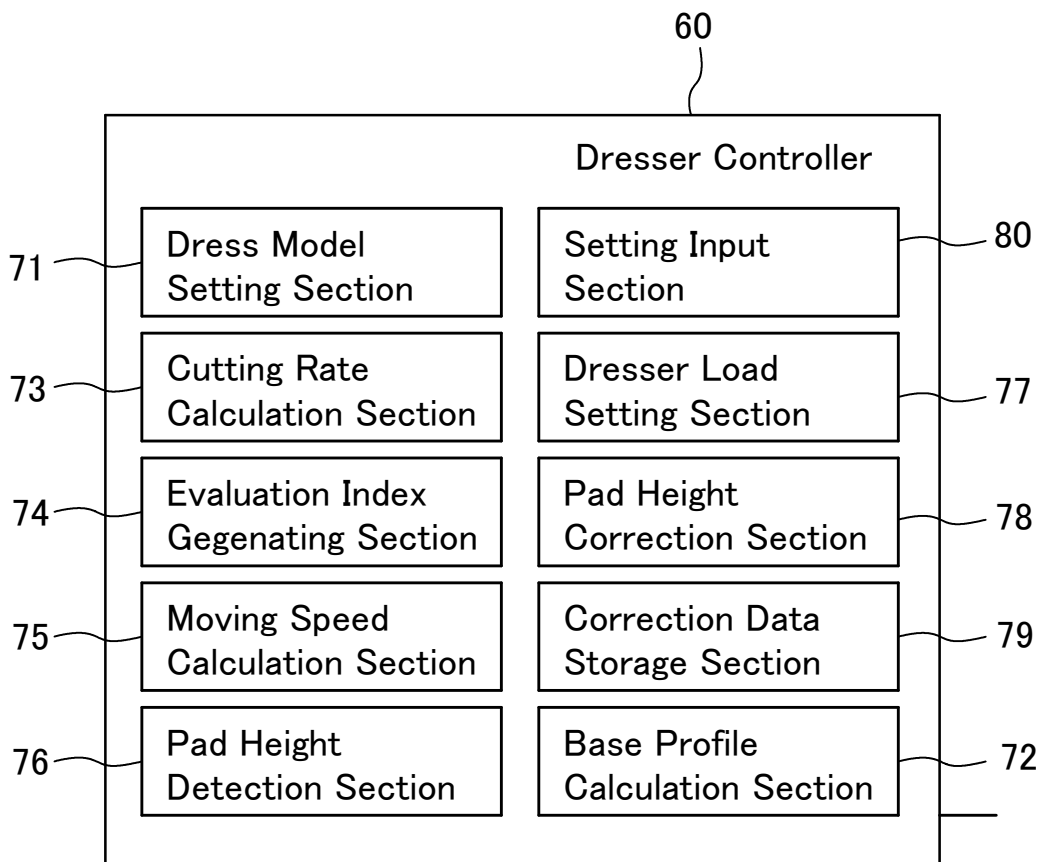


FIG. 7

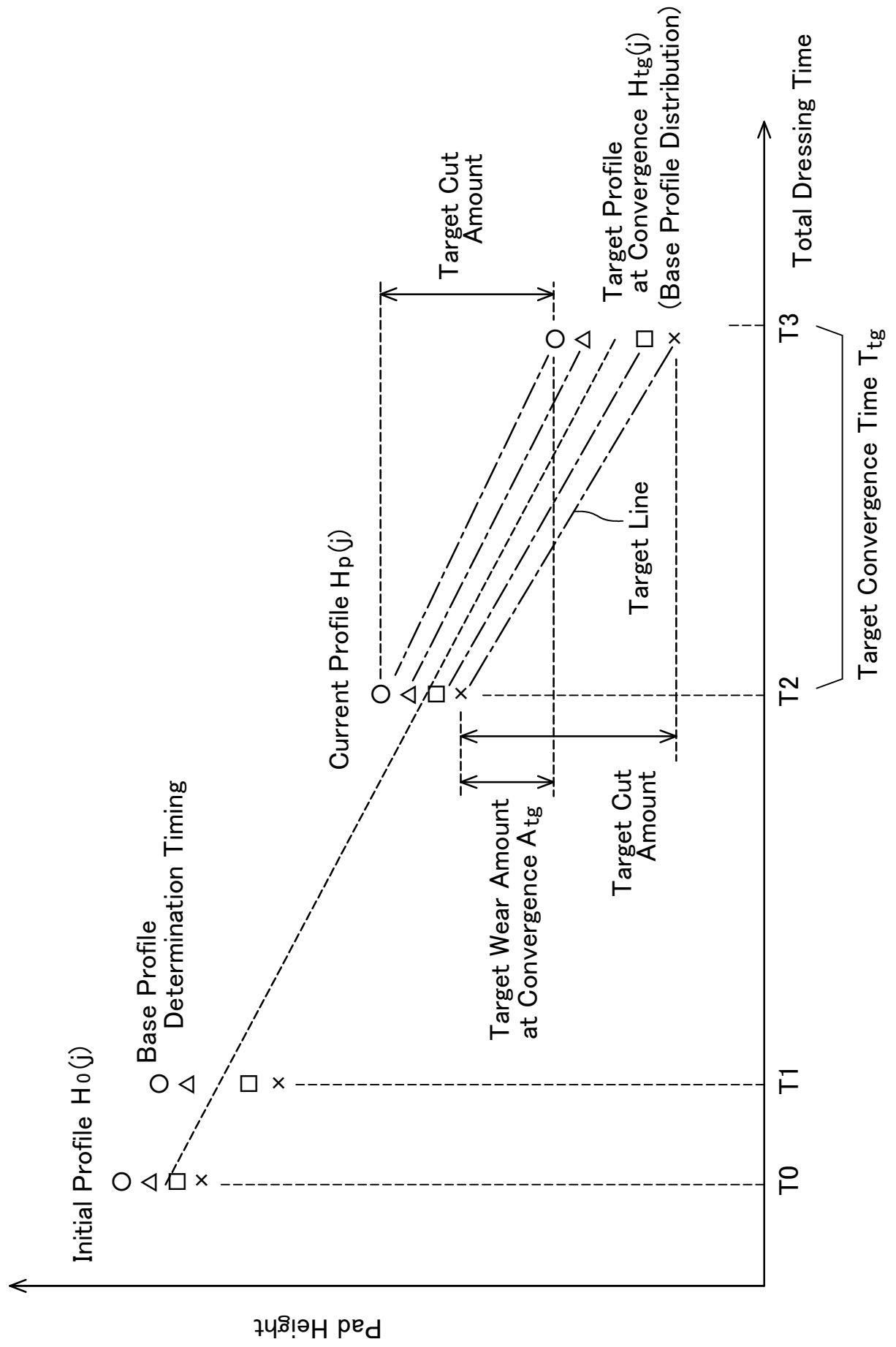


FIG. 8

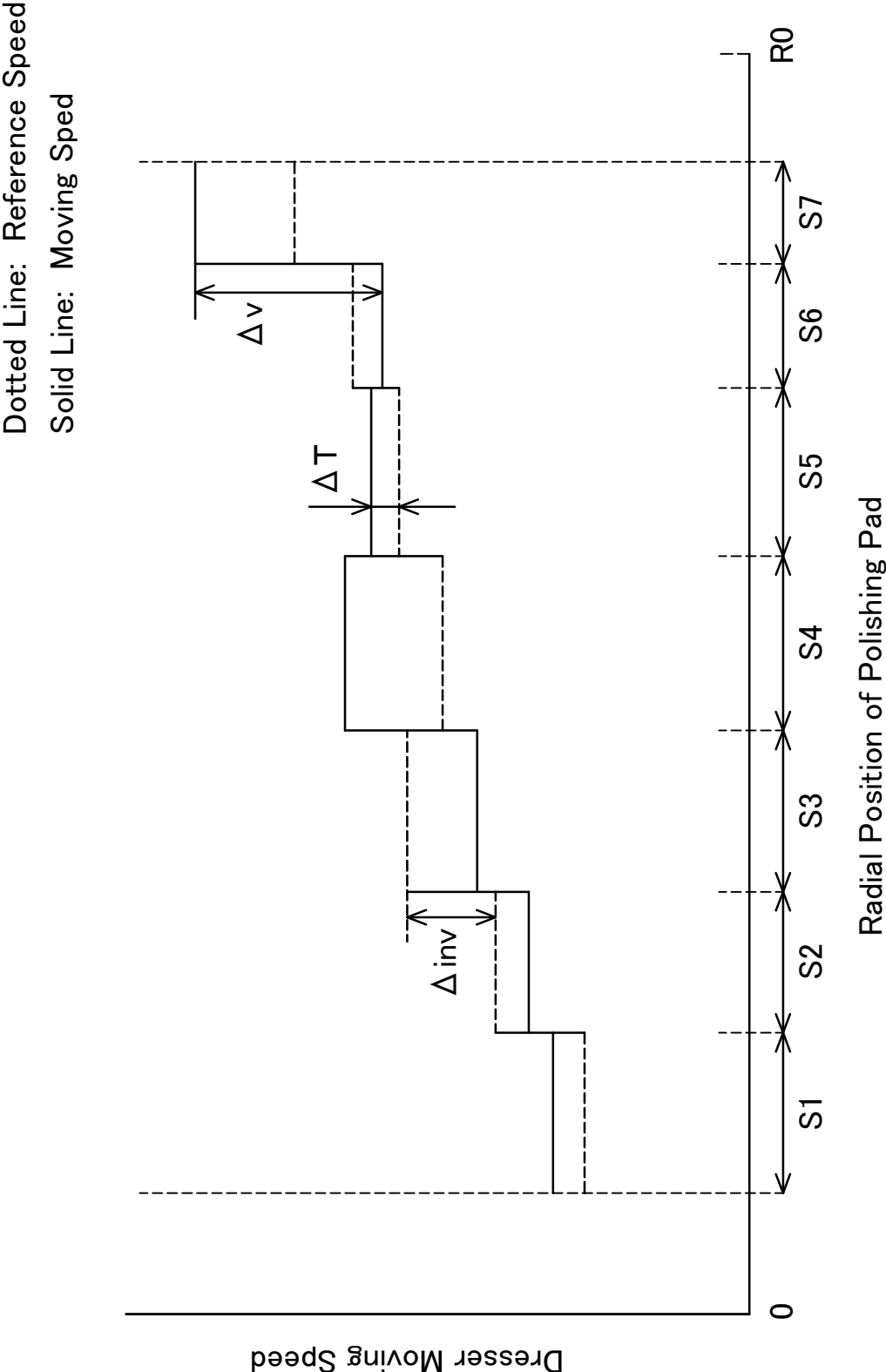


FIG. 9

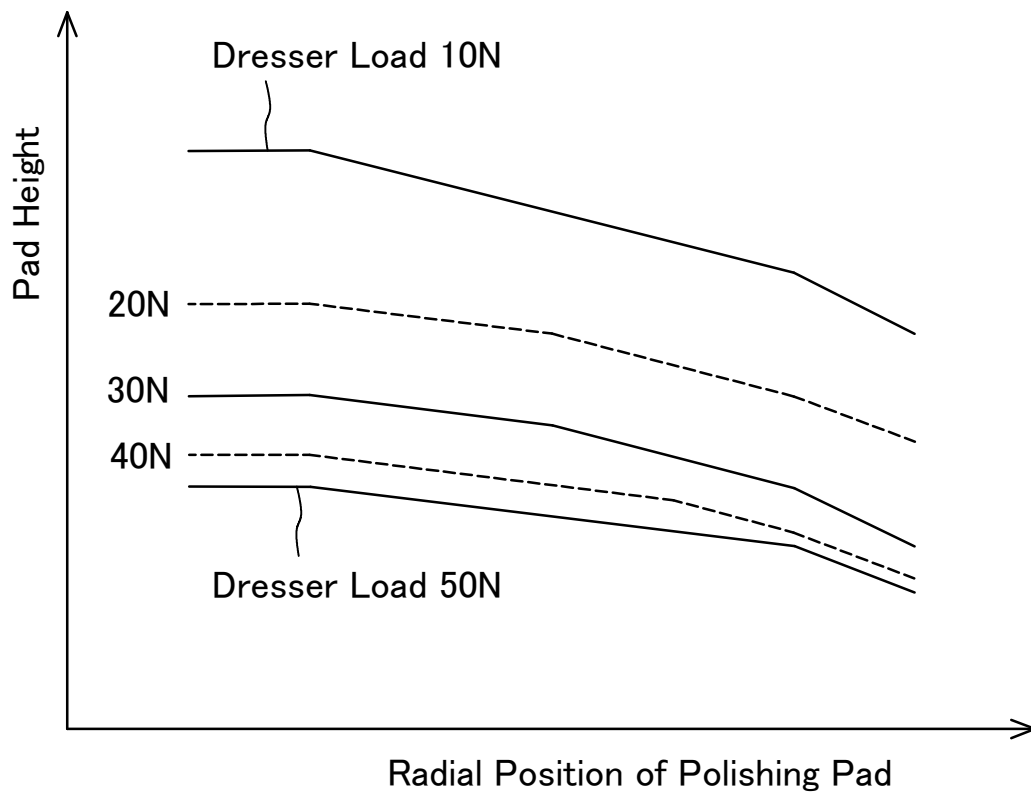


FIG. 10

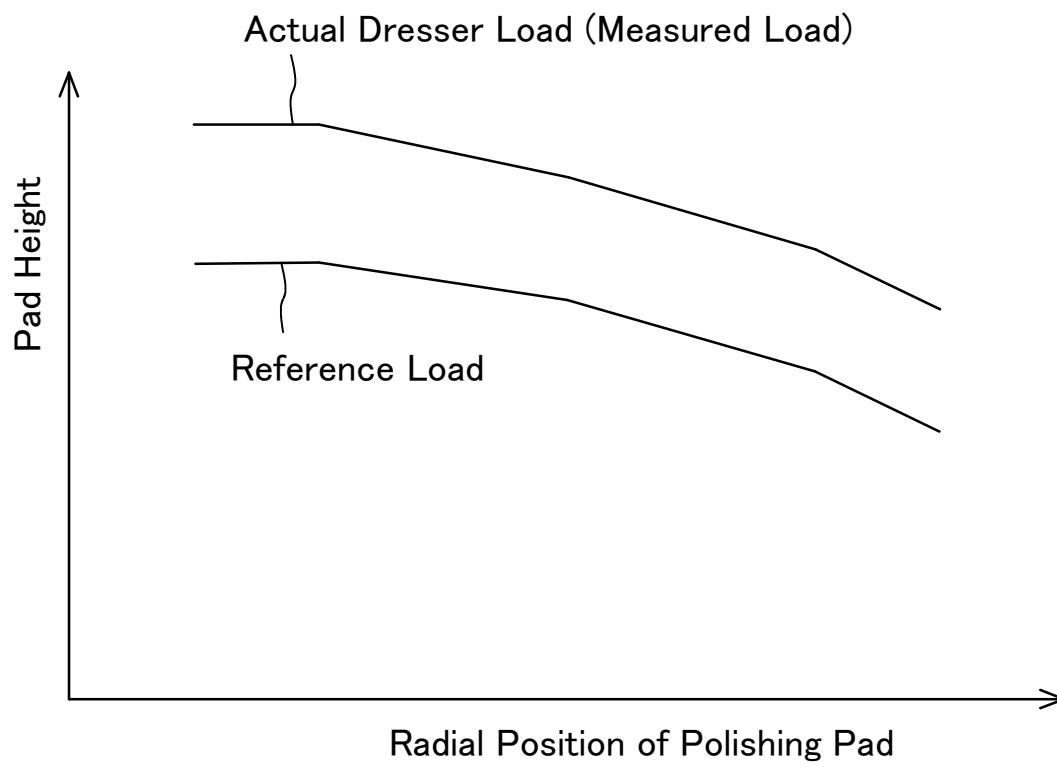


FIG. 11

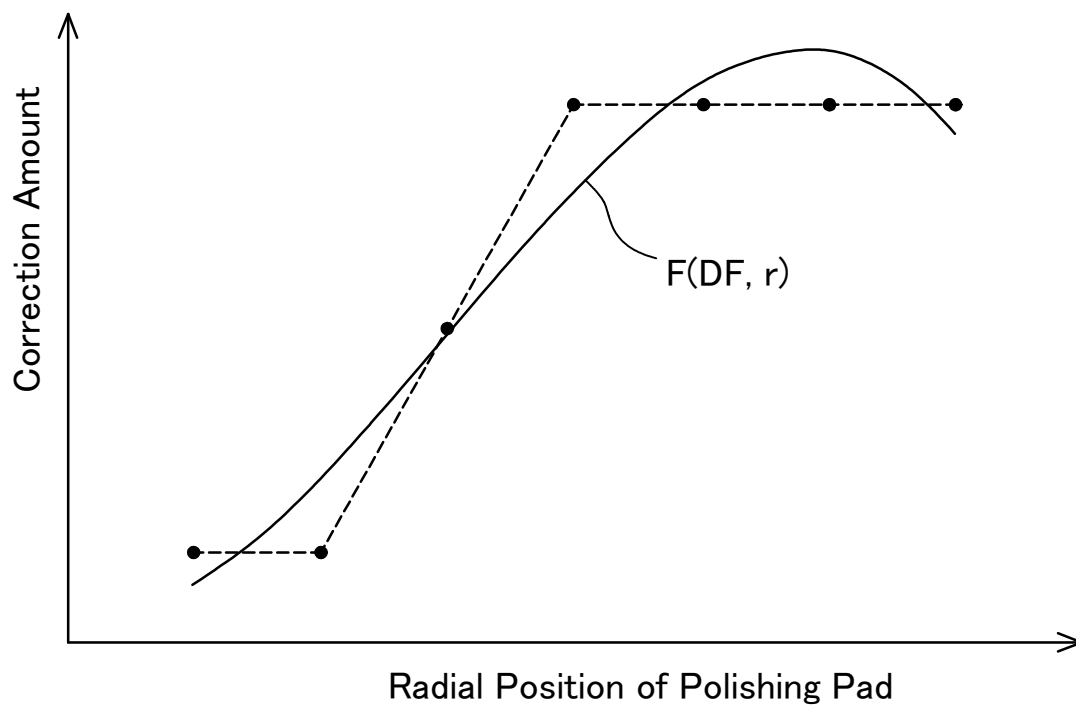


FIG. 12

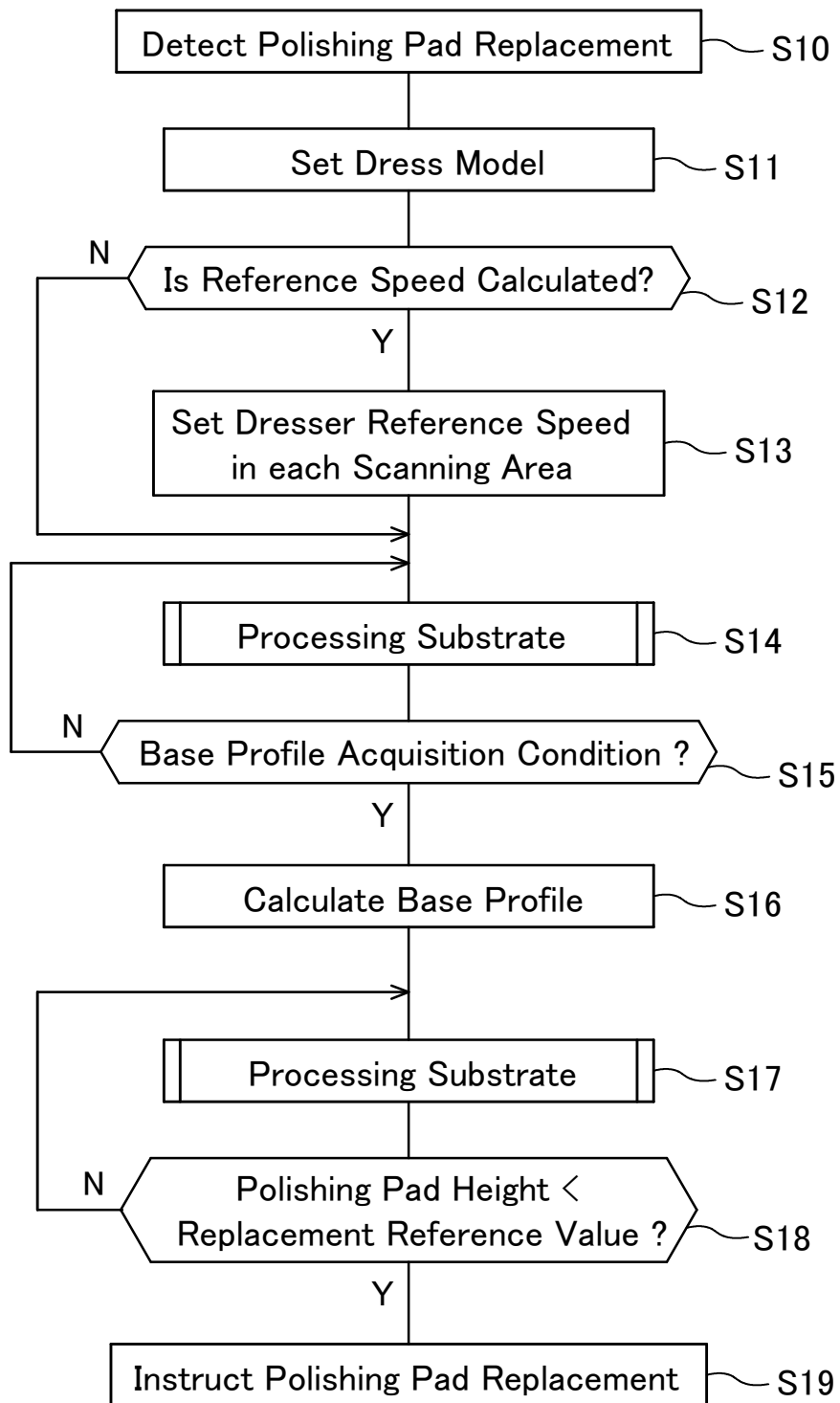


FIG. 13

(Substrate Processing)

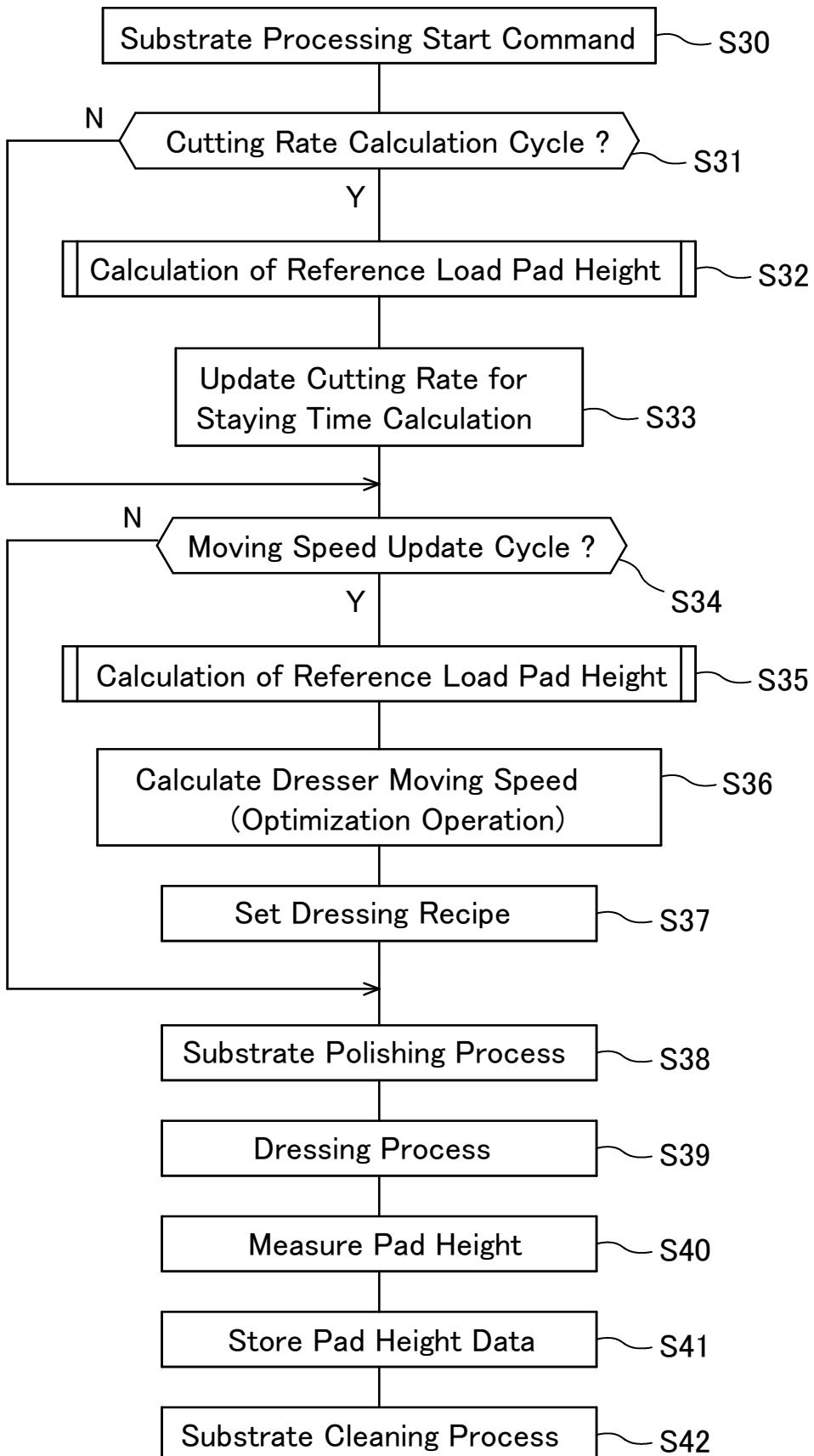


FIG. 14

(Calculation of Reference Load Pad Height)

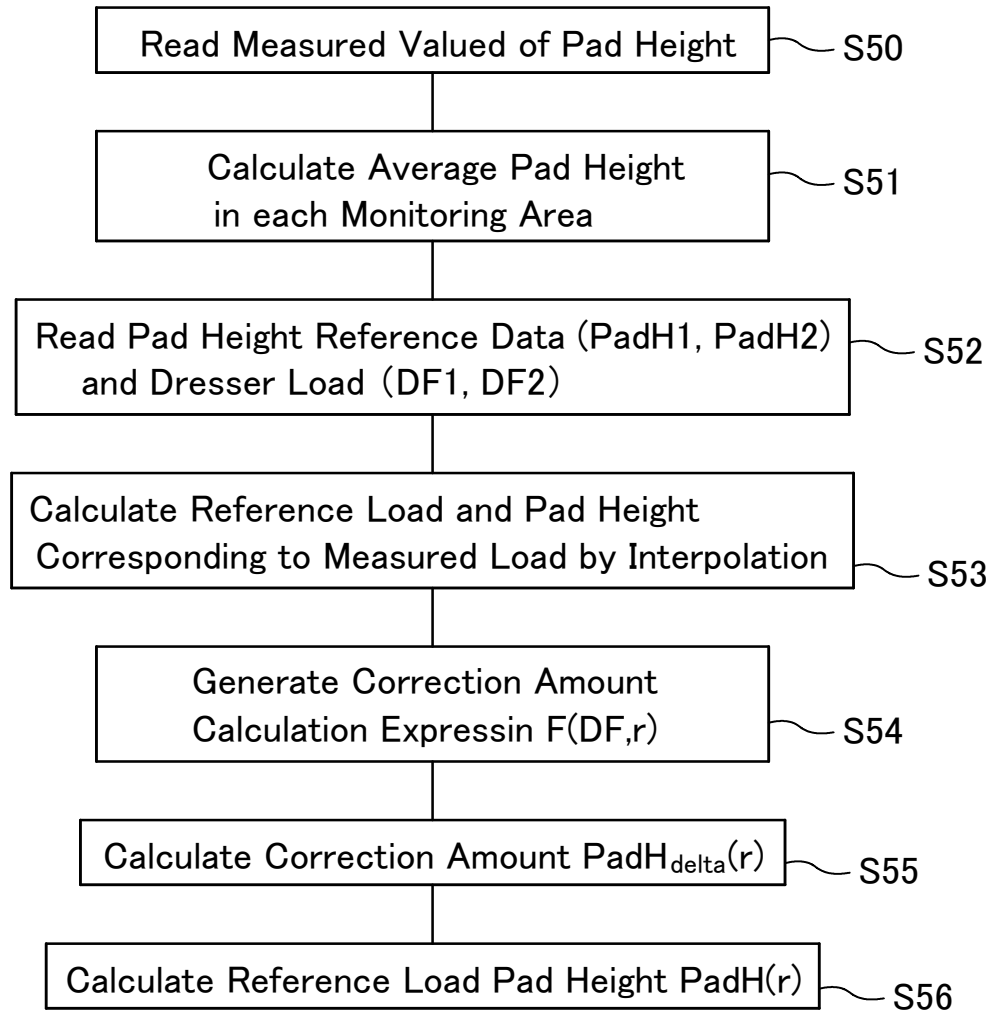
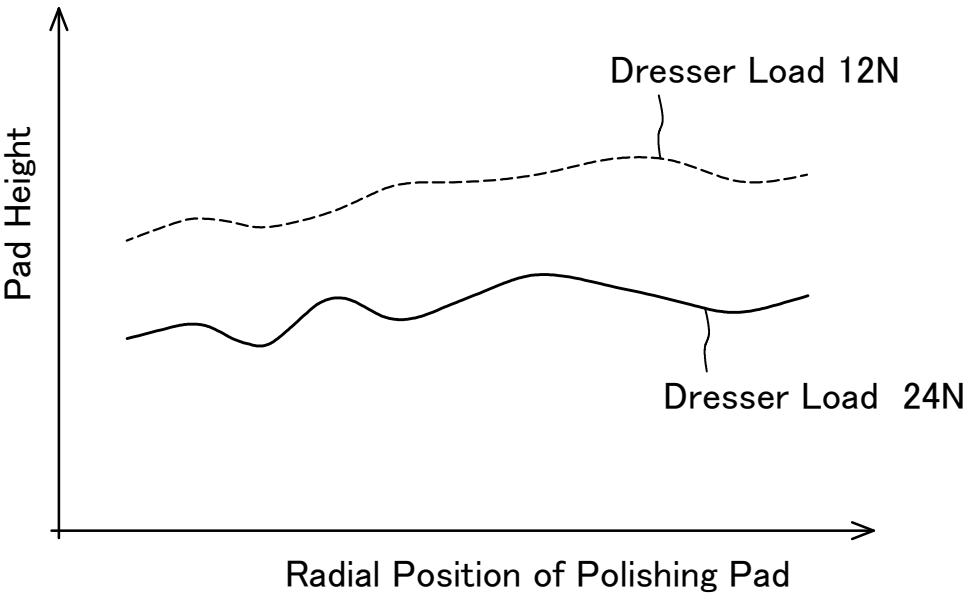
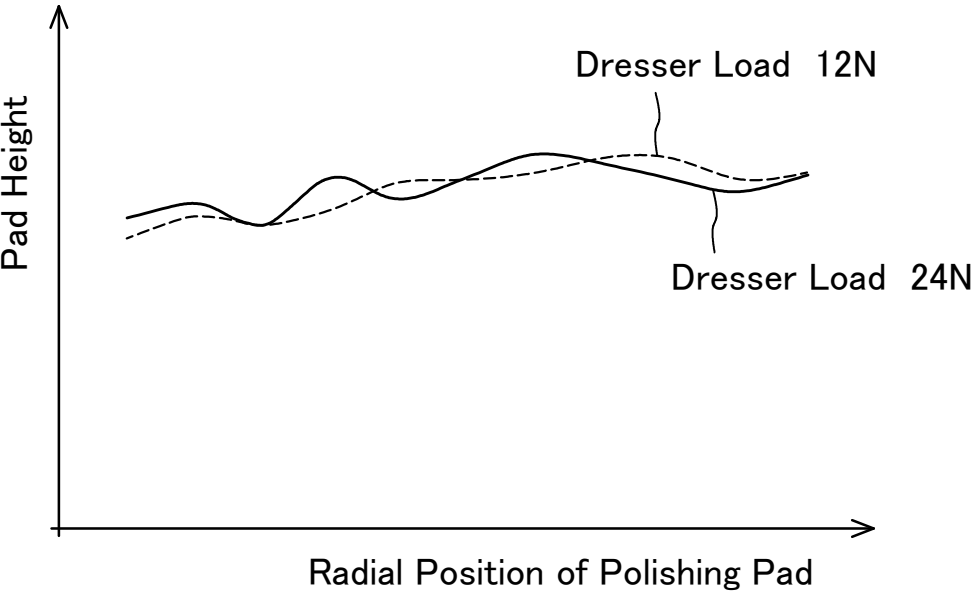


FIG. 15



(a)



(b)