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ELECTRONIC DISABLING DEVICE

FIELD OF THE INVENTION

The present invention relates to devices for disabling an animal or human
5 target; and to methods for providing an electric current through electrodes and the target in a
circuit having an air gap between an electrode and the target.

The original stun gun was invented in the 1960's by Jack Cover. Such prior art
stun guns incapacitated a target by delivering a sequence of high voltage pulses into the skin
of the target such that the current flow through the target interferes with the target's
10 neuromuscular system. Lower power systems cause a stun effect. Higher power systems
cause involuntary muscle contractions. Electronic disabling devices, such as stun guns, have
been made in two designs. A first design has electrodes fixed to the gun. In operation the
user establishes direct contact of the electrodes to the target. A second design operates on a
remote target by launching a pair of darts. Each dart includes an electrode that typically
15 includes a barbed point. The darts either engage the clothing worn by the target or engage the
target's skin. In most cases, a high impedance air gap exists between one or both of the
electrodes and the skin of the target because one or both of the electrodes contact the target's
clothing rather than penetrating the target's skin.

A conventional stun gun 100 may be implemented according to the functional
20 block diagram of FIG. 1. In stun gun 100, closing safety switch S1 connects a battery 102 to
a microprocessor circuit 124 and places stun gun 100 in the "armed" and ready to fire
configuration. Subsequent closure of trigger switch S2 causes microprocessor 124 to activate
high voltage power supply 104. High voltage power supply 104 outputs a pulsed voltage of
about 2,000 volts that is coupled to charge a capacitor 106 to the 2,000 volt power supply
25 output voltage. When the voltage across spark gap GAP1 exceeds the ionization voltage of
air, a relatively high voltage appears across the primary winding of transformer 108.
Transformer 108 steps up this voltage to about 50,000 volts across the electrodes E1 and E2,
ionizing the air in air gaps GAP_A and GAP_B at the target, modeled as a load having an
impedance Z1. A relatively high voltage is thereby applied to load Z1. As the output voltage
30 of capacitor 106 rapidly decreases, current flow through spark gap GAP1 decreases, causing
air in the spark gap to deionize and to resume an open circuit impedance. This "reopening"
of spark gap GAP1 defines the end of each output pulse applied to electrodes E1 and E2. A

typical stun gun of the type illustrated in FIG. 1 produces from five to twenty pulses per second.

Taser International of Scottsdale, Arizona, has for several years manufactured stun guns of the type illustrated in FIG. 1 and designated as the Taser® Model M18 and Model M26 stun guns. High power stun guns such as these typically incorporate an energy storage capacitor 106 having a capacitance of from about 0.2 to about 0.88 microfarads.

It is desirable to disable targets that may be wearing clothing such as a leather or cloth jacket. Clothing functions to establish a gap of about 0.6 cm (0.25 inch) to about 2.5 cm (1 inch) between the target's skin and an electrode. An output voltage of about 50,000 volts will ionize an air gap of this length and support a current sufficient to induce muscular contractions in the target. With high power stun guns, such as the M18 and M26 stun guns, the magnitude of the current flow across the spaced apart stun gun output electrodes may cause numerous groups of skeletal muscles to rigidly contract. For a human target, the stun gun causes the target to lose its ability to maintain an erect, balanced posture. As a result, the target falls to the ground and is disabled.

At about 50,000 volts, the air in one or both GAP_A and GAP_B between output electrodes E1 and E2 and the target ionizes and current begins flowing through electrodes E1 and E2. When electrodes E1 and E2 are presented with a relatively low impedance load Z1 instead of the high impedance air gap or gaps, the stun gun output voltage will drop to a significantly lower voltage level. For example, with a human target and with about probe to probe separation of about 25 cm (10 inches), the output voltage of a model M26 stun gun might drop from about 55,000 volts to about 5,000 volts. Conventional stun guns exhibit this rapid voltage drop because such stun guns are tuned to operate in only a single mode to consistently create an electrical arc across a very high, near infinite impedance air gap. After a low impedance circuit is formed through the electrodes and air gap or gaps at the target, the effective stun gun load impedance decreases toward the target's impedance, generally about 1,000 ohms or less. A typical human subject may present a load impedance of about 200 ohms.

Conventional stun guns have by necessity been designed to have the capability of causing ionization across one or more very high impedance air gaps at the target. As a result, such stun guns have been designed to produce an output from about 50,000 to about 60,000 volts. After ionization, the gap impedance is reduced to a very low level, yet the stun gun continues to operate in the same mode, delivering current or charge into a now very low

impedance target. Consequently, the conventional high power, high voltage stun gun 100 discussed above operates relatively inefficiently, yielding a relatively low electro-muscular effect with relatively high battery power consumption.

5 The M26 stun gun delivers about 26 watts of output power as measured at the capacitor 106. Due to inefficiencies of the high voltage power supply, the battery provides about 35 watts at a pulse rate of 15 pulses per second. Due to the requirement to generate a high voltage, high power output signal, the M26 stun gun requires a relatively large and relatively heavy eight AA cell battery pack 102. In addition, the M26 stun gun power generating solid state components 104, capacitor 106, step up transformer 108, and related
10 parts on the primary side of transformer 108 must operate with relatively high current and high voltage (2,000 volts) and parts on the secondary side of transformer 108 must operate with repeated exposure to even higher voltage (50,000 volts).

Without devices and methods of the present invention, the cost of manufacturing and operating electronic disabling devices will restrict widespread use of these
15 weapons for law enforcement and personal safety.

SUMMARY OF THE INVENTION

An electronic disabling device, according to various aspects of the present invention, for disabling a target includes first and second electrodes to establish first and
20 second spaced apart contact points on the target; and a high voltage power supply for generating an output voltage delivered across the first and second contact points on the target to generate a positive voltage potential at one electrode and a negative voltage potential at the other electrode.

A method for disabling a target, according to various aspects of the present invention, includes: providing from a first stored energy device a first signal to the target to
25 ionize an air gap at the target; and providing from a second stored energy device a second signal to the target to continue a current through the gap and through the target.

A device for disabling a target, according to various aspects of the present invention, includes a circuit for providing from a first stored energy device a first signal to
30 the target to ionize an air gap at the target; and a circuit for providing from a second stored energy device a second signal to the target to continue a current through the gap and through the target.

A method, according to various aspects of the present invention, for monitoring battery capacity for a device operated by a battery, includes: monitoring an operating mode of a plurality of modes of the device; measuring a time that the device operates in each respective operating mode of the plurality; storing indicia of the original battery capacity and indicia of a rate of battery capacity consumption associated with each operating mode of the plurality; and computing a battery capacity consumed based on data received from the operating mode monitoring means, from the operating time monitoring means and from the memory.

A warranty information system, according to various aspects of the present invention, for a device includes a circuit for storing indicia of a duration of a warranty; a circuit for storing a start time for the warranty; and a circuit for providing power for operating the device. The system may further be provided as an operator replaceable part of the device, a replacement system facilitating an extended warranty.

A method, according to various aspects of the present invention, for providing warranty information to a processor of a device covered by the warranty includes: storing indicia of a duration of a warranty; storing a start time for the warranty; and providing power for operating the device. The method may further include providing as an operator replaceable part of the device a replacement module that performs storing of the indicia, storing of the start time, and providing power, thereby facilitating an extended warranty.

DESCRIPTION OF THE DRAWINGS

Systems and methods of the present invention will be described with reference to the drawing wherein like numbers denote like elements, and:

FIG. 1 is a functional block diagram of a stun gun of the prior art;

FIG. 2 is a functional block diagram of an electronic disabling device according to various aspects of the present invention;

FIG. 3 is a graph illustrating a generalized output voltage waveform of the circuit portion 201 of FIG. 2;

FIG. 4 is a graph illustrating a generalized output voltage waveform of the circuit portion 203 of FIG. 2;

FIG. 5 illustrates a high impedance air gap which may exist between one of the electronic disabling device output electrodes E1 and a spaced apart location E3 on a target;

FIG. 6 illustrates the air gap of FIG. 5 after ionization;

5 FIG. 7 is a graph illustrating the impedance of air gap GAP_A of FIG. 5 during time periods of FIGs. 3 and 4;

FIG. 8 is a graph of voltage versus time for the device of FIG. 2;

FIG. 9 is a graph of voltage versus time for the device of FIG. 2;

FIG. 10 is a graph of time for the sequence of two output pulses of FIG. 9;

10 FIG. 11 is a functional block diagram of another electronic disabling device according to various aspects of the present invention;

FIG. 12 is a functional block diagram of yet another electronic disabling device according to various aspects of the present invention;

15 FIGs. 13-18 are timing diagrams illustrating the voltages across capacitors C1, C2, and C3 of FIG. 12 during times T0-T3;

FIG. 19 is a table indicating the effective impedance of GAP1 and GAP2 during time intervals of FIGs. 13-18;

FIG. 20 is a functional block diagram of an alternative implementation of circuit portions 201 and 203 of FIG. 2;

20 FIG. 21 is a schematic diagram of controller 1214 of FIG. 12;

FIG. 22 is a schematic diagram of power supply 1201 of FIG. 12;

FIGs. 23A and 23B form a schematic diagram of another portion of circuitry of the device of FIG. 12;

25 FIG. 24 is a schematic diagram of an alternative circuit for the circuit of FIG. 23B; and

FIG. 25 is a battery power consumption table.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

30 An electronic disabling device, according to various aspects of the present invention, temporarily disables an animal or person (e.g., the target) and may to some extent immobilize and/or incapacitate the target while an electric current from the device is passing through the target. For example, the electronic disabling device 200 of FIG. 2 includes a power supply 202, first and second energy storage capacitors 204 and 210, and switches S1

and S2 that each operate as SPST switches and serve to selectively connect the two energy storage capacitors to down stream circuit elements. Any number of physical capacitors in parallel or series connection may be used to implement a capacitor as discussed herein. The switches may be implemented in any conventional manner such as spark gaps and/or electronic switches (e.g., transistors). Capacitor 204 is selectively connected by switch S1 to a voltage multiplier 208 that is coupled to first and second electrodes E1 and E2. Electrodes may be fixed or implemented in darts as discussed above. Capacitors 204 and 210 are also coupled through a common conductor (circuit ground) to electrode E2.

Trigger 216 (e.g., a switch similar to a gun trigger) controls switch controller 214 that controls the timing and closure of switches S1 206 and S2 212.

The output voltage V_{OUT} across electrode E1 and E2 provided by operation of device 200 is the superposition of the voltage provided by each of two circuit portions 201 and 203. In operation power supply 202 is activated at time T_0 . Capacitors 204 and 210 charge during the time interval T_0 - T_1 . At time T_1 of FIG. 3, switch controller 214 closes switch S1 to couple capacitor 204 to voltage multiplier 208. FIG. 3 shows V_{OUT} as a relatively high voltage during period T_1 to T_2 .

In the hypothetical situation illustrated in FIG. 5, a high impedance air gap exists between electrode E1 and target contact point E3; and skin contact exists between electrode E2 and target contact point E4. Skin contact provides a low (e.g. near zero) impedance. Contact points E3 and E4 are spaced apart on the target as discussed above. The resistor and Z_{LOAD} symbols represent the internal target resistance, typically less than 1,000 ohms, and may be about 200 ohms for a typical human target.

Application of the V_{HIGH} voltage across the E1 to E3 gap GAP_A ionizes the air in the gap to form an arc. Consequently, the impedance of GAP_A drops from a near infinite amount to a near zero amount as in FIG. 7, producing the circuit configuration as in FIG. 6. After this low impedance ionized path from E1 to E3 has been established by the short duration application of the V_{HIGH} output signal, switch controller 214 opens switch S1 and closes switch S2 to couple capacitor 210 to electrodes E1 and E2 as illustrated during period T_2 to T_3 of FIG. 4. Capacitor 210 continues the ionization and maintains the arc across GAP_A for a significant additional time interval. This continuing, lower voltage discharge of the capacitor 210 during the interval T_2 to T_3 transfers a substantial amount of electrical charge through the target to disable the target. The continuing discharge of capacitor 210 through the target will eventually exhaust the charge stored in capacitor 210 and will

ultimately cause the output voltage to drop to a voltage at which ionization is no longer supported in GAP_A. GAP_A will then revert to the non-ionized, high impedance state causing cessation of current flow through the target. FIGs. 8 and 9 illustrate the voltage across electrodes for times T₀ - T₃.

5 Switch controller 214 may be programmed to close switch S1 for a predetermined period of time and then to close switch S2 for a predetermined period of time.

During interval T₃ to T₄, power supply 202 is disabled to maintain a factory preset pulse repetition rate. As illustrated in the timing diagrams of FIGs. 9 and 10, this factory preset pulse repetition rate defines the overall T₀ to T₄ time interval and its
10 repetitions as in times T₄ to T₈ corresponding respectively to times T₀ to T₄. A timing control circuit implemented by a microprocessor maintains switches S1 and S2 in the open condition during the T₃ to T₄ time interval and disables the power supply until the desired T₀ to T₄ time interval has been completed. At time T₄, the power supply will be reactivated to recharge capacitors 204 and 210 to the power supply output voltage.

15 In an alternate implementation, the duration of the interval T₂ to T₃ may be extended. For example, electronic disabling device 1100 of FIG. 11 includes components described above and further includes third capacitor 1118 and diode D1. High voltage power supply 1102 charges capacitors 1110 and 1118 in parallel. While the second terminal of capacitor 1102 is connected to ground, the second terminal of capacitor 1118 is returned to
20 ground through diode D1.

Another electronic disabling device 1200 of FIG. 12 is an implementation of the functions of device 1100 discussed above with reference to the functional block diagram of FIG. 11. In device 1200, high voltage power supply 1202 provides two outputs of equal output voltage capability. Each output supplies a current: I₁ to capacitors 1204 and 1218
25 (corresponding in function to first and third capacitors discussed above), and current I₂ to capacitor 1210 (corresponding in function to the second capacitor discussed above). The first voltage output of high voltage power supply 1202 is also connected to GAP1, a 2,000 volt spark gap; and to the primary winding of output transformer 1208 having a one to twenty-five primary to secondary winding step up ratio. The second terminal of capacitor 1210 is
30 connected to ground while the second terminal of capacitor 1218 is returned to ground through resistor R1. The second voltage output of high voltage power supply 1202 is also connected to GAP2, a 3,000 volt spark gap.

Spark gaps GAP1 and GAP2 are respectively in series with the primary and secondary windings of transformer 1208 having a 1 to 25 step up ratio.

In device 1200, closure of safety switch S1 enables operation of high voltage power supply 1202 and places device 1200 in a standby/ready to operate configuration.

5 Closure of trigger switch S2 causes microprocessor 1224 to assert an activate signal to high voltage power supply 1202. In response, power supply 1202 initiates current flow I1 charging capacitors 1204 and 1218 and current flow I2 charging capacitor 1210. This capacitor charging time interval will now be further described with reference to the voltage versus time graphs of FIGs. 13 through 18.

10 During the interval T0 to T1 capacitors 1204 (C1), 1210 (C2), and 1218 (C3) charge from a zero voltage up to about 2,000 volts in response to outputs from high voltage power supply 1202. Spark gaps GAP1 and GAP2 remain open with near infinite impedance. At time T1 the voltages of capacitors C1 and C3 approach the 2,000 volt breakdown rating of GAP1. At the breakdown voltage of spark gap GAP1, an arc will be formed across GAP1
15 and the impedance of GAP1 will drop to a near zero amount. This drop begins at time T1 in FIGs. 13-16. Beginning at time T1, capacitor C1 will begin discharging through the primary winding of transformer 1208. By operation of transformer 1208, the voltage across electrodes E1 and E2 decreases rapidly to about -50,000 volts as shown in FIG. 16. The voltage across capacitor C1 (FIG. 15) decreases relatively slowly from about 2,000 volts
20 while voltage across spark gap GAP2 increases relatively slowly toward the breakdown voltage of GAP2 (FIG. 16).

Device 1200 exhibits two modes of providing output signal V_{OUT} across output electrodes E1 and E2. In a first operating mode a relatively high voltage is supplied to ionize air in GAP_A with energy supplied by capacitor C1 during time interval T1 to T2. In a
25 second operating mode, a relatively lower voltage is supplied with energy supplied by capacitors C2 and C3 during time interval T2 to T3. At the end of the interval T1 to T2, device 1200 begins operating in the second mode of operation as spark gaps GAP2 and GAP_A conduct at a low (near zero) impedance. The air in spark gaps GAP2 and GAP_A is ionized at time T2 allowing capacitors C2 and C3 to discharge through electrodes E1 and E2 and the
30 relatively low impedance load of the target. As illustrated in FIG. 17, capacitor C1 is discharging to a near zero amount as time approaches T2. Capacitor C1 does not discharge prior to time T2 because spark gap GAP2 is open. During the time interval T2 to T3, the

voltage across capacitors C2 and C3 decreases to zero as these capacitors discharge through the now low impedance (target only) load seen across output terminals E1 and E2.

FIG. 18 presents the voltage across GAP2 and the voltage across electrodes E1 and E2 during time interval T2 to T3. During most of the interval T2 to T3, the voltage across electrodes E1 and E2 has an absolute value less than about 2,000 volts.

In an electronic disabling device according to various aspects of the present invention, capacitor C1 may provide about 0.14 microfarads and discharge during a time interval T1 to T2 of about 1.5 microseconds. Capacitors C2 and C3 may each provide about 0.02 microfarads and discharge during a time interval T2 to T3 of about 50 microseconds.

In other implementations, other durations are used for the duration of interval T1 to T2. This duration may be in the range from about 1.5 to about 0.5 microseconds.

In other implementations, other durations are used for the duration of interval T2 to T3. This duration may be in the range from about 20 to about 200 microseconds.

The duration of interval T0 to T1 depends on the ability of power supply 1201 to supply current sufficient operate device 1200 while charging capacitors C1, C2, and C3. For example, a fresh battery 1201 may shorten the T0 to T1 time interval in comparison to circuit operation with a partially discharged battery. Operation of device 1200 in cold ambient temperature may degrade battery capacity and may also increase the duration of interval T0 to T1.

It is highly desirable to operate electronic disabling devices as discussed above with a predetermined pulse repetition rate as discussed with reference to FIGs. 9 and 10. In one implementation, controller 1214 includes a conventional microprocessor circuit programmed to perform methods according to various aspects of the present invention. According to various aspects of the present invention, controller 1214 provides the activate signal to high voltage power supply 1202 in accordance with a feedback signal to control the duration of digital pulse control intervals (FIG. 10) and consequently cycle durations (TA and TB of FIG. 10). Digital pulse control intervals correspond to interval T3 to T4 discussed above.

For example, controller 12 14 of FIG. 12 includes microprocessor 1224 and feedback signal conditioning circuit 1222. Microprocessor 1224 receives a feedback signal from high voltage power supply 1202 via feedback signal conditioning circuit 1222. Feedback signal conditioning circuit provides to microprocessor 1224 a status signal in response to the feedback signal. Microprocessor 1224 detects when time T3 has been

reached as illustrated in FIGs. 4, 7, 8, 9, 10, 17 and 18. Since the commencement time T0 of the operating cycle is known, the microprocessor will maintain the high voltage power supply in a shut down or disabled operating mode from time T3 until a time sufficient to implement the preset pulse repetition rate (e.g., interval T3 to T4). While the duration of interval T3 to T4 may vary to compensate for other intervals, the microprocessor maintains the T0 to T4 time interval to accomplish the preset pulse repetition rate.

The FIG. 19 table entitled "Gap On/Off Timing" represents a simplified summary of the configuration of GAP1 and GAP2 during the four relevant operating time intervals. The configuration "off" represents the high impedance, non-ionized spark gap state while the configuration "on" represents the ionized state where the spark gap breakdown voltage has been reached.

In an alternate device implementation, the voltages within the device are reduced to facilitate the design of a compact electronic disabling device using conventional insulating materials. For example, an implementation may use a voltage multiplier having dual outputs each providing half the output voltage. The voltage across electrodes E1 and E2 may then be the sum of the dual output voltages. For example, voltage multiplier circuit 2000 of FIG. 20 includes transformer 1008 having a single primary winding and a center-tapped or two separate secondary windings. The step up ratio from the primary winding to each secondary winding is 1 to 12.5. Transformer 1208 still accomplishes the objective of achieving a 25 to 1 step-up ratio for generating an output signal of about 50,000 volts from about a 2,000 volt power supply. One advantage of this double secondary transformer configuration is that the maximum voltage applied to each secondary winding is reduced by 50% in comparison to designs using one secondary winding. Such reduced secondary winding operating potentials may be desired to achieve a higher output voltage with a given amount of transformer insulation or for placing less high voltage stress on the elements of the output transformer.

Substantial and impressive benefits may be achieved by using an electronic disabling device according to various aspects of the present invention in comparison to conventional stun guns represented by the Taser M26 stun gun as discussed above. For example, the M26 stun gun utilizes a single energy storage capacitor of about 0.88 microfarads. When charged to 2,000 volts, that capacitor stores and subsequently discharges about 1.76 joules of energy during each output pulse. For a standard pulse repetition rate of 15 pulses per second and 1.76 joules per pulse, the M26 stun gun requires about 35 watts of

input power which, as explained above, must be provided by a large, relatively heavy battery power supply utilizing 8 series-connected AA alkaline battery cells.

An electronic disabling device according to various aspects of the present invention may use capacitors having capacitance as follows: C1 about 0.07 microfarads and
5 C2 about 0.01 microfarads. The sum of capacitance for C1 and C2 is about 0.08 microfarads. An electronic disabling device 200 using these values for C1 and C2 provides each output pulse from about 0.16 joules of energy stored on these capacitors. With a pulse repetition rate of about 15 pulses per second, these two capacitors consume battery power of about 2.4 watts at the capacitors and roughly 3.5 to 4 watts at the battery. As a result, the battery may
10 be a single AA size battery. This electronic disabling device achieves a 90% reduction in power consumption compared to the M26 stun gun, discussed above.

An electronic disabling device according to various aspects of the present invention generates a time-sequenced, shaped, voltage output waveform as illustrated in FIGs. 3 and 4. The output waveform accommodates two different load configurations
15 presented: a relatively high voltage output operating mode during the high impedance T1 to T2 first operating interval and, a relatively low voltage output operating mode during the low impedance second T2 to T3 operating interval.

As an additional benefit, the circuit elements operate at lower power levels and lower voltage levels resulting in more reliable circuit operation. Further, such electronic
20 disabling devices may be packaged in a much more physically compact design. In a laboratory prototype embodiment of a stun gun according to various aspects of the present invention, the prototype size in comparison to the size of an M26 stun gun is reduced by approximately 50% and the weight is reduced by approximately 60%.

According to other aspects of the present invention, battery capacity is
25 predicted by the controller. Further, a readout of battery capacity may be provided to the user. In most electronic devices the remaining battery capacity can be predicted either by measuring the battery voltage during operation or integrating the battery discharge current over time. Due to the several modes of operation discussed above, prior art battery management methods yield unreliable results. Since ambient temperature strongly affects
30 battery capacity and operation of electronic disabling devices is desired in a wide range of ambient temperatures, non-temperature compensated prior art battery capacity prediction methods produce even less reliable results.

The battery power consumption of an electronic disabling device (e.g., per FIGs. 21-25) according to various aspects of the present invention varies with operating mode as follows. In one implementation, the device includes a real time clock, a laser, and a flashlight in addition to elements discussed above. The real time clock may draw about 3.5 microamps. If the system safety switch S1 is armed, the now-activated the microprocessor and its clock may draw about 4 milliamps. If enabled, and if the safety switch is armed, the laser target designator may draw about 11 milliamps. If enabled, and if the safety switch is armed, the forward facing low intensity twin white LED flashlight may draw about 63 milliamps. If the safety switch is armed and the trigger switch S2 is pulled, the device will draw from about 3 to about 4 amps. Therefore, the minimum to maximum current drain will vary in a ratio of about 1,000,000 to 1.

To further complicate matters, the capacity of lithium batteries packaged in the system battery module may vary greatly over the operating temperature range. At -20°C, the battery module may deliver about 100 5-second discharge cycles. At +30°C, the battery module may deliver about 350 5-second discharge cycles.

From the warmest to the coldest operating temperature range and from the lowest to the highest battery drain functions, battery life varies from about 5,000,000 to 1.

A battery capacity assessment system according to various aspects of the present invention predicts the remaining battery capacity based on laboratory measurements of critical battery parameters under different loads and at different temperature conditions. These measured battery capacity parameters are stored electronically as a table (e.g., columns 1 and 2 of FIG. 25) in an electronic non-volatile memory device included with each battery module. (FIG. 22) As illustrated in FIGs. 21 and 22, appropriate data interface contacts enable the microprocessor to communicate with the table electronically stored in the battery module 2200 to predict remaining capacity of the battery (2202 and 2204). The battery module 2200 with internal electronic non-volatile memory may be referred to as the Digital Power Magazine (DPM) or simply as the system battery module.

The data required to construct the data tables for the battery module were collected by operating the electronic disabling device at selected temperatures while recording the battery performance and longevity at each temperature interval.

The resulting battery capacity measurements were collected and organized into a tabular spreadsheet of the type illustrated in FIG. 25. The battery drain parameters for each system feature were calculated and translated into standardized drain values in

microamp-hours (μAH) based on the sensible operating condition of that feature. For example, the battery drain required to keep the clock alive is represented by a number in μAH that totals the current required to keep the clock alive for about 24 hours. The battery drain to power up the microprocessor, the forward directed flashlight, and the laser target designator for one second are represented by separate table entries with values in μAH . The battery drain required to operate the gun in the firing mode is represented by numbers in μAH of battery drain required to fire a single power output pulse.

To enable operation at all desired temperatures, while keeping track of battery drain and remaining battery capacity, the total available battery capacity at each incremental temperature was measured. The battery capacity in μAH at 25°C (ambient) was programmed into the table to represent a normalized one hundred percent battery capacity value. The battery table drain numbers at other temperatures were adjusted to coordinate with the 25°C total (one hundred percent) battery capacity number. For example, since the total battery capacity at -20°C was measured to approximate 35% of the battery capacity at 25°C , the μAH numbers at -20°C were multiplied by $1/0.35$.

An additional location in the memory for the table discussed above (not shown in FIG. 25) is used by the microprocessor to keep track of used battery capacity. This number (i.e., used batter capacity) is updated about every one second if the safety selector remains in the "armed" position, and about every twenty-four hours if the safety selector remains in the "safe" position. Remaining battery capacity percentage is calculated by dividing this number by the total battery capacity. The device displays this percent of battery capacity remaining on a two digit Central Information Display (CID) for two seconds each time the device is armed.

In the discussion that follows, device 2300 is referred to as the model X26.

Figure 22 illustrates the electronic circuit located inside the X26 battery module. As illustrated in the FIG. 22 schematic diagram, the removable battery module consists of two series-connected, 3-volt CR123 lithium batteries and a nonvolatile memory device. The nonvolatile memory device may take the form of a 24AA128 flash memory which contains 128K bits of data storage. As shown in FIGs. 21 and 22, the electrical and data interface between the X26 system microprocessor and battery module is established by a 6-pin jack JP1 and provides a 2-line I^2C serial bus for data transmission purposes.

While the battery capacity monitoring apparatus and methodology has been described in connection with monitoring the remaining capacity of a battery energized power

supply for a stun gun, this inventive feature could readily be applied to any battery powered electronic device which includes a microprocessor, such as cell phones, video camcorders, laptop computers, digital cameras, and PDA's. Each of these categories of electronic devices frequently shift among various different operating modes where each operating mode

5 consumes a different level of battery power. For example, a cell phone selectively operates in the following different power consumption modes: (1) power off/microprocessor clock on; (2) power on, standby/receive mode; (3) receiving an incoming telephone call and amplifying the received audio input signal; (4) transmit mode generating an RF power output of about 600 milliwatts; (5) ring signal activated in response to an incoming call; and (6) backlight on.

10 To implement the present invention in a cell phone embodiment, a battery module analogous to that illustrated in the FIG. 22 electrical schematic diagram would be provided. That module would include a memory storage device such as the element designated by reference number U1 in the FIG. 22 schematic diagram to receive and store a battery consumption table of the type discussed above with reference to FIG. 25. The cell

15 phone microprocessor can then be programmed to read out and display either at power up or in response to a user-selectable request the battery capacity remaining within the battery module or the percent of used capacity.

Similar analysis and benefits apply to the application of the battery capacity monitor of the present invention to other applications such as a laptop computer which

20 selectively switches between the following different battery power consumption modes: (1) CPU on, but operating in a standby power conservation mode; (2) CPU operating in a normal mode with the hard drive in the "on" configuration; (3) CPU operating in a normal mode with the hard drive in the "off" configuration; (4) CPU "on" and LCD screen also in the "on" fully illuminated mode; (5) CPU operating normally with the LCD screen switched into the "off"

25 power conservation configuration; (6) modem on/modem off modes; (7) optical drives such as DVD or CD ROM drives operating in the playback mode; (8) optical drives such as DVD or CD ROM drives operating in the record or write mode; and (9) laptop audio system generating an audible output as opposed to operating without an audio output signal.

In each of the cases addressed above, the battery capacity table would be

30 calibrated for each different power consumption mode based on the power consumption of each individual operating element. Battery capacity would also be quantified for a specified number of different ambient temperature operating ranges.

Tracking the time remaining on the manufacturer's warranty as well as updating and extending the expiration date may be implemented according to various aspects of the present invention. An X26 system embodiment of the present invention is shipped from the factory with an internal battery module (DPM) having sufficient battery capacity to energize the internal clock for much longer than 10 years. The internal clock is set at the factory to Greenwich Mean Time (GMT). The internal X26 system electronic warranty tracker begins to count down the factory preset warranty period or duration beginning with the first trigger pull occurring about 24 hours or more after the X26 system has been packaged for shipment by the factory.

Whenever the battery module is removed from the X26 system and replaced one or more seconds later, the X26 system will implement an initialization procedure. During that procedure, the 2-digit LED Central Information Display (CID), sequentially reads out a series of 2-digit numbers which represent the following data: (1) The first 3 sets of 2-digit numbers represent the warranty expiration date in the format YY/MM/DD; (2) The current date is displayed: YY/MM/DD; (3) The internal temperature in degrees Centigrade is displayed: XX (negative numbers are represented by blinking the number); and (4) The software revision is displayed: XX.

The system warranty can be extended by communication via the Internet or by purchase of a replacement battery module. The X26 system includes a USB data interface module accessory which is physically compatible with the shape of the X26 system receptacle for battery module 12. The USB data module can be inserted within the X26 system battery module receptacle and includes a set of electrical contacts compatible with jack JP1 located inside the X26 system battery module housing. The USB interface module may be electrically connected to a computer USB port which supplies power via jack JP1 to the X26 system. While the USB interface is normally used to download firing data from the X26 system, it can also be used to extend the warranty period or to download new software into the X26 microprocessor system. To update the warranty, the user removes the X26 battery module, inserts the USB module, connects a USB cable to an Internet enabled computer, goes to the www.Taser.com website, follows the download X26 system warranty extension instructions, and pays for the desired extended warranty period by credit card.

Alternatively, the system warranty can also be extended by purchasing from the factory a specially programmed battery module having the software and data required to reprogram the warranty expiration data stored in the X26 microprocessor. The warranty

extension battery module is inserted into the X26 system battery receptacle. If the X26 system warranty period has not yet expired, the data transferred to the X26 microprocessor will extend the current warranty expiration date by the period pre-programmed into the extended warranty battery module. Once the extended warranty expiration date has been
5 stored within the X26 system, the microprocessor will initiate a battery insertion initialization sequence and will then display the new warranty expiration date. Various different warranty extension modules can be provided to either extend the warranty of only a single X26 system or to provide warranty extensions for multiple system as might be required to extend the warranty for X26 systems used by an entire police department. If the warranty extension
10 module contains only one warranty extension, the X26 microprocessor will reset the warranty update data in the module to zero. The module can function either before or after the warranty extension operation as a standard battery module. An X26 system may be programmed to accept one warranty extension, for example a one year extension, each time that the warranty extension module is inserted into the weapon.

15 The warranty configuration/warranty extension feature of the present invention could also readily be adapted for use with any microprocessor-based electronic device or system having a removable battery. For example, as applied to a cell phone having a removable battery module, a circuit similar to that illustrated in the FIG. 22 electrical schematic diagram could be provided in the cell phone battery module to interface with the
20 cellular phone microprocessor system. As was the case with the X26 system of the present invention, the cell phone would be originally programmed at the factory to reflect a device warranty of predetermined duration at the initial time that the cell phone was powered up by the ultimate user/customer. By purchasing a specially configured cell phone replacement battery including data suitable for reprogramming the warranty expiration date within the cell
25 phone microprocessor, a customer could readily replace the cell phone battery while simultaneously updating the system warranty.

Alternatively, a purchaser of an electronic device incorporating the warranty extension feature of the present invention could return to a retail outlet, such as Best Buy or Circuit City, purchase a warranty extension and have the on-board system warranty extended
30 by a representative at that retail vendor. This warranty extension could be implemented by temporarily inserting a master battery module incorporating a specified number of warranty extensions purchased by the retail vendor from the OEM manufacturer. Alternatively, the retail vendor could attach a USB interface module to the customer's cell phone and either

provide a warranty extension directly from the vendor's computer system or by means of data supplied by the OEM manufacturer's website.

For electronic devices utilizing rechargeable battery power supplies such as is the case with cell phones and video camcorders, battery depletion occurs less frequently than with the system described above which typically utilizes non-rechargeable battery modules. For such rechargeable battery applications, the end user/customer could purchase a replacement rechargeable battery module including warranty update data and could simultaneously trade in the customer's original rechargeable battery.

For an even broader application of the warranty extension feature of the present invention, that feature could be provided to extend the warranty of other devices such as desktop computer systems, computer monitors or even an automobile. For such applications, either the OEM manufacturer or a retail vendor could supply to the customer's desktop computer, monitor or automobile with appropriate warranty extension data in exchange for an appropriate fee. Such data could be provided to the warranted product via direct interface with the customer's product by means of an infrared data communication port, by a hard-wired USB data link, by an IEEE 1394 data interface port, by a wireless protocol such as Bluetooth or by any other means of exchanging warranty extension data between a product and a source of warranty extension data.

Another benefit of providing an "intelligent" battery module is that the X26 system can be supplied with firmware updates by the battery module. When a battery module with new firmware is inserted into the X26 system, the X26 system microcontroller will read several identification bytes of data from the battery module. After reading the software configuration and hardware compatibility table bytes of the new program stored in the nonvolatile memory within the battery module to evaluate hardware/software compatibility and software version number, a system software update will take place when appropriate. The system firmware update process is implemented by having the microprocessor (see FIG. 21) in the X26 system read the bytes in the battery module memory program section and programming the appropriate software into the X26 system nonvolatile program memory.

The X26 system can also receive program updates through a USB interface module by connecting the USB module to a computer to download the new program to a nonvolatile memory provided within the USB module. The USB module is next inserted into the X26 system battery receptacle. The X26 system will recognize the USB module as

providing a USB reprogramming function and will implement the same sequence as described above in connection with X26 system reprogramming via battery module.

The High Voltage Assembly (HVA) schematically illustrated in FIGS. 23 and 24 provides an output of about 50,000 from an input of from about 3 to about 6 volts. To provide maximum safety, to avoid false triggering, and to minimize the risk that the X26 system could activate or stay activated if the microprocessor malfunctions or locks up, the ENABLE signal from the microprocessor (FIG. 22) to the HVA (FIGs. 23A and 23B(or 24)) has been specially encoded.

To enable the HVA, the microprocessor must output a 500 Hz square wave with an amplitude of from about 2.5 to about 6 volts and a duty cycle of about 50%. The D6 series diode within the HVA power supply "rectifies" the ENABLE signal and uses it to charge up capacitor C6. The voltage across capacitor C6 is used to run pulse width modulation (PWM) controller U1 in the HVA.

If the ENABLE signal goes low for more than about one millisecond, several functions operate to turn the PWM controller off. The voltage across capacitor C6 will drop to a level where the PWM can no longer run causing the HVA to turn off. The input to the U1 "RUN" pin must be above a threshold level. The voltage level at that point represents a time average of the ENABLE waveform (due to R1 and C7). If the ENABLE signal goes low, capacitor C7 will discharge and disable the controller after about 1 millisecond.

As the ENABLE signal goes high, resistor R3 charges capacitor C8. If the charge level on C8 goes above about 1.23 volts, the PWM will shut down -- stopping delivery of 50,000 volt output pulses. Every time the ENABLE signal goes low, capacitor C8 is discharged, making sure the PWM can stay "on" as the ENABLE signal goes back high and starts charging C8 again. Any time the ENABLE signal remains high for more than about 1 millisecond, the PWM controller will be shut down.

The encoded ENABLE signal requirements dictate that the ENABLE signal must be pulsed at a frequency of about 500 Hz to activate the HVA. If the ENABLE signal sticks at a high or low level, the PWM controller will shut down, stopping the delivery of the 50,000 volt output pulses.

The configuration of the X26 system high voltage output circuit represents a key distinction between the X26 system and conventional prior art stun guns. Referring now to FIGS. 23A and B, the structure and function of the X26 system high voltage "shaped pulse" assembly will be explained. The switch mode power supply will charge up capacitors

C1, C2, and C3 through diodes D1, D2, and D3. Note that diodes D1 and D2 can be connected to the same or to different windings of 2301 (T1) to modify the output waveform. The ratios of the T1 primary and secondary windings and the spark gap voltages on GAP1, GAP2, and GAP3 are configured so that GAP1 will always breakdown and fire first. When
5 GAP1 fires, 2,000 volts is applied across the primary windings of spark coil transformer 2305 (T2) from pin 6 to pin 5. The secondary voltage on spark coil transformer T2 from pins 1 to 2 and from pins 3 to 4 will approximate 25,000 volts, depending on the air gap spacing between the two output electrodes E1 and E2. The smaller the air gap, the smaller the output voltage before the air gap across output terminals E1 to E2 breaks down, effectively clamping
10 the output voltage level.

The voltage induced in the secondary current path by the discharge of C1 through GAP1 and T2 sets up a voltage across C2, GAP2, E1 to E2, GAP3, C3 and C1. When the cumulative voltage across the air gaps (GAP2, E1 to E2, and GAP3) is high enough to cause them to break down, current will start flowing in the circuit, from C2 through GAP2,
15 through the output electrodes E1 to E2, through GAP3, and through C3 in series with C1 back to ground. As long as C1 is driving the output current through GAP1 and T2, the output current as described will remain negative in polarity. As a result, the charge level stored in both C2 and C3 will increase. Once C1 has become somewhat discharged, T1 will not be able to maintain the output voltage across the output windings (from pin 1 to pin 2, and from
20 pin 3 to pin 4). At that time, the output current will reverse and begin flowing in a positive direction and will begin depleting the charge on C2 and C3. The discharge of C1 is known as the "arc" phase. The discharge of C2 and C3 is known as the muscle "stimulation" phase.

Since the high voltage output coil T2 as illustrated in FIG. 24 consists of two separate secondary windings that create a negative polarity spark voltage on E1 followed by a
25 positive polarity spark voltage on E2, the peak voltage measured from either electrode E1 or E2 to primary weapon ground will not exceed about 25,000 volts yet the peak voltage measured across power supply output terminals E1 and E2 will reach about 50,000 volts. If the output coil T2 had utilized only a single secondary winding as is the case with all prior art stun guns and in other embodiments of the present invention, the maximum voltage from one
30 output electrode (E1 or E2) referenced to primary weapon ground would reach about 50,000 volts. Since a 25,000 volt output can establish an arc across a gap less than half the size of a gap that can establish an arc with a 50,000 volt, reducing the peak output terminal to ground voltage by 50% from about 50,000 volts to about 25,000 volts reduces by more than a 2 to 1

ratio the risk that the user of this version of the X26 system will be shocked by the high voltage output pulses. This represents a significant safety enhancement for a handheld stun gun weapon.

Referring now to the FIG. 23 and 24 schematic diagrams, a feedback signal from the primary side of the HVA (at T1) provides a mechanism for the FIG. 21 microprocessor to indirectly determine the voltage on capacitor C1, and hence where the X26 system power supply is operating within its pulse firing sequence. This feedback signal is used by the microprocessor to control the output pulse repetition rate.

The system pulse rate may be controlled to create either a constant or a time-varying pulse rate by having the microcontroller stop toggling the ENABLE signal for short time periods, thereby holding back the pulse rate to reach a preset, lower value. The preset values may be changed based on the length of the pulse train. For example, in a police model, the system may be preprogrammed such that a single trigger pull will produce a 5-second long power supply activation period. For the first 2 seconds of that 5-second period, the microprocessor may be programmed to control (pull back) the pulse rate to about 19 pulses per second (PPS), while for the last 3 seconds of the 5-second period, the pulse rate could be programmed to be reduced to about 15 PPS. If the operator continues to hold the trigger down, after the 5-second period has lapsed, the X26 system may be programmed to continue discharging at 15 PPS for as long as the trigger is held down. The X26 system could alternatively be programmed to produce various different pulse repetition rate configurations such as, for example:

0-2 seconds	:	17 PPS,
2-5 seconds	:	12 PPS,
5-6 seconds	:	0.1 PPS,
6-12 seconds	:	11 PPS,
12-13 seconds	:	0.1 PPS,
13-18 seconds	:	10 PPS,
18-19 seconds	:	0.1 PPS,
18-23 seconds	:	9 PPS.

Such alternative pulse repetition rate configurations could be applied to a civilian version of the X26 system where longer activation periods are desirable. In addition, lowering the pulse rate will reduce battery power consumption, extend battery life, and potentially enhance the medical safety factor.

To explain the operation of the X26 system illustrated in FIGS. 21-24 in more detail, the operating cycle of the HVA can be divided into the following 4 time periods as illustrated in FIG. 26. In a first period from T0 to T1, capacitors C1, C2 and C3 are charged by one, two, or three power supplies to the breakdown voltage of spark gap GAP1. In a second period from T1 to T2, GAP1 has switched ON, allowing C1 to pass a current through the primary winding of the high voltage spark transformer T2 which causes the secondary voltage (across E1 to E2) to increase rapidly. At a certain point, the high output voltage caused by the discharge of C1 through the primary transformer winding will cause voltage breakdown across GAP2, across E1 to E2, and across GAP3. This voltage breakdown completes the secondary circuit current path, allowing output current to flow. During the T1 to T2 time interval, capacitor C1 is still passing current through the primary winding of the spark transformer T2. As C1 is discharging, it drives a charging current into both C2 and C3. In a third period from T2 to T3, capacitor C1 is now mostly discharged. The load current is being supplied by C2 and C3. The magnitude of the output current during the T2 to T3 time interval will be much lower than the much higher output current produced by the discharge of C1 through spark transformer T2 during the initial T1 to T2 current output time interval. The duration of this significantly reduced magnitude output current during time interval T2 to T3 may readily be tuned by appropriate component parameter adjustments to achieve the desired muscle response from the target subject. During the time period T0 through T3, the microprocessor measured the time required to generate a single shaped waveform output pulse. The desired pulse repetition rate was pre-programmed into the microprocessor. During the T3 to T4 time interval, the microprocessor will temporarily shut down the power supply for a period required to achieve the preset pulse repetition rate. Because the microprocessor is inserting a variable length T3 to T4 shut-off period, the system pulse repetition rate will remain constant independent of battery voltage and circuit component variations (tolerance). The microprocessor-controlled pulse rate methodology allows the pulse rate to be software controlled to meet different customer requirements.

The FIG. 10 timing diagram shows an initial fixed timing cycle TA followed by a subsequent, longer duration timing cycle TB. The shorter timing cycle followed by the longer timing cycle reflects a reduction in the pulse rate. Hence, it is understood that the X26 system can vary the pulse rate digitally during a fixed duration operating cycle. As an example, a pulse rate of about 19 PPS may be achieved for about 2 seconds of initial

operation and then reduced to about 15 PPS for about 3 seconds, further reduced to about 0.1 PPS for about 1 second, and then increased to about 14 PPS for about 5 seconds.

The implementations illustrated in FIGs. 23A and 23B utilizes three spark gaps. Only GAP1 requires a precise breakdown voltage rating, in this case about 2,000 volts. GAP2 and GAP3 only require a breakdown voltage rating significantly higher than the voltage stress induced on them during the time interval before GAP1 breaks down. GAP2 and GAP3 have been provided solely to ensure that if a significant target skin resistance is encountered during the initial current discharge into the target that the muscle activation capacitors C2 and C3 will not discharge before GAP1 breaks down. To perform this optional, enhanced function, only one of these secondary spark gaps (either GAP2 or GAP3) need be provided.

FIG. 24 illustrates a high voltage section with significantly improved efficiency. Instead of rectifying the T1 high voltage transformer outputs through diodes directly to very high voltages, as is the case with the FIG. 23B circuit, transformer T1 has been reconfigured to provide 3 series-connected secondary windings where the design output voltage of each winding has been limited to about 1,000 volts.

In the FIG. 23B circuit, capacitor C1 is charged to about 2,000 volts by transformer winding and diode D1. In the FIG. 24 circuit, C1 is charged by combining the voltages across C5 and C6. Each T1 transformer winding coupled to charge C5 and C6 is designed to charge each capacitor to about 1,000 volts, rather than to 2,000 volts as in the FIG. 23B circuit.

Since the losses due to parasitic circuit capacitances are a function of the transformer AC output voltage squared, the losses due to parasitic circuit capacitances with the FIG. 24 1,000 volt output voltage compared to the FIG. 23B 2,000 volt transformer output voltage are reduced by a factor of 4. Furthermore, in the FIG. 24 embodiment, the current required to charge C2 is derived in part from capacitor C6, the positive side of which is charged to about 2,000 volts. Hence, to charge C2 to about 3,000 volts, the voltage across the transformer winding is reduced to about 1,000 volts in comparison to the 3,000 volts produced across the corresponding transformer T1 winding in the FIG. 23B circuit.

Another benefit of the novel FIG. 23B and FIG. 24 circuit designs relates to the interaction of C1 to C3. Just before GAP1 breaks down, the charge on C1 is about 2,000 volts while the charge on C3 is about 3,000 volts. After C1 has discharged and the output current is being supported by C2 and C3, the voltage across C3 remains at about 3,000 volts.

However, since the positive side of C3 is now at ground level, the negative terminal of C3 will be at about -3,000 volts. Hence a differential voltage of about 6,000 volts has been created between the positive terminal of C2 and the negative terminal of C3. During the time interval when C2 and C3 discharge after C1 has been discharged, the T2 output windings
5 merely act as conductors.

The X26 system trigger position is read by the microprocessor which may be programmed to extend the duration of the operating cycle in response to additional trigger pulls. Each time the trigger is pulled, the microprocessor senses that event and activates a fixed time period operating cycle. After the gun has been activated, the Central Information
10 Display (CID) on the back of the X26 handle indicates how much longer the X26 system will remain activated. The X26 system activation period may be preset to yield a fixed operating time, for example, about 5 seconds. Alternatively, the activation period may be programmed to be extended in increments in response to additional, sequential trigger pulls. Each time the trigger is pulled, the CID readout will update the countdown timer to the new, longer timeout.
15 The incrementing trigger feature will allow a civilian who uses the X26 system on an aggressive attacker to initiate multiple trigger pulls to activate the gun for a prolonged period, enabling the user to lay the gun down on the ground and get away.

To protect police officers against allegations of stun gun misuse, the X26 system may provide an internal non-volatile memory set aside for logging the time, duration
20 of discharge, internal temperature and battery level each time the weapon is fired.

The stun gun clock time always remains set to GMT. When downloading system data to a computer using the USB interface module, a translation from GMT to local time may be provided. On the displayed data log, both GMT and local time may be shown. Whenever the system clock is reset or reprogrammed, a separate entry may be made in the
25 system log to record such changes.

It will be apparent to those skilled in the art that the disclosed electronic disabling device may be modified in numerous ways and may assume many embodiments other than the preferred forms specifically set out and described above. Accordingly, it is intended by the appended claims to cover all such modifications of the invention which fall
30 within the true spirit and scope of the invention.

CLAIMS

What is claimed is:

1. An electronic disabling device for disabling a target comprising:
 - a. first and second electrodes to establish first and second spaced apart contact points
5 on the target wherein a high impedance air gap may exist between at least one of the electrodes and the target; and
 - b. a power supply for operating in a first mode to generate a first high voltage, short duration output across the first and second electrodes during a first time interval to ionize the air within the air gap to reduce the high impedance across the air gap to a lower impedance to
10 enable current flow across the air gap at a lower voltage level and for subsequently operating in a second mode to generate a second lower voltage output across the first and second electrodes during a second time interval to maintain the current flow across the first and second electrodes and between the first and second contact points on the target to enable the current flow through the target.
- 15 2. An electronic disabling device for disabling a target comprising:
 - a. first and second electrodes to establish first and second spaced apart contact points on the target wherein a high impedance air gap may exist between at least one of the electrodes and the target;
 - b. a high voltage power supply for generating an output voltage; and
 - 20 c. a high voltage power output circuit which generates a first high voltage output across the first and second electrodes to ionize the air within the air gap reducing the high impedance across the air gap a lower impedance to enable current flow across the air gap at a lower voltage level and for subsequently enabling a second lower voltage output to cause current to flow across the first and second electrodes and between the first and second contact
25 points on the target allowing current flow through the target.
3. An electronic disabling device for disabling a target comprising:
 - a. first and second electrodes to establish first and second spaced apart contact points on the target wherein a high impedance air gap may exist between at least one of the electrodes and the target;
 - 30 b. a high voltage power supply for generating an output voltage; and
 - c. a switchable output circuit for the high voltage power supply for switching into and operating in a first output circuit configuration to generate a first high voltage output across the first and second electrodes during a first time interval to ionize the air within the air gap

- and reduce the high impedance across the air gap to a lower impedance to enable current flow across the air gap at a lower voltage level and for subsequently switching into and operating in a second output circuit configuration to generate a second lower voltage output across the first and second electrodes during a second time interval to maintain the current flow across the first and second electrodes and between the first and second contact points on the target allowing current flow through the target.
- 5 4. The electronic disabling device of Claim 3 wherein the switchable output circuit includes:
- a. a high voltage output circuit for generating a relatively high voltage output across the first and second electrodes during the first time interval; and
 - 10 b. a low voltage output circuit for generating a relatively low voltage output across the first and second electrodes during the second time interval.
5. The electronic disabling device of Claim 4 wherein the high voltage output circuit includes:
- a. a first energy storage capacitor;
 - 15 b. a voltage conversion circuit coupled between the first energy storage capacitor and the first electrode for increasing the energy storage capacitor voltage from a first voltage level to a higher second voltage level; and
 - c. a first switch for closing to couple the high voltage output circuit across the first and second electrodes after the voltage on the first energy storage capacitor reaches a first predetermined level.
 - 20
6. The electronic disabling device of Claim 5 wherein the low voltage output circuit includes:
- a. a second energy storage capacitor; and
 - b. a second switch for closing to couple the second energy storage capacitor across
 - 25 the first and second electrodes at about the time that the first high voltage output has ionized the air in the air gap.
7. The electronic disabling device of Claim 6 wherein the first energy storage capacitor and the second energy storage capacitor each receive a charging current from the high voltage power supply.
- 30 8. The electronic disabling device of Claim 7 wherein the first switch opens to decouple the high voltage output circuit from the first and second electrodes after the second switch closes.
9. The electronic disabling device of Claim 8 wherein closure of the first switch defines a time T_1 .

10. The electronic disabling device of Claim 9 wherein closure of the second switch defines a time T_2 .
11. The electronic disabling device of Claim 8 wherein the second switch is configured to open when the second energy storage capacitor voltage falls below a predetermined level and defines a time T_3 .

12. The electronic disabling device of Claim 10 wherein the relationship between the open and closed states of the first and second switches is defined by the following table:

Time Interval	First Switch	Second Switch
$T_1 - T_2$	Closed	Open
$T_2 - T_3$	Open or Closed	Closed

13. The electronic disabling device of Claim 12 wherein the first and second switches include voltage activated switches.
14. The electronic disabling device of Claim 13 wherein the first and second voltage activated switches include spark gaps and wherein the breakdown voltage of the first spark gap is less than the breakdown voltage of the second spark gap.
15. The electronic disabling device of Claim 14 wherein the capacitance rating of the first energy storage capacitor is substantially greater than the capacitance rating of the second energy storage capacitor.
16. The electronic disabling device of Claim 12 further including:
- a trigger switch for activating and deactivating the electronic disabling device; and
 - a controller for sensing the configuration of the trigger switch and for controlling the operation of the high voltage power supply.
17. The electronic disabling device of Claim 16 wherein closure of the trigger switch defines a time T_0 and causes the controller to activate the voltage conversion stage of the high voltage power supply.
18. The electronic disabling device of Claim 17 wherein second the controller deactivates the voltage conversion stage of the high voltage power supply at time T_3 .
19. The electronic disabling device of Claim 18 wherein the controller maintains the deactivated state of the high voltage power supply voltage conversion stage until a time T_4 to maintain a fixed pulse repetition rate corresponding to a preset pulse repetition rate.

20. The electronic disabling device of Claim 19 wherein the controller repeatedly activates and deactivates the high voltage power supply to maintain the fixed pulse repetition rate.
21. The electronic disabling device of Claim 7 wherein voltage conversion circuit comprises a voltage multiplier.
- 5 22. The electronic disabling device of Claim 21 wherein the voltage multiplier includes a step-up transformer.
23. The electronic disabling device of Claim 22 wherein the step-up transformer includes a primary winding and a secondary winding and wherein the primary winding is coupled in series with the discharge path of the first energy storage capacitor.
- 10 24. The electronic disabling device of Claim 23 wherein the step-up transformer secondary winding is coupled in series with the discharge path of the second energy storage capacitor.
25. The electronic disabling device of Claim 7 further including a third energy storage capacitor having a first lead coupled to the output of the high voltage power supply voltage conversion stage and a second lead coupled to the second electrode.
- 15 26. The electronic disabling device of Claim 12 wherein the $T_2 - T_3$ time interval approximates 1.5 microseconds and wherein the $T_2 - T_3$ time interval approximates 50 microseconds.
27. The electronic disabling device of Claim 14 wherein the breakdown voltage of the first spark gap approximates 2000 volts and wherein the breakdown voltage of the second spark
- 20 gap approximates 3000 volts.
28. The electronic disabling device of Claim 7 wherein the capacitance rating of the first energy storage capacitor approximates 0.14 microfarads and wherein the capacitance rating of the second energy storage capacitor is substantially equal to or less than about 0.02 microfarads.
- 25 29. An electronic disabling device for disabling a target comprising:
- a. first and second electrodes to establish first and second spaced apart contact points on the target wherein a high impedance air gap may exist between at least one of the electrodes and the target;
- b. a high voltage power supply having a voltage conversion stage for receiving a low
- 30 voltage DC input and for generating at an output terminal a substantially increased DC output voltage;

c. a high voltage output circuit coupled to the voltage conversion stage output terminal for generating a high voltage output across the first and second electrodes during a time interval $T_1 - T_2$; and

5 d. a low voltage output circuit coupled to the voltage conversion stage output terminal for generating a lower voltage output across the first and second electrodes during a time interval $T_2 - T_3$.

30. The electronic disabling device of Claim 29 wherein:

a. the high voltage output circuit includes a first energy storage capacitor coupled to the output terminal of the high voltage power supply voltage conversion stage for receiving a
10 charging current from the high voltage power supply during a time interval $T_0 - T_1$; and

b. the low voltage output circuit includes a second energy storage capacitor coupled in parallel with the output terminal of the high voltage power supply voltage conversion stage for receiving the charging current from the high voltage power supply during the time interval $T_0 - T_1$.

15 31. The electronic disabling device of Claim 30 wherein the high voltage output stage further includes:

a. a voltage multiplier coupled between the first energy storage capacitor and the first electrode for increasing the energy storage capacitor voltage to a high voltage level; and

20 b. a first switch for closing to couple the high voltage output circuit across the first and second electrodes when the voltage on the first energy storage capacitor reaches a first predetermined level.

32. The electronic disabling device of Claim 31 wherein the low voltage output circuit further includes a second switch for closing to couple the second energy storage capacitor across the first and second electrodes after the voltage applied by the high voltage output
25 circuit across the first and second electrodes establishes an arc allowing current to flow at a lower voltage.

33. The electronic disabling device of Claim 32 wherein the first switch opens to decouple the high voltage output circuit from the first and second electrodes when the second switch closes.

30 34. The electronic disabling device of Claim 33 wherein the first and second switches include spark gap switches.

35. The electronic disabling device of Claim 34 wherein the breakdown voltage of the first spark gap switch is less than the breakdown voltage of the second spark gap switch.

36. The electronic disabling device of Claim 35 wherein the capacitance rating of the first energy storage capacitor is substantially greater than the capacitance rating of the second energy storage capacitor.
37. The electronic disabling device of Claim 32 further including:
- 5 a. a trigger switch for activating and deactivating the electronic disabling device; and
- b. a controller for sensing the configuration of the trigger switch and for controlling the operation of the high voltage power supply.
38. The electronic disabling device of Claim 37 wherein the voltage multiplier comprises a step-up transformer.
- 10 39. A method for disabling a target comprising:
- a. directing a charging current to first and second energy storage capacitors during a first time interval;
- b. sensing the voltage on the first energy storage capacitor and coupling the first energy storage capacitor to a voltage multiplier when the first energy storage capacitor
- 15 voltage exceeds a first voltage threshold;
- c. discharging the first energy storage capacitor through the voltage multiplier during a second time interval to generate a multiplied output voltage across first and second output electrodes while positioning the output electrodes in proximity to the target to establish first and second spaced apart intended contact points on the target wherein a high impedance air
- 20 gap may exist between at least one of the electrodes and the target;
- d. establishing a current flow between the first and second electrodes to create a reduced impedance ionized pathway across the air gap to reduce the high impedance previously existing across the air gap to a substantially lower impedance; and
- e. sensing the voltage applied across the first and second electrodes as the first energy
- 25 storage capacitor is discharging and coupling the second energy storage capacitor across the first and second electrodes to discharge current through the reduced impedance ionized pathway established across the air gap to maintain the current flow between the first and second electrodes during a third time interval.
40. The method of Claim 39 wherein the first and second energy storage capacitors are
- 30 charged to substantially equal voltage levels during the first time interval.
41. The method of Claim 40 wherein the capacitance rating of the first energy storage capacitor substantially exceeds the capacitance rating of the second energy storage capacitor.

42. The method of Claim 39 wherein the voltage multiplier includes a step-up transformer having primary and secondary windings and wherein the discharge current from the first energy storage capacitor passes through the primary transformer winding.
43. The method of Claim 39 wherein the multiplied output voltage generated during the
5 second time interval substantially exceeds the first voltage level.
44. The method of Claim 39 wherein the duration of the second time interval is substantially shorter than the duration of the third time interval.
45. The method of Claim 44 wherein sensing the voltage on the first energy storage capacitor is performed by a first spark gap having a first breakdown voltage substantially
10 equal to the first voltage threshold.
46. The method of Claim 45 wherein sensing the voltage applied across the first and second electrodes as the first energy storage capacitor is being discharged is performed by a second spark gap having a second breakdown voltage substantially equal to the second voltage threshold.
47. The method of Claim 39 wherein the target is a remote target further including first and
15 second darts coupled by separate lengths of flexible wire to the first and second output electrodes, the wire length being sufficient to span the distance between the output electrodes and the remote target.
48. The method of Claim 47 further including propelling the darts from a first location in
20 proximity to the output electrodes toward the remote target.
49. The method of Claim 48 wherein the first and second darts include electrically conductive tips coupled to the separate lengths of flexible wire.
50. The method of Claim 39 further including coupleing the second energy storage capacitor across the first and second electrodes when the sensed voltage exceeds the second voltage
25 threshold.
51. An electronic disabling device for disabling a target comprising:
- a. first and second electrodes to establish first and second spaced apart contact points on the target;
 - b. a high voltage power supply that generates an output voltage delivered in pulses to
30 the target; and
 - c. a circuit that controls a time between pulses in response to a feedback signal.
52. An electronic disabling device for disabling a target comprising:

- a. first and second electrodes to establish first and second spaced apart contact points on the target; and
 - b. a high voltage power supply that generates a positive voltage with respect to earth ground at the first electrode and a negative voltage with respect to earth ground at the second electrode.
53. A method for disabling a target comprising:
- sourcing at a first voltage a signal to ionize an air gap at the target; and
 - sourcing at a second voltage less in magnitude than the first voltage the signal to continue current flow through the target.
54. A battery capacity monitor for a device operated by a battery, the monitor comprising:
- a. operating mode monitoring means for monitoring an operating mode of a plurality of modes of the device;
 - b. operating time monitoring means for measuring a time that the device operates in each respective operating mode of the plurality;
 - c. a memory that stores indicia of the original battery capacity and indicia of a rate of battery capacity consumption associated with each operating mode of the plurality; and
 - d. means for computing a battery capacity consumed based on data received from the operating mode monitoring means, from the operating time monitoring means and from the memory.
55. The battery capacity monitoring system of Claim 54 wherein the electronic device includes an electronic disabling device.
56. The battery capacity monitoring system of Claim 55 wherein the electronic disabling device is packaged in a housing having a battery receptacle and wherein the battery is packaged in a removable battery module dimensioned to fit within the electronic disabling device battery receptacle.
57. The battery capacity monitoring system of Claim 56 wherein the lookup table is located in the battery receptacle.
58. The battery capacity monitoring system of Claim 57 wherein the lookup table is stored in a non-volatile memory device.
59. The battery capacity monitoring system of Claim 57 wherein the electronic disabling device and the battery receptacle further include data interface contacts for transferring data between the electronic disabling device and the battery module.

60. The battery capacity monitoring system of Claim 54 wherein the lookup table further includes data representing the rate of battery capacity consumption associated with each device operating mode for two or more ambient temperature levels, wherein the electronic disabling device further includes means for measuring the device operating temperature, and
5 wherein the computing means displays data compensated for temperature deviations.
61. The battery capacity monitoring system of Claim 54 wherein in a first operating mode an electronic clock is energized by the battery.
62. The battery capacity monitoring system of Claim 61 further including a second operating mode wherein the clock and a microprocessor are energized by the battery.
- 10 63. The battery capacity monitoring system of Claim 62 including a third operating mode wherein the clock, the microprocessor and the device itself are energized by the battery.
64. The battery capacity monitoring system of Claim 55 wherein the electronic disabling device includes an electronic clock, a microprocessor and a high voltage power supply and wherein in a first operating mode only the electronic clock is energized by the battery,
15 wherein in a second operating mode the electronic clock and the microprocessor are energized by the battery, and wherein in a third operating mode the clock, the microprocessor and the high voltage power supply are energized by the battery.
65. The battery capacity monitoring system of Claim 64 wherein in a fourth operating mode the clock, the microprocessor and a laser target designator are energized by the battery.
- 20 66. The battery capacity monitoring system of Claim 64 wherein in a fifth operating mode the clock, the microprocessor and a flashlight are energized by the battery.
67. A method for monitoring the battery capacity of a battery powered electronic device having two or more operating modes with a different current level associated with each operating mode comprising:
- 25 a. monitoring the electronic device operating mode;
- b. measuring the time that the electronic device operates in each different operating mode;
- c. storing data representing the original battery capacity and rate of battery capacity consumption associated with each device operating mode; and
- 30 d. computing the battery capacity consumed based on the device operating mode, the time that the electronic device has operated in each different operating mode and the data representing the original battery capacity and the rate of battery capacity consumption

associated with each device operating mode and displaying data indicating either the battery capacity consumed or the battery capacity remaining.

68. The method of Claim 67 wherein the battery powered electronic device includes an electronic disabling device.

5 69. The method of Claim 67 further including storing data representing the rate of battery capacity consumption associated with each device operating mode for two or more ambient temperature levels.

70. The method of Claim 69 further including measuring the device operating temperature and computing the battery capacity consumed based on the appropriate temperature-related
10 stored battery capacity consumption data.

71. A method for disabling the muscles of a target comprising:

a. providing first and second electrodes to establish first and second spaced apart contact points on the target wherein a high impedance air gap may exist between at least one of the electrodes and the target;

15 b. applying a first high voltage, short duration output across the first and second electrodes during a first time interval to ionize the air within the air gap to reduce the high impedance across the air gap to a lower impedance to enable current to flow across the air gap at a lower voltage level;

20 c. subsequently applying a second lower voltage output across the first and second electrodes during a second time interval to maintain the current flow across the first and second electrodes and between the first and second contact points on the target to enable the current flow through the target;

d. providing a battery to supply the power required to generate the first high voltage output and the second lower voltage output; and

25 e. accessing stored data representing the original battery capacity, computing the battery capacity consumed as a function of operating time, and displaying data indicating either the battery capacity consumed or the battery capacity remaining.

72. An electronic disabling device for disabling a target comprising:

30 a. first and second electrodes to establish first and second spaced apart contact points on the target wherein a high impedance air gap may exist between at least one of the electrodes and the target;

b. a power supply for operating in a first mode to generate a first high voltage, short duration output across the first and second electrodes during a first time interval to ionize the

- air within the air gap to reduce the high impedance across the air gap to a lower impedance to enable current flow across the air gap at a lower voltage level and for subsequently operating in a second mode to generate a second lower voltage output across the first and second electrodes during a second time interval to maintain the current flow across the first and second electrodes and between the first and second contact points on the target to enable the current flow through the target;
- 5 c. operating mode monitoring means for monitoring the electronic disabling device operating mode;
- d. operating time monitoring means for measuring the time that the electronic
- 10 disabling device has operated in each different operating mode;
- e. a battery for supplying electrical energy to the electronic disabling device;
- f. a lookup table for storing data representing the original battery capacity and the rate of battery capacity consumption associated with each device operating mode; and
- g. means for computing the battery capacity consumed based on the data received
- 15 from the operating mode monitoring means, from the operating time monitoring means and from the data stored in the lookup table and displaying data indicating either the battery capacity consumed or the battery capacity remaining.
73. The battery capacity monitoring system of Claim 72 wherein the electronic disabling device is packaged in a housing having a battery receptacle and wherein the battery is
- 20 packaged in a removable battery module dimensioned to fit within the electronic disabling device battery receptacle.
74. The battery capacity monitoring system of Claim 73 wherein the lookup table is located in the battery receptacle.
75. The battery capacity monitoring system of Claim 74 wherein the lookup table is stored in
- 25 a non-volatile memory device.
76. The battery capacity monitoring system of Claim 74 wherein the electronic disabling device and the battery receptacle further include data interface contacts for transferring data between the electronic disabling device and the battery module.
77. The battery capacity monitoring system of Claim 72 wherein the lookup table further
- 30 includes data representing the rate of battery capacity consumption associated with each device operating mode for two or more ambient temperature levels, wherein the electronic disabling device further includes means for measuring the device operating temperature, and

wherein the computing means displays battery capacity data compensated for temperature variations.

78. A warranty control system for an electronic device including a microprocessor, comprising

- 5 a. a lookup table for storing data representing the duration of a manufacturer's warranty;
- b. warranty activation means for setting the device warranty expiration date; and
- c. a display for providing a visual readout of the warranty expiration date.

79. The warranty control system of Claim 78 wherein the warranty activation means sets and
10 records the warranty expiration date upon detecting the initial activation of the device.

80. The warranty control system of Claim 79 wherein the warranty activation means sets and records the warranty expiration date upon detecting the initial activation of the device by a purchaser.

81. The warranty control system of Claim 78 wherein the microprocessor includes a calendar
15 date time keeping function and the warranty expiration date is displayed as a calendar-based data readout.

82. The warranty control system of Claim 81 wherein the warranty expiration date is displayed as the month and year of the warranty expiration date.

83. The warranty control system of Claim 82 wherein the display includes two digital display
20 segments for displaying the month and year elements of the warranty expiration date as two digit data elements.

84. The warranty control system of Claim 83 wherein the display sequentially displays the month and year components of the warranty expiration date.

85. The warranty control system of Claim 81 wherein the warranty activation means sets the
25 warranty expiration date by adding the stored warranty duration data to the current calendar date.

86. The warranty control system of Claim 78 wherein the electronic device is energized by a battery housed in a removable warranty extension battery module interconnectable to the electronic device by power and data interface contacts, and wherein the battery module
30 includes a data module having stored warranty expiration data.

87. The warranty control system of Claim 86 wherein the warranty activation means includes the capability of receiving warranty extension data from the warranty extension battery module and resetting the warranty expiration date based on that data.

88. The warranty control system of Claim 78 wherein the electronic device includes a data interface for interconnecting the microprocessor with an external data source and wherein the warranty activation means includes the capability of receiving warranty extension data from the external data source and resetting the warranty extension date based upon the warranty extension data.
89. The warranty control system of Claim 88 wherein the data interface includes a USB port.
90. The warranty control system of Claim 88 wherein the data interface includes a wireless data interface.
91. The warranty control system of Claim 88 wherein the data interface includes means for establishing an Internet connection.
92. A replaceable battery module configured to supply electrical energy through a power input connector to an electronic device having at least first and second operating modes, where each operating mode consumes battery capacity at a different rate and the electronic device includes the capability of monitoring the operating time corresponding to each device operating mode, the battery module comprising:
- a. a chamber within the battery module for holding at least one battery having positive and negative output terminals;
 - b. a power output connector for interfacing with the electronic device power input connector when the battery module is attached to the electronic device to transfer power from the battery output terminals to the electronic device;
 - c. a lookup table for storing data representing the original battery capacity and the rate of battery power consumption associated with each different device operating mode; and
 - d. a data transfer system for transferring the data stored in the battery module lookup table to the electronic device to enable the electronic device to compute the battery capacity based on the operating time corresponding to each device operating mode and the data stored in the battery module lookup table.
93. The battery module of Claim 92 wherein the lookup table stores data representing the original battery capacity and the rate of battery capacity consumption associated with each device operating mode for at least two different device operating temperatures to enable the electronic device to compute temperature-compensated battery capacity consumption data.
94. The battery module of Claim 93 wherein the lookup table stores battery capacity and power consumption data corresponding to multiple different device operating temperatures.

95. The battery module of Claim 92 wherein the electronic device includes a housing having a battery module receptacle dimensioned to receive the battery module.
96. The battery module of Claim 95 wherein the housing battery module receptacle is configured to mechanically retain or to selectively release the battery module.
- 5 97. The battery module of Claim 96 wherein the housing battery module receptacle comprises an internal receptacle.
98. The battery module of Claim 97 wherein the housing includes a handgrip section and the internal battery module receptacle is positioned within the hand grip section.
99. The battery module of Claim 92 further including a battery module data interface for
10 interfacing with an electronic device data interface for enabling the electronic device to access the data stored in the battery module lookup table.
100. The battery module of Claim 92 further including a second battery coupled to the first battery.
101. The battery module of Claim 100 wherein the first battery is coupled in series with the
15 second battery.
102. The battery module of Claim 92 wherein the lookup table also stores warranty expiration data to enable the electronic device to compute and display a computed, device specific warranty expiration date.
103. An electronic disabling device having a first high voltage transformer for creating an
20 arc and a second transformer with a lower output voltage to maintain current flow across the arc to disable a subject.
104. An electronic disabling device for disabling a target comprising:
- a. first and second electrodes to establish first and second spaced apart contact points on the target;
 - 25 b. high voltage power supply for generating an output voltage delivered in a series of electrical pulses to the target;
 - c. a battery system including
 - i. a battery;
 - ii. a digital memory device for storing information related to the amount of
30 battery power consumed or remaining;
 - iii. a data interface for communicating between the battery system and the device to adjust the amount of power consumed recorded in the battery system; and
 - d. a display for indicating to the user the battery capacity status.

105. An electronic disabling device for disabling a target comprising:
- a. first and second electrodes to establish first and second spaced apart contact points on the target;
 - b. a high voltage power supply for generating an output voltage delivered in a pre-timed series of electrical pulses to the target; and
 - c. a display for indicating to the user the amount of time remaining in each pulse sequence.
106. An electronic disabling device for disabling a target comprising:
- a. first and second electrodes to establish first and second spaced apart contact points on the target;
 - b. a high voltage power supply for generating an output voltage delivered in a pre-timed series of electrical pulses to the target;
 - c. a trigger mechanism to initiate the pre-timed series of electrical pulses; and
 - d. a mechanism for allowing the user to extend the duration of the pre-timed series of electrical pulses.
107. An electronic disabling device for disabling a target comprising:
- a. first and second electrodes to establish first and second spaced apart contact points on the target; and
 - b. a high voltage power supply for generating an output voltage delivered across the first and second contact points on the target to generate a positive voltage potential at one electrode and a negative voltage potential at the other electrode.
108. A method for disabling a target comprising:
- providing from a first stored energy device a first signal to the target to ionize an air gap at the target; and
 - providing from a second stored energy device a second signal to the target to continue a current through the gap and through the target.
109. A device for disabling a target comprising:
- means for providing from a first stored energy device a first signal to the target to ionize an air gap at the target; and
 - means for providing from a second stored energy device a second signal to the target to continue a current through the gap and through the target.
110. A method for monitoring battery capacity for a device operated by a battery, the method comprising:

- a. monitoring an operating mode of a plurality of modes of the device;
 - b. measuring a time that the device operates in each respective operating mode of the plurality;
 - c. storing indicia of the original battery capacity and indicia of a rate of battery capacity consumption associated with each operating mode of the plurality; and
 - d. computing a battery capacity consumed based on data received from the operating mode monitoring means, from the operating time monitoring means and from the memory.
111. A warranty information system for a device, the system comprising:
- a. means for storing indicia of a duration of a warranty;
 - b. means for storing a start time for the warranty; and
 - c. means for providing power for operating the device.
112. The system of claim 111 wherein the warranty information system is provided as an operator replaceable part of the device, a replacement system facilitating an extended warranty.
113. A method for providing warranty information to a processor of a device covered by the warranty, the method comprising:
- a. storing indicia of a duration of a warranty;
 - b. storing a start time for the warranty; and
 - c. providing power for operating the device.
114. The method of claim 113 wherein the warranty information system is provided as an operator replaceable part of the device, and the method further includes providing a replacement module that performs storing of the indicia, storing of the start time, and providing power, thereby facilitating an extended warranty.