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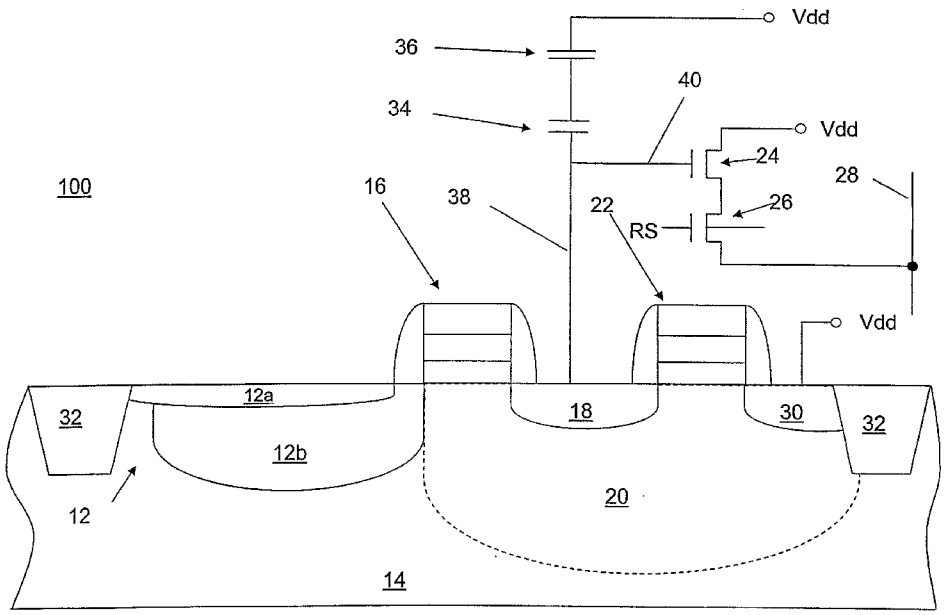
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(54) Title: AN IMAGER PHOTO DIODE CAPACITOR STRUCTURE WITH REDUCED PROCESS VARIATION SENSITIVITY



(57) Abstract: A pixel cell having two capacitors connected in series where each capacitor has a capacitance approximating that of the periphery capacitors and such that the effective capacitance of the series capacitors is smaller than that of each of the periphery capacitors. The series-connected capacitors are coupled to the floating diffusion (FD) region for receiving "surplus" charge from the FD region during saturation conditions.

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