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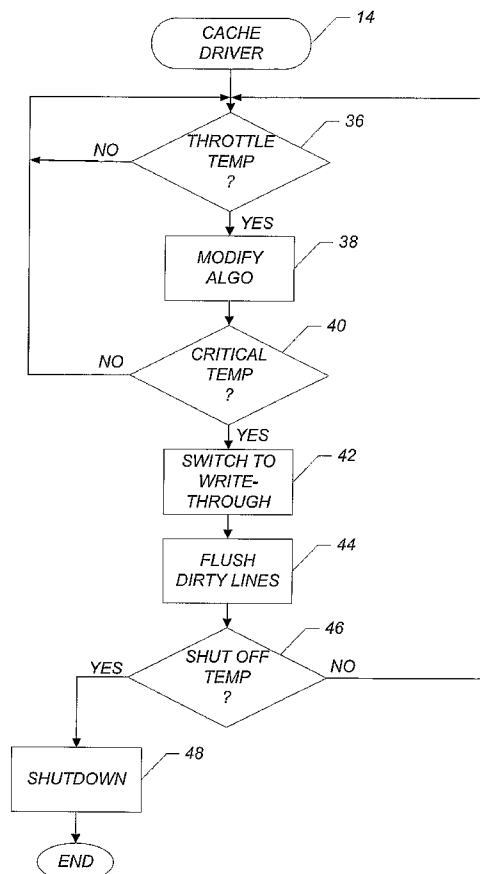
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(54) Title: ADAPTIVE CACHE ALGORITHM FOR TEMPERATURE SENSITIVE MEMORY



(57) Abstract: A temperature sensitive memory, such as a ferroelectric polymer memory, may be utilized as a disk cache memory in one embodiment. If the temperature begins to threaten shutdown, the memory may be transitioned from a write-back to a write-through cache memory. In such case, the system is ready for shutdown without the loss of critical data.

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