



199908156-7

A SUBSCRIBER UNIT AND METHOD FOR USE IN A WIRELESS COMMUNICATION SYSTEM

BACKGROUND OF THE INVENTION

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I. Field of the Invention

The present invention relates to a subscriber unit and method for use in a wireless communication system.

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II. Description of the Related Art

Wireless communication systems including cellular, satellite and point to point communication systems use a wireless link comprised of a modulated radio frequency (RF) signal to transmit data between two systems. The use of a wireless link is desirable for a variety of reasons including increased mobility and reduced infrastructure requirements when compared to wire line communication systems. One drawback of using a wireless link is the limited amount of communication capacity that results from the limited amount of available RF bandwidth. This limited communication capacity is in contrast to wire based communication systems where additional capacity can be added by installing additional wire line connections.

Recognizing the limited nature of RF bandwidth, various signal processing techniques have been developed for increasing the efficiency with which wireless communication systems utilize the available RF bandwidth. One widely accepted example of such a bandwidth efficient signal processing technique is the IS-95 over the air interface standard and its derivatives such as IS-95-A and ANSI J-STD-008 (referred to hereafter collectively as the IS-95 standard) promulgated by the telecommunication industry association (TIA) and used primarily within cellular telecommunications systems. The IS-95 standard incorporates code division multiple access (CDMA) signal modulation techniques to conduct multiple communications simultaneously over the same RF bandwidth. When combined with comprehensive power control, conducting multiple communications over the same bandwidth increases the total number of calls and other communications that can be conducted in a wireless communication system by, among other things, increasing the frequency reuse in comparison to other wireless telecommunication technologies. The use of CDMA techniques in a multiple access communication system is

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disclosed in U.S. Patent No. 4,901,307, entitled "SPREAD SPECTRUM COMMUNICATION SYSTEM USING SATELLITE OR TERRESTRIAL REPEATERS", and U.S. Patent No. 5,103,459, entitled "SYSTEM AND METHOD FOR GENERATING SIGNAL WAVEFORMS IN A CDMA CELLULAR TELEPHONE SYSTEM", both of which are assigned to the assignee of the present invention and incorporated by reference herein.

FIG. 1 provides a highly simplified illustration of a cellular telephone system configured in accordance with the use of the IS-95 standard. During operation, a set of subscriber units 10a - d conduct wireless communication by establishing one or more RF interfaces with one or more base stations 12a - d using CDMA modulated RF signals. Each RF interface between a base station 12 and a subscriber unit 10 is comprised of a forward link signal transmitted from the base station 12, and a reverse link signal transmitted from the subscriber unit. Using these RF interfaces, a communication with another user is generally conducted by way of mobile telephone switching office (MTSO) 14 and public switch telephone network (PSTN) 16. The links between base stations 12, MTSO 14 and PSTN 16 are usually formed via wire line connections, although the use of additional RF or microwave links is also known.

In accordance with the IS-95 standard each subscriber unit 10 transmits user data via a single channel, non-coherent, reverse link signal at a maximum data rate of 9.6 or 14.4 kbits/sec depending on which rate set from a set of rate sets is selected. A non-coherent link is one in which phase information is not utilized by the received system. A coherent link is one in which the receiver exploits knowledge of the carrier signals phase during processing. The phase information typically takes the form of a pilot signal, but can also be estimated from the data transmitted. The IS-95 standard calls for a set of sixty four Walsh codes, each comprised of sixty four chips, to be used for the forward link.

The use of a single channel, non-coherent, reverse link signal having a maximum data rate of 9.6 or 14.4 kbits/sec as specified by IS-95 is well suited for a wireless cellular telephone system in which the typical communication involves the transmission of digitized voice or lower rate digital data such as a facsimile. A non-coherent reverse link was selected because, in a system in which up to 80 subscriber units 10 may communicate with a base station 12 for each 1.2288 MHz of bandwidth allocated, providing the necessary pilot data in the transmission from each subscriber unit 10 would substantially increase the degree to which a set of subscriber units 10

interfere with one another. Also, at data rates of 9.6 or 14.4 kbits/sec, the ratio of the transmit power of any pilot data to the user data would be significant, and therefore also increase inter-subscriber unit interference. The use of a single channel reverse link signal was chosen because engaging
5 in only one type of communication at a time is consistent with the use of wireline telephones, the paradigm on which current wireless cellular communications is based. Also, the complexity of processing a single channel is less than that associated with processing multiple channels.

As digital communications progress, the demand for wireless
10 transmission of data for applications such as interactive file browsing and video teleconferencing is anticipated to increase substantially. This increase will transform the way in which wireless communications systems are used, and the conditions under which the associated RF interfaces are conducted. In particular, data will be transmitted at higher maximum rates and with a
15 greater variety of possible rates. Also, more reliable transmission may become necessary as errors in the transmission of data are less tolerable than errors in the transmission of audio information. Additionally, the increased number of data types will create a need to transmit multiple types of data simultaneously. For example, it may be necessary to exchange a data file
20 while maintaining an audio or video interface. Also, as the rate of transmission from a subscriber unit increases the number of subscriber units communicating with a base station 12 per amount of RF bandwidth will decrease, as the higher data transmission rates will cause the data processing capacity of the base station to be reached with fewer subscriber units 10. In
25 some instances, the current IS-95 reverse link may not be ideally suited for all these changes. Therefore, the present invention is related to providing a higher data rate, bandwidth efficient, CDMA interface over which multiple types of communication can be performed.

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SUMMARY OF THE INVENTION

In one aspect the invention provides a subscriber unit or other
transmitter for use in a wireless communication system, the subscriber unit
35 comprising: plural information sources of information data; an encoder for encoding the information data; plural control sources of control data; and a modulator for modulating encoded information data and the control data from one or more of the plural control sources with respective different

modulating codes for transmission on a carrier signal, wherein the modulator is arranged to combine encoded information data from one information source with the encoded control data before the same is output for transmission.

5 In another aspect the invention provides a base station or other receiver for use in a wireless communication system, the base station comprising: a receiver for receiving a carrier signal and removing therefrom encoded information data from plural information sources modulated with
10 respective different modulating codes and control data from plural control sources with the one or more of the control data being modulated with a respective different modulating code and with the encoded information data from one information source being combined with the encoded control data; a demodulator for demodulating the encoded information data and the control data from their respective different modulating codes; and a decoder
15 for decoding the encoded information and control data.

In a further aspect the invention provides a method for transmitting control data, fundamental data, and supplemental data from a first subscriber unit in a set of subscriber units to a base station in communication with the set of subscriber units comprising: a) modulating
20 the supplemental data with a first Walsh code; b) modulating the fundamental data with a second Walsh code; and c) modulating the control data with a third Walsh code, where said first Walsh code is shorter than said second Walsh code, and said second Walsh code is shorter than said third Walsh code.

25 In another aspect the invention provides a method of transmitting data from a subscriber unit for use in a wireless communication system, the method comprising: acquiring information data from plural information sources; encoding the information data; acquiring control data from plural control sources; and modulating the encoded information data and the control data from one or more of the plural control sources with respective
30 different modulating codes for transmission on a carrier signal, wherein the encoded information data from one information source is combined with the encoded control data before the same is output for transmission.

In accordance with one embodiment of the invention, a set of
35 individually gain adjusted subscriber channels are formed via the use of a set of orthogonal subchannel codes having a small number of PN spreading chips per orthogonal waveform period. Data to be transmitted via one of the transmit channels is low code rate error correction encoded and

sequence repeated before being modulated with one of the subchannel codes, gain adjusted, and summed with data modulated using the other subchannel codes. The resulting summed data is modulated using a user long code and a pseudorandom spreading code (PN code) and upconverted
5 for transmission. The use of the short orthogonal codes provides interference suppression while still allowing extensive error correction coding and repetition for time diversity to overcome the Raleigh fading commonly experienced in terrestrial wireless systems. In the exemplary embodiment of the invention provided, the set of sub-channel codes are
10 comprised of four Walsh codes, each orthogonal to the remaining set and four chips in duration. The use of a small number (e.g. four) sub-channels is preferred as it allows shorter orthogonal codes to be used, however, the use of a greater number of channels and therefore longer codes is consistent with the invention. In another embodiment of the invention the length, or
15 number of chips, in each channel code is different to further reduced the peak-to-average transmit power.

In a preferred exemplary embodiment of the invention, pilot data is transmitted via a first one of the transmit channels and power control data transmitted via a second transmit channel. The remaining two transmit
20 channels are used for transmitting non-specified digital data including user data or signaling data, or both. In an exemplary embodiment, one of the two non-specified transmit channels is configured for BPSK modulation and transmission over the quadrature channel.

25 BRIEF DESCRIPTION OF THE DRAWINGS

The features, objects, and advantages of the present invention will become more apparent from the detailed description set forth below of an embodiment of the invention when taken in conjunction with the
30 drawings in which like reference characters identify correspondingly throughout and wherein:

FIG. 1 is a block diagram of cellular telephone system;

FIG. 2 is a block diagram of a subscriber unit and base station configured in accordance with an exemplary embodiment of the invention;

35 FIG. 3 is a block diagram of a BPSK channel encoder and a QPSK channel encoder configured in accordance with the exemplary embodiment of the invention;

FIG. 4 is a block diagram of a transmit signal processing system configured in accordance with the exemplary embodiment of the invention;

FIG. 5 is a block diagram of a receive processing system configured in accordance with the exemplary embodiment of the invention;

FIG. 6 is a block diagram of a finger processing system configured in accordance with one embodiment of the invention;

5 FIG. 7 is a block diagram of a BPSK channel decoder and a QPSK channel decoder configured in accordance with the exemplary embodiment of the invention; and

FIG. 8 is a block diagram of a transmit signal processing system configured in accordance with a second exemplary embodiment of the invention;

FIG. 9 is a block diagram of a finger processing system configured in accordance with one embodiment of the invention;

FIG. 10 is a block diagram of a transmit signal processing system configured in accordance with another embodiment of the invention;

15 FIG. 11 is a block diagram of the coding performed for the fundamental channel when configured in accordance with one embodiment of the invention;

FIG. 12 is a block diagram of the coding performed for the fundamental channel when configured in accordance with one embodiment of the invention;

FIG. 13 is a block diagram of the coding performed for the supplemental channel when configured in accordance with one embodiment of the invention; and

25 FIG. 14 is a block diagram of the coding performed for the control channel when configured in accordance with one embodiment of the invention.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

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A novel and improved method and apparatus for high rate CDMA wireless communication is described in the context of the reverse link transmission portion of a cellular telecommunications system. While the invention may be adapted for use within the multipoint-to-point reverse link transmission of a cellular telephone system, the present invention is equally applicable to forward link transmissions. In addition, many other wireless communication systems will benefit by incorporation of the invention, including satellite based wireless communication systems, point

to point wireless communication systems, and systems transmitting radio frequency signals via the use of co-axial or other broadband cables.

FIG. 2 is a block diagram of receive and transmit systems configured as a subscriber unit 100 and a base station 120. A first set of data (BPSK data) is received by BPSK channel encoder 103, which generates a code symbol stream configured for performing BPSK modulation that is received by modulator 104. A second set of data (QPSK data) is received by QPSK channel encoder 102, which generates a code symbol stream configured for performing QPSK modulation that is also received by modulator 104. Modulator 104 also receives power control data and pilot data, which are modulated along with the BPSK and QPSK encoded data in accordance with code division multiple access (CDMA) techniques to generate a set of modulation symbols received by RF processing system 106. RF processing system 106 filters and upconverts the set of modulation symbols to a carrier frequency for transmission to the base station 120 using antenna 108. While only one subscriber unit 100 is shown, multiple subscriber units may communicate with base station 120.

Within base station 120, RF processing system 122 receives the transmitted RF signals by way of antenna 121 and performs bandpass filtering, downconversion to baseband, and digitization. Demodulator 124 receives the digitized signals and performs demodulation in accordance with CDMA techniques to produce power control, BPSK, and QPSK soft decision data. BPSK channel decoder 128 decodes the BPSK soft decision data received from demodulator 124 to yield a best estimate of the BPSK data, and QPSK channel decoder 126 decodes the QPSK soft decision data received by demodulator 124 to produce a best estimate of the QPSK data. The best estimate of first and second set of data is then available for further processing or forwarding to a next destination, and the received power control data used either directly, or after decoding, to adjust the transmit power of the forward link channel used to transmit data to subscriber unit 100.

FIG. 3 is a block diagram of BPSK channel encoder 103 and QPSK channel encoder 102 when configured in accordance with the exemplary embodiment of the invention. Within BPSK channel encoder 103 the BPSK data is received by CRC check sum generator 130 which generates a check sum for each 20 ms frame of the first set of data. The frame of data along with the CRC check sum is received by tail bit generator 132 which appends tail bits comprised of eight logic zeros at the end of each frame to provide a

known state at the end of the decoding process. The frame including the code tail bits and CRC check sum is then received by convolutional encoder 134 which performs, constraint length (K) 9, rate (R) 1/4 convolutional encoding thereby generating code symbols at a rate four times the encoder input rate (E_R). In an alternative, other encoding rates are performed including rate 1/2, but the use of rate 1/4 is preferred due to its optimal complexity-performance characteristics. Block interleaver 136 performs bit interleaving on the code symbols to provide time diversity for more reliable transmission in fast fading environments. The resulting interleaved symbols are received by variable starting point repeater 138, which repeats the interleaved symbol sequence a sufficient number of times N_R to provide a constant rate symbol stream, which corresponds to outputting frames having a constant number of symbols. Repeating the symbol sequence also increases the time diversity of the data to overcome fading. In the exemplary embodiment, the constant number of symbols is equal to 6,144 symbols for each frame making the symbol rate 307.2 kilosymbols per second (ksps). Also, repeater 138 uses a different starting point to begin the repetition for each symbol sequence. When the value of N_R necessary to generate 6,144 symbols per frame is not an integer, the final repetition is only performed for a portion of the symbol sequence. The resulting set of repeated symbols are received by BPSK mapper 139 which generates a BPSK code symbol stream (BPSK) of +1 and -1 values for performing BPSK modulation. In an alternative, repeater 138 is placed before block interleaver 136 so that block interleaver 136 receives the same number of symbols for each frame.

Within QPSK channel encoder 102 the QPSK data is received by CRC check sum generator 140 which generates a check sum for each 20 ms frame. The frame including the CRC check sum is received by code tail bits generator 142 which appends a set of eight tail bits of logic zeros at the end of the frame. The frame, now including the code tail bits and CRC check sum, is received by convolutional encoder 144 which performs $K=9$, $R=1/4$ convolutional encoding thereby generating symbols at a rate four times the encoder input rate (E_R). Block interleaver 146 performs bit interleaving on the symbols and the resulting interleaved symbols are received by variable starting point repeater 148. Variable starting point repeater 148 repeats the interleaved symbol sequence a sufficient number of times N_R using a different starting point within the symbol sequence for each repetition to generate 12,288 symbols for each frame making the code symbol rate 614.4

kilosymbols per second (ksps). When N_R is not an integer, the final repetition is performed for only a portion of the symbol sequence. The resulting repeated symbols are received by QPSK mapper 149 which generates a QPSK code symbol stream configured for performing QPSK modulation comprised of an in-phase QPSK code symbol stream of +1 and -1 values (QPSK_I), and a quadrature-phase QPSK code symbol stream of +1 and -1 values (QPSK_Q). In an alternative, repeater 148 is placed before block interleaver 146 so that block interleaver 146 receives the same number of symbols for each frame.

FIG. 4 is a block diagram of modulator 104 of FIG. 2 configured in accordance with the exemplary embodiment of the invention. The BPSK symbols from BPSK channel encoder 103 are each modulated by Walsh code W_2 using a multiplier 150b, and the QPSK_I and QPSK_Q symbols from QPSK channel encoder 102 are each modulated with Walsh code W_3 using multipliers 150c and 154d. The power control data (PC) is modulated by Walsh code W_1 using multiplier 150a. Gain adjust 152 receives pilot data (PILOT), which preferably is comprised of the logic level associated with positive voltage, and adjusts the amplitude according to a gain adjust factor A_0 . The PILOT signal provides no user data but rather provides phase and amplitude information to the base station so that it can coherently demodulate the data carried on the remaining sub-channels, and scale the soft-decision output values for combining. Gain adjust 154 adjusts the amplitude of the Walsh code W_1 modulated power control data according to gain adjust factor A_1 , and gain adjust 156 adjusts the amplitude of the Walsh code W_2 modulated BPSK channel data according amplification variable A_2 . Gain adjust 158a and b adjust the amplitude of the in-phase and quadrature-phase Walsh code W_3 modulated QPSK symbols respectively according to gain adjust factor A_3 . The four Walsh codes used in the preferred embodiment of the invention are shown in Table I.

Walsh Code	Modulation Symbols
W_0	+ + + +
W_1	+ - + -
W_2	+ + - -
W_3	+ - - +

Table I.

It will be apparent to one skilled in the art that the W_0 code is effectively no modulation at all, which is consistent with processing of the pilot data shown. The power control data is modulated with the W_1 code, the BPSK data with the W_2 code, and the QPSK data with the W_3 code. Once modulated with the appropriate Walsh code, the pilot, power control data, and BPSK data are transmitted in accordance with BPSK techniques, and the QPSK data (QPSK_I and QPSK_Q) in accordance with QPSK techniques as described below. It should also be understood that it is not necessary that every orthogonal channel be used, and that the use of only three of the four Walsh codes where only one user channel is provided is employed in an alternative embodiment of the invention.

The use of short orthogonal codes generates fewer chips per symbol, and therefore allows for more extensive coding and repetition when compared to systems incorporating the use of longer Walsh codes. This more extensive coding and repetition provides protection against Raleigh fading which is a major source of error in terrestrial communication systems. The use of other numbers of codes and code lengths is consistent with the present invention, however, the use of a larger set of longer Walsh codes reduces this enhanced protection against fading. The use of four chip codes is considered optimal because four channels provides substantial flexibility for the transmission of various types of data as illustrated below while also maintaining short code length.

Summer 160 sums the resulting amplitude adjusted modulation symbols from gain adjusts 152, 154, 156 and 158a to generate summed modulation symbols 161. PN spreading codes PN_I and PN_Q are spread via multiplication with long code 180 using multipliers 162a and b. The resulting pseudorandom code provided by multipliers 162a and 162b are used to modulate the summed modulation symbols 161, and gain adjusted quadrature-phase symbols QPSK_Q 163, via complex multiplication using multipliers 164a-d and summers 166a and b. The resulting in-phase term X_I and quadrature-phase term X_Q are then filtered (filtering not shown), and upconverted to the carrier frequency within RF processing system 106 shown in a highly simplified form using multipliers 168 and an in-phase and a quadrature-phase sinusoid. An offset QPSK upconversion could also be used in an alternative embodiment of the invention. The resulting in-phase and quadrature-phase upconverted signals are summed using summer 170 and amplified by master amplifier 172 according to master gain

adjust A_M to generate signal $s(t)$ which is transmitted to base station 120. In the preferred embodiment of the invention, the signal is spread and filtered to a 1.2288 MHz bandwidth to remain compatible with the bandwidth of existing CDMA channels.

- 5 By providing multiple orthogonal channels over which data may be transmitted, as well as by using variable rate repeaters that reduce the amount of repeating N_R performed in response to high input data rates, the above described method and system of transmit signal processing allows a single subscriber unit or other transmit system to transmit data at a variety
- 10 of data rates. In particular, by decreasing the rate of repetition N_R performed by variable starting point repeaters 138 or 148 of FIG. 3, an increasingly higher encoder input rate E_R can be sustained. In an alternative embodiment of the invention rate 1/2 convolution encoding is performed with the rate of repetition N_R increased by two. A set of exemplary encoder
- 15 rates E_R supported by various rates of repetition N_R and encoding rates R equal to 1/4 and 1/2 for the BPSK channel and the QPSK channel are shown in Tables II and III respectively.

Label	E_R ,BPSK (bps)	Encoder Out $R=1/4$ (bits/frame)	N_R , $R=1/4$ (Repetition Rate, $R=1/4$)	Encoder Out $R=1/2$ (bits/frame)	N_R , $R=1/2$ (Repetition Rate, $R=1/2$)
High Rate-72	76,800	6,144	1	3,072	2
High Rate-64	70,400	5,632	1 1/11	2,816	2 2/11
	51,200	4,096	1 1/2	2,048	3
High Rate-32	38,400	3,072	2	1,536	4
	25,600	2,048	3	1,024	6
RS2-Full Rate	14,400	1,152	5 1/3	576	10 2/3
RS1-Full Rate	9,600	768	8	384	16
NULL	850	68	90 6/17	34	180 12/17

Table II. BPSK Channel

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Label	E_R ,QPSK (bps)	Encoder Out $R=1/4$ (bits/frame)	N_R , $R=1/4$ (Repetition Rate, $R=1/4$)	Encoder Out $R=1/2$ (bits/frame)	N_R , $R=1/2$ (Repetition Rate, $R=1/2$)
	153,600	12,288	1	6,144	2
High Rate-72	76,800	6,144	2	3,072	4
High Rate-64	70,400	5,632	2 2/11	2,816	4 4/11
	51,200	4,096	3	2,048	6
High Rate-32	38,400	3,072	4	1,536	8
	25,600	2,048	6	1,024	12
RS2-Full Rate	14,400	1,152	10 2/3	576	21 1/3
RS1-Full Rate	9,600	768	16	384	32
NULL	850	68	180 12/17	34	361 7/17

Table III. QPSK Channel

10 Tables II and III show that by adjusting the number of sequence repetitions N_R , a wide variety of data rates can be supported including high data rates, as the encoder input rate E_R corresponds to the data transmission rate minus a constant necessary for the transmission of CRC, code tail bits and any other overhead information. As also shown by tables II and III, QPSK modulation may also be used to increase the data transmission rate. Rates expected to be used commonly are provided labels such as "High Rate-

72" and "High Rate-32." Those rates noted as High Rate-72, High Rate-64, and High Rate-32 have traffic rates of 72, 64 and 32 kbps respectively, plus multiplexed in signaling and other control data with rates of 3.6, 5.2, and 5.2 kbps respectively. Rates RS1-Full Rate and RS2-Full Rate correspond to rates
5 used in IS-95 compliant communication systems, and therefore are also expected to receive substantial use for purposes of compatibility. The null rate is the transmission of a single bit and is used to indicate a frame erasure, which is also part of the IS-95 standard.

The data transmission rate may also be increased by simultaneously
10 transmitting data over two or more of the multiple orthogonal channels performed either in addition to, or instead of, increasing the transmission rate via reduction of the repetition rate N_R . For example, a multiplexer (not shown) could split a single data source into a multiple data sources to be transmitted over multiple data sub-channels. Thus, the total transmit rate
15 can be increased via either transmission over a particular channel at higher rates, or multiple transmission performed simultaneously over multiple channels, or both, until the signal processing capability of the receive system is exceeded and the error rate becomes unacceptable, or the maximum transmit power of the of the transmit system power is reached.

20 Providing multiple channels also enhances flexibility in the transmission of different types of data. For example, the BPSK channel may be designated for voice information and the QPSK channel designated for transmission of digital data. This embodiment could be more generalized by designating one channel for transmission of time sensitive data such as
25 voice at a lower data rate, and designating the other channel for transmission of less time sensitive data such as digital files. In this embodiment interleaving could be performed in larger blocks for the less time sensitive data to further increase time diversity. In another embodiment of the invention, the BPSK channel performs the primary
30 transmission of data, and the QPSK channel performs overflow transmission. The use of orthogonal Walsh codes eliminates or substantially reduces any interference among the set of channels transmitted from a subscriber unit, and thus minimizes the transmit energy necessary for their successful reception at the base station.

35 To increase the processing capability at the receive system, and therefore increase the extent to which the higher transmission capability of the subscriber unit may be utilized, pilot data is also transmitted via one of the orthogonal channels. Using the pilot data, coherent processing can be

performed at the receive system by determining and removing the phase offset of the reverse link signal. Also, the pilot data can be used to optimally weigh multipath signals received with different time delays before being combined in a rake receiver. Once the phase offset is removed, and the
5 multipath signals properly weighted, the multipath signals can be combined decreasing the power at which the reverse link signal must be received for proper processing. This decrease in the required receive power allows greater transmissions rates to be processed successfully, or conversely, the interference between a set of reverse link signals to be decreased. While
10 some additional transmit power is necessary for the transmission of the pilot signal, in the context of higher transmission rates the ratio of pilot channel power to the total reverse link signal power is substantially lower than that associated with lower data rate digital voice data transmission cellular systems. Thus, within a high data rate CDMA system the E_b/N_0
15 gains achieved by the use of a coherent reverse link outweigh the additional power necessary to transmit pilot data from each subscriber unit.

The use of gain adjusts 152 - 158 as well as master amplifier 172 further increases the degree to which the high transmission capability of the above described system can be utilized by allowing the transmit system to
20 adapt to various radio channel conditions, transmission rates, and data types. In particular, the transmit power of a channel that is necessary for proper reception may change over time, and with changing conditions, in a manner that is independent of the other orthogonal channels. For example, during the initial acquisition of the reverse link signal the power of the pilot
25 channel may need to be increased to facilitate detection and synchronization at the base station. Once the reverse link signal is acquired, however, the necessary transmit power of the pilot channel would substantially decrease, and would vary depending on various factors including the subscriber units rate of movement. Accordingly, the value of the gain adjust factor A_0
30 would be increased during signal acquisition, and then reduced during an ongoing communication. In another example, when information more tolerable of error is being transmitted via the forward link, or the environment in which the forward link transmission is taking place is not prone to fade conditions, the gain adjust factor A_1 may be reduced as the
35 need to transmit power control data with a low error rate decreases. Preferably, whenever power control adjustment is not necessary the gain adjust factor A_1 is reduced to zero.

In another embodiment of the invention, the ability to gain adjust each orthogonal channel or the entire reverse link signal is further exploited by allowing the base station 120 or other receive system to alter the gain adjust of a channel, or of the entire reverse link signal, via the use of power control commands transmitted via the forward link signal. In particular, the base station may transmit power control information requesting the transmit power of a particular channel or the entire reverse link signal be adjusted. This is advantageous in many instances including when two types of data having different sensitivity to error, such as digitized voice and digital data, are being transmitted via the BPSK and QPSK channels. In this case, the base station 120 would establish different target error rates for the two associated channels. If the actual error rate of a channel exceeded the target error rate, the base station would instruct the subscriber unit to reduce the gain adjust of that channel until the actual error rate reached the target error rate. This would eventually lead to the gain adjust factor of one channel being increased relative to the other. That is, the gain adjust factor associated with the more error sensitive data would be increased relative to the gain adjust factor associated with the less sensitive data. In other instances, the transmit power of the entire reverse link may require adjustment due to fade conditions or movement of the subscriber unit 100. In these instances, the base station 120 can do so via transmission of a single power control command.

Thus, by allowing the gain of the four orthogonal channels to be adjusted independently, as well as in conjunction with one another, the total transmit power of the reverse link signal can be kept at the minimum necessary for successful transmission of each data type, whether it is pilot data, power control data, signaling data, or different types of user data. Furthermore, successful transmission can be defined differently for each data type. Transmitting with the minimum amount of power necessary allows the greatest amount of data to be transmitted to the base station given the finite transmit power capability of a subscriber unit, and also reduces the interfere between subscriber units. This reduction in interference increases the total communication capacity of the entire CDMA wireless cellular system.

The power control channel used in the reverse link signal allows the subscriber unit to transmit power control information to the base station at a variety of rates including a rate of 800 power control bits per second. In the preferred embodiment of the invention, a power control bit instructs the

base station to increase or decrease the transmit power of the forward link traffic channel being used to transmit information to the subscriber unit. While it is generally useful to have rapid power control within a CDMA system, it is especially useful in the context of higher data rate communications involving data transmission, because digital data is more sensitive to errors, and the high transmission causes substantial amounts of data to be lost during even brief fade conditions. Given that a high speed reverse link transmission is likely to be accompanied by a high speed forward link transmission, providing for the rapid transmission of power control over the reverse link further facilitates high speed communications within CDMA wireless telecommunications systems.

In an alternative exemplary embodiment of the invention a set of encoder input rates E_R defined by the particular N_R are used to transmit a particular type of data. That is, data may be transmitted at a maximum encoder input rate E_R or at a set of lower encoder input rates E_R , with the associated N_R adjusted accordingly. In the preferred implementation of this embodiment, the maximum rates corresponds to the maximum rates used in IS-95 compliant wireless communication system, referred to above with respect to Tables II and III as RS1-Full Rate and RS2-Full Rate, and each lower rate is approximately one half the next higher rate, creating a set of rates comprised of a full rate, a half rate, a quarter rate, and an eighth rate. The lower data rates are preferable generated by increasing the symbol repetition rate N_R with value of N_R for rate set one and rate set two in a BPSK channel provided in Table IV.

Label	E_R , QPSK (bps)	Encoder Out $R=1/4$ (bits/frame)	N_R , $R=1/4$ (Repetition Rate, $R=1/4$)	Encoder Out $R=1/2$ (bits/frame)	N_R , $R=1/2$ (Repetition Rate, $R=1/2$)
RS2-Full Rate	14,400	1,152	5 1/3	576	10 2/3
RS2-Half Rate	7,200	576	10 2/3	288	21 1/3
RS2-Quarter Rate	3,600	288	21 1/3	144	42 2/3
RS2-Eighth Rate	1,900	152	40 8/19	76	80 16/19
RS1-Full Rate	9,600	768	8	384	16
RS1-Half Rate	4,800	384	16	192	32
RS1-Quarter Rate	2,800	224	27 3/7	112	54 6/7
RS1-Eighth Rate	1,600	128	48	64	96
NULL	850	68	90 6/17	34	180 12/17

Table IV. RS1 and RS2 Rate Sets

in BPSK Channel

5 The repetition rates for a QPSK channel is twice that for the BPSK channel.

10 In accordance with the exemplary embodiment of the invention, when the data rate of a frame changes with respect to the previous frame the transmit power of the frame is adjusted according to the change in transmission rate. That is, when a lower rate frame is transmitted after a higher rate frame, the transmit power of the transmit channel over which the frame is being transmitted is reduced for the lower rate frame in proportion to the reduction in rate, and vice versa. For example, if the transmit power of a channel during the transmission of a full rate frame is transmit power T, the transmit power during the subsequent transmission of a half rate frame is transmit power T/2. The reduction in transmit power is preferably performed by reducing the transmit power for the entire duration of the frame, but may also be performed by reducing the transmit duty cycle such that some redundant information is "blanked out." In either case, the transmit power adjustment takes place in combination with a closed loop power control mechanism whereby the transmit power is further adjusted in response to power control data transmitted from the base station.

20 FIG. 5 is a block diagram of RF processing system 122 and demodulator 124 of FIG. 2 configured in accordance with the exemplary

embodiment of the invention. Multipliers 180a and 180b downconvert the signals received from antenna 121 with an in-phase sinusoid and a quadrature phase sinusoid producing in-phase receive samples R_I and quadrature-phase receive samples R_Q receptively. It should be understood that RF processing system 122 is shown in a highly simplified form, and that the signals are also match filtered and digitized (not shown) in accordance with widely known techniques. Receive samples R_I and R_Q are then applied to finger demodulators 182 within demodulator 124. Each finger demodulator 182 processes an instance of the reverse link signal transmitted by subscriber unit 100, if such an instance is available, where each instance of the reverse link signal is generated via multipath phenomenon. While three finger demodulators are shown, the use of alternative numbers of finger processors are consistent with the invention including the use of a single finger demodulator 182. Each finger demodulator 182 produces a set of soft decision data comprised of power control data, BPSK data, and QPSK_I data and QPSK_Q data. Each set of soft decision data is also time adjusted within the corresponding finger demodulator 182, although time adjustment could be performed within combiner 184 in an alternative embodiment of the invention. Combiner 184 then sums the sets of soft decision data received from finger demodulators 182 to yield a single instance of power control, BPSK, QPSK_I and QPSK_Q soft decision data.

FIG. 6 is block diagram a finger demodulator 182 of FIG. 5 configured in accordance with the exemplary embodiment of the invention. The R_I and R_Q receive samples are first time adjusted using time adjust 190 in accordance with the amount of delay introduced by the transmission path of the particular instance of the reverse link signal being processed. Long code 200 is mixed with pseudorandom spreading codes PN_I and PN_Q using multipliers 201, and the complex conjugate of the resulting long code modulated PN_I and PN_Q spreading codes are complex multiplied with the time adjusted R_I and R_Q receive samples using multipliers 202 and summers 204 yielding terms X_I and X_Q . Three separate instances of the X_I and X_Q terms are then demodulated using the Walsh codes W_1 , W_2 and W_3 respectively, and the resulting Walsh demodulated data is summed over four demodulation chips using 4 to 1 summers 212. A fourth instance of the X_I and X_Q data is summed over four demodulation chips using summers 208, and then filtered using pilot filters 214. In the preferred embodiment of the invention pilot filter 214 performs averaging over a series of summations performed by summers 208, but other filtering

techniques will be apparent to one skilled in the art. The filtered in-phase and quadrature-phase pilot signals are used to phase rotate and scale the W_1 , and W_2 Walsh code demodulated data in accordance with BPSK modulated data via complex conjugate multiplication using multipliers 216 and adders 217 yielding soft decision power control and BPSK data. The W_3 Walsh code modulated data is phase rotated using the in-phase and quadrature-phase filtered pilot signals in accordance with QPSK modulated data using multipliers 218 and adders 220, yielding soft decision QPSK data. The soft decision power control data is summed over 384 modulation symbols by 384 to 1 summer 222 yielding power control soft decision data. The phase rotated W_2 Walsh code modulated data, the W_3 Walsh code modulated data, and the power control soft decision data are then made available for combining. In an alternative embodiment of the invention, encoding and decoding is performed on the power control data as well.

In addition to providing phase information the pilot may also be used within the receive system to facilitate time tracking. Time tracking is performed by also processing the received data at one sample time before (early), and one sample time after (late), the present receive sample being processed. To determine the time that most closely matches the actual arrival time, the amplitude of the pilot channel at the early and late sample time can be compared with the amplitude at the present sample time to determine that which is greatest. If the signal at one of the adjacent sample times is greater than that at the present sample time, the timing can be adjusted so that the best demodulation results are obtained.

FIG. 7 is a block diagram of BPSK channel decoder 128 and QPSK channel decoder 126 (FIG. 2) configured in accordance with the exemplary embodiment of the invention. BPSK soft decision data from combiner 184 (FIG. 5) is received by accumulator 240 which stores the first sequence of $6,144/N_R$ demodulation symbols in the received frame where N_R depends on the transmission rate of the BPSK soft decision data as described above, and adds each subsequent set of $6,144/N_R$ demodulated symbols contained in the frame with the corresponding stored accumulated symbols. Block deinterleaver 242 deinterleaves the accumulated soft decision data from variable starting point summer 240, and Viterbi decoder 244 decodes the deinterleaved soft decision data to produce hard decision data as well as CRC check sum results. Within QPSK decoder 126 $QPSK_I$ and $QPSK_Q$ soft decision data from combiner 184 (FIG. 5) are demultiplexed into a single soft decision data stream by demux 246 and the single soft decision data stream is

received by accumulator 248 which accumulates every $6,144/N_R$ demodulation symbols where N_R depends on the transmission rate of the QPSK data. Block deinterleaver 250 deinterleaves the soft decision data from variable starting point summer 248, and Viterbi decoder 252 decodes the deinterleaved modulation symbols to produce hard decision data as well as CRC check sum results. In the alternative exemplary embodiment described above with respect to FIG. 3 in which symbol repetition is performed before interleaving, accumulators 240 and 248 are placed after block deinterleavers 242 and 250. In the embodiment of the invention incorporating the use of rate sets, and therefore in which the rate of particular frame is not known, multiple decoders are employed, each operating at a different transmission rate, and then the frame associated with the transmission rate most likely to have been used is selected based on the CRC checksum results. The use of other error checking methods is consistent with the practice of the present invention.

FIG. 8 is a block diagram of modulator 104 (FIG. 2) configured in an alternative embodiment of the invention in which a single BPSK data channel is employed. Pilot data is gain adjusted by gain adjust 452 in accordance with gain adjust factor A_0 . Power control data is modulated with Walsh code W_1 by multiplier 150a and gain adjusted by gain adjust 454 in accordance with gain adjust factor A_1 . The gain adjusted pilot data and power control data are summed by summer 460 producing summed data 461. BPSK data is modulated with Walsh code W_2 by multiplier 150b and then gain adjusted using gain adjust 456 in accordance with gain adjust factor A_2 .

In-phase pseudo random spreading code (PN_I) and quadrature-phase pseudo random spreading code (PN_Q) are both modulated with long code 480. The resulting long code modulated PN_I and PN_Q codes are complex multiplied with the summed data 461 and the gain adjusted BPSK data from gain adjust 456 using multipliers 464a - d and summers 466a - b yielding terms X_I and X_Q . Terms X_I and X_Q are then upconverted with in-phase and quadrature-phase sinusoids using multipliers 468 and the resulting upconverted signals are summed by summers 470 respectively, and amplified by amplifier 472 in accordance with amplitude factor A_M generating signal $s(t)$.

The embodiment shown in FIG. 8 differs from the other embodiments described herein in that the BPSK data is placed in the quadrature-phase channel while the pilot data and power control data are

placed in the in-phase channel. In the previous embodiments of the invention described herein the BPSK data is placed the in-phase channel along with the pilot data and power control data. Placing the BPSK data in the quadrature-phase channel and the pilot and power control data in the in-phase channel reduces the peak-to-average power ratio of the reverse link signal the phases of the channels are orthogonal causing the magnitude of the sum of the two channels to vary less in response to changing data. This reduces the peak power required to maintain a given average power, and thus reduces the peak-to-average power ratio characteristic of the reverse link signal. This reduction in the peak-to-average power ratio decreases the peak power at which a reverse link signal must be received at the base station in order to sustain a given transmission rate, and therefore increases the distance a subscriber unit having a maximum transmit power may be located from the base station before it is unable to transmit a signal that can received at base station with the necessary peak power. This increases the range at which the subscriber unit can successfully conduct communication at any given data rate, or alternatively allows greater data rates to be sustained at a given distance.

FIG. 9 is a block diagram of finger demodulator 182 when configured in accordance with the embodiment of the invention shown in FIG. 8. Receive samples R_I and R_Q are time adjusted by timing adjust 290 and the PN_I and PN_Q codes are multiplied by long code 200 using multipliers 301. The time adjusted receive samples are then multiplied by the complex conjugate of the PN_I and PN_Q codes using multipliers 302 and summers 304 yielding terms X_I and X_Q . A first and second instance of the X_I and X_Q terms are demodulated using Walsh code W_1 and Walsh code W_2 using multipliers 310 and the resulting demodulation symbols are summed in sets of four using summers 312. A third instance of the X_I and X_Q terms are summed over four demodulation symbols by summers 308 to generate pilot reference data. The pilot reference data is filtered by pilot filters 314 and used to phase rotate and scale the summed Walsh code modulated data using multipliers 316 and adders 320 producing BPSK soft decision data, and after being summed over 384 symbols by 384:1 summer 322, soft decision power control data.

FIG. 10 is a block diagram of a transmit system configured in accordance with still another embodiment of the invention. Channel gain 400 gain adjusts pilot channel 402 based on gain variable A_0 . Fundamental channel symbols 404 are mapped into +1 and -1 values by mapper 405, and

each symbol is modulated with Walsh code W_F equal to $+,+,-,-$ (where $+$ = $+1$ and $-$ = -1). The W_F modulated data is gain adjusted based on gain variable A_1 by gain adjust 406. The outputs of gain adjusts 400 and 406 are summed by summer 408 yielding in-phase data 410.

5 Supplemental channel symbols 411 are mapped to $+$ and $-$ values by mapper 412, and each symbol is modulated with a Walsh code W_S equal to $+, -$. Gain adjust 414 adjusts the gain of the W_S modulated data. Control channel data 415 is mapped to $+$ and $-$ values by mapper 416. Each symbol is modulated with a Walsh code W_C equal to $+, +, +, +, -, -, -, -$. The W_C
10 modulated symbols are gain adjusted by gain adjust 418 based on gain variable A_3 , and the output of gain adjusts 414 and 418 are summed by summer 419 to produce quadrature phase data 420.

It should be apparent that, since the Walsh codes W_F and W_S are different lengths, and are generated at the same chip rate, the fundamental
15 channel transmits data symbols at a rate that is half that of the supplemental channel. For similar reasons, it should be apparent that the control channel transmits data symbols at half the rate of the fundamental channel.

In-phase data 410 and quadrature phase data 420 are complex multiplied by the PN_I and PN_Q spreading codes as shown, yielding in-phase
20 term X_I and quadrature phase term X_Q . The quadrature phase term X_Q is delay by $1/2$ the duration of a PN spreading code chip to perform offset QPSK spreading, and then term X_I and term X_Q are upconverted in accordance the RF processing system 106 shown in FIG. 4, and described above.

By using Walsh codes W_F , W_S and W_C having different lengths as
25 described above, this alternative provides a set of communication channels having a greater variety of rates. Additionally, the use of a shorter, two chip, Walsh code W_S for the supplemental channel provides an orthogonal higher data rate supplemental channel with a peak-to-average transmit power ratio that is less than that associated with the use of two channels
30 based on 4 chip Walsh codes. This further enhances the performance of the transmit system in that a given amplifier will be able to sustain higher rate, or transmit with greater range, using the lower peak-to-average transmit power waveform.

The Walsh code allocation scheme described with regard to FIG. 10,
35 can also be viewed as the allocation of eight chip Walsh space in accordance with Table VI.

Eight-Chip Walsh Code	Channel
+ + + + + + + +	Pilot
+ - + - + - + -	Supplemental
+ + - - + + - -	Fundamental
+ - - + + - - +	Supplemental
+ + + + - - - -	Control
+ - + - - + - +	Supplemental
+ + - - - - + +	Fundamental
+ - - + - + + -	Supplemental

Table VI.

5 In addition to reducing the peak to average transmit power ratio, allocating sets of eight-chip Walsh channels using a single shorter Walsh code decreases the complexity of the transmit system. For example, modulating with four eight-chip Walsh codes and summing the results requires additional circuitry and therefore would be more complex.

10 It is further contemplated that the transmission system shown in FIG. 10 can operate at various spreading bandwidths, and therefore with the Walsh codes and spreading codes generated at various rates other than 1.2288 Mcchips/second. In particular, a spreading bandwidth of 3.6864 MHz is contemplated, with a corresponding Walsh and spreading code rate of 3.6864 Mcchips/second. FIGS. 11 - 14 illustrate the coding performed for the
15 fundamental, supplemental and control channels in accordance with the use of a 3.6864 MHz spreading bandwidth. Typically, to adjust the coding for use with a 1.2288 MHz spreading bandwidth the number of symbol repeats is reduced. This principal or adjusting the number of symbol repeats can be applied more generally to increases in the spreading bandwidth including,
20 for example, the use of a 5 MHz spreading bandwidth. Adjustments performed to the coding for a 1.2288 MHz spreading bandwidth system other than reduction in the number of symbol repeats are particularly noted in the description of FIGS. 11 - 14 provided below.

25 FIG. 11 shows the coding performed for the four rates (i.e. full, half, quarter and eight rate) that make up the IS-95 rate set 1 when performed in accordance with one embodiment of the invention. Data is supplied in 20 ms frames having the number of bits shown for each rate, and CRC check bits and eight tail bits are added by CRC checks sum generators 500a - d and

tail bit generators 502a - d. Additionally, rate 1/4 convolutional encoding is performed for each rate by convolutional encoders 504a-d, generating four code symbols for each data bit, CRC bit, or tail bit. The resulting frame of code symbols is block interleaved using block interleavers 506a - d, generating the number of symbols indicated. For the lower three rates, the symbols are transmitted repeatedly by transmission repeaters 508a - c, as indicated, causing 768 code symbols to be generated for each frame. The 768 code symbols for each rate are then repeated 24 times by symbol repeaters 510a - d generating 18,432 code symbols per frame for each rate.

As discussed above, each code symbol in the fundamental channel is modulated with a four bit Walsh code W_F generated at 3,686,400 chips per second (3.6864 Mchips/second). Thus, for a 20 ms time interval (1/50th of a second) the number of Walsh and spreading code chips is 73,728, which corresponds to 4 Walsh chips for each of the 18,432 code symbol in the frame.

For a system operating at 1.2288 Mchips/second, the number of symbol repeats performed by symbol repeaters 510a-d is reduced to eight (8). Additionally, transmission repeater 508b repeats the sequence of symbols in the frame three (3) times, plus 120 of the symbols are transmitted a fourth time, and transmission repeater 508c repeats the sequence of symbols in the frame six (6) times, plus 48 of the symbols are repeated a seventh time. Additionally, a fourth transmission repeater (or fourth transmission repeat step) is included for the full rate (not shown) which transmits 384 of the sequence of symbols contained in the frame a second time. These repeated transmissions all provide 768 symbols of data which, when repeated eight times by symbol repeaters 510a - d, correspond to 6,144 symbols, which is the number of chips in a 20 ms frame at 1.2288 Mchips/second.

FIG. 12 shows the coding performed for the four rates that make up IS-95 rate set 2 when performed in accordance with one embodiment of the invention. Data is supplied in 20 ms frames having the number of bits shown for each rate, and a reserve bit is added by reserve bit augmenters 521a - d for each rate. CRC check bits and eight tail bits are also added by CRC checks sum generators 520a - d and tail bit generators 522a - d. Additionally, rate 1/4 convolutional encoding is performed for each rate by convolutional encoders 524a-d, generating four code symbols for each data, CRC or tail bit. The resulting frame of code symbols is block interleaved using block interleaves 526a - d generating the number of symbols indicated. For the lower three rates, the symbols are transmitted repeatedly by transmission

repeaters 528a - c as indicated, causing 768 code symbols to be generated for each frame. The 768 code symbols for each rate are then repeated 24 times by symbol repeaters 530a - d generating 18,432 code symbols per frame for each rate.

5 For a system operating at 1.2288 MHz spreading bandwidth, the number of symbol repeats performed by symbol repeaters 530a-d is reduced to four (4). Additionally, transmission repeater 528a transmits the sequence of symbols in the frame two (2) times, plus 384 of the symbols are transmitted a third time. Transmission repeater 528b repeats the sequence of
10 symbols in the frame five (5) times, plus 96 of the symbols are transmitted a sixth time. Transmission repeater 528c repeats the sequence of symbols in the frame ten (10) times, plus 96 of the symbols are repeated an eleventh time. Additionally, a fourth transmission repeater (or fourth transmission repeat step) is included for the full rate (not shown) which transmits 384 of
15 the sequence of symbols contained in the frame a second time. These repeated transmissions all provide 1,536 symbols of data which, when repeated four times by symbol repeaters 530a - d, correspond to 6,144 symbols.

FIG. 13 illustrates the coding performed for the supplemental channel
20 when performed in accordance with one embodiment of the invention. Frames of data are supplied at any of the eleven rates indicated, and CRC check sum generator 540 adds 16 bits of CRC checksum data. Tail bit generator 542 adds eight bits of encoder tail data resulting in frames having the data rates shown. Convolution encoder 544 performs rate 1/4, constraint
25 length $K = 9$, encoding generating four code symbols for each data, CRC or tail bit received, and block interleaver 546 performs block interleaving on each frame, and outputs the number of code symbols shown for each frame in accordance with the input frame size. Symbol repeater 548 repeats the frames N times depending on the input frame size as indicated.

30 The encoding for an additional twelfth rate is shown, which is performed in similar fashion to the eleven rates, with the exception that rate 1/2 encoding is performed instead of rate 1/4. Additionally, no symbol repetition is performed.

A list of frame sizes, encoder input rates, code rates and symbol
35 repetition factors N for various chip rates that can be applied to FIG. 13 to adjust for different chip rates (which correspond to spreading bandwidths) is provided in Table VII.

Chip Rate (Mcps)	Number of Octets per Frame	Encoder Input Rate (kbps)	Code Rate	Symbol Repetition Factor (N)	Chip Rate (Mcps)	Number of Octets per Frame	Encoder Input Rate (kbps)	Code Rate	Symbol Repetition Factor (N)
1.2288	21	9.6	1/4	16	7.3728	21	9.6	1/4	96
1.2288	45	19.2	1/4	8	7.3728	33	14.4	1/4	64
1.2288	93	38.4	1/4	4	7.3728	45	19.2	1/4	48
1.2288	189	76.8	1/4	2	7.3728	69	28.8	1/4	32
1.2288	381	153.6	1/4	1	7.3728	93	38.4	1/4	24
					7.3728	141	57.6	1/4	16
					7.3728	189	76.8	1/4	12
					7.3728	285	115.2	1/4	8
					7.3728	381	153.6	1/4	6
					7.3728	573	230.4	1/4	4
					7.3728	765	307.2	1/4	3
					7.3728	1,149	460.8	1/4	2
					7.3728	2,301	921.6	1/4	1
1.2288	765	307.2	1/2	1	7.3728	4,605	1,843.2	1/2	1
3.6864	21	9.6	1/4	48	14.7456	21	9.6	1/4	192
3.6864	33	14.4	1/4	32	14.7456	33	14.4	1/4	128
3.6864	45	19.2	1/4	24	14.7456	45	19.2	1/4	96
3.6864	69	28.8	1/4	16	14.7456	69	28.8	1/4	64
3.6864	93	38.4	1/4	12	14.7456	93	38.4	1/4	48
3.6864	141	57.6	1/4	8	14.7456	141	57.6	1/4	32
3.6864	189	76.8	1/4	6	14.7456	189	76.8	1/4	24
3.6864	285	115.2	1/4	4	14.7456	285	115.2	1/4	16
3.6864	381	153.6	1/4	3	14.7456	381	153.6	1/4	12
3.6864	573	230.4	1/4	2	14.7456	573	230.4	1/4	8
					14.7456	765	307.2	1/4	6
3.6864	1,149	460.8	1/4	1	14.7456	1,149	460.8	1/4	4
					14.7456	1,533	614.4	1/4	3
					14.7456	2,301	921.6	1/4	2
					14.7456	4,605	1,843.2	1/4	1
3.6864	2,301	921.6	1/2	1	14.7456	9,213	3,686.4	1/2	1

Table VII.

5 FIG. 14 is a block diagram of the processing performed for the control channel for a 3.6864 MHz spreading bandwidth system. The processing is substantially similar to that associated with the other channels, except for the addition a mux 560 and symbol repeater 562, which operate to introduce uncoded power control bits into the code symbol stream. The power control bits are generated at a rate of 16 per frame, and repeated 18 times by symbol repeater 562 resulting in 288 power control bits per frame. The 288 power control bits are multiplexed into the frame of code symbols at a ratio of three power control bits per coded data symbol, generating 384 total symbols per frame. Symbol repeater 564 repeats the 384 bits 24 times generating 9,216 symbols per frame for an effective data rate of 500 kbits/second for the control data, and 800 kbits/second for the power control bits. The preferred processing performed for a 1.2288 MHz bandwidth system simply reduces the number of symbol repetitions performed from 24 to 8.

20 Thus, a multi-channel, high rate, CDMA wireless communication system has been described. The description is provided to enable any person skilled in the art to make or use the present invention. The various

- modifications to these embodiments will be readily apparent to those skilled in the art, and the generic principles defined herein may be applied to other embodiments without the use of the inventive faculty. Thus, the present invention is not intended to be limited to the embodiments shown herein
- 5 but is to be accorded the widest scope consistent with the principles and novel features disclosed herein.

What I claim is:

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CLAIMS:

1. A subscriber unit or other transmitter for use in a wireless communication system, the subscriber unit comprising:
 - plural information sources of information data;
 - an encoder for encoding the information data;
 - plural control sources of control data; and
 - a modulator for modulating encoded information data and the control data from one or more of the plural control sources with respective different modulating codes for transmission on a carrier signal, wherein the modulator is arranged to combine encoded information data from one information source with the encoded control data before the same is output for transmission.
2. A subscriber unit as claimed in claim 1, wherein the control data comprises power control data and pilot data.
3. A subscriber unit as claimed in claim 2, wherein the modulator is operable to modulate the power control data with a modulation code.
4. A subscriber unit as claimed in any preceding claim, wherein the modulation codes are Walsh codes.
5. A subscriber unit as claimed in claim 4, wherein the Walsh code used to modulate the information data from a first one of the information sources is longer than the Walsh code used to modulate the information data from a second one of the information sources.
6. A subscriber unit as claimed in claim 5, wherein the Walsh code used to modulate the encoded control is longer than the Walsh code used to modulate the data information data from the second one of the information sources.
7. A subscriber unit as claimed in claim 6, wherein the Walsh code used to modulate the encoded control data comprises eight chips, the Walsh code used to modulate the information data from the first one of the information sources comprises four chips, and the Walsh code used to

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6 modulate the information data from the second one of the information
sources comprises two chips.

8. A subscriber unit as claimed in any preceding claim, further
2 comprising a combiner for combining the modulated data from the
modulator with each other and with a spreading code for transmission on a
4 carrier signal.

9. A subscriber unit as claimed in claim 8, further comprising a
2 transmitter circuit for transmitting the carrier signal carrying the spread,
combined, modulated data.

10. A subscriber unit as claimed in any preceding claim, wherein
2 the encoder is arranged to effect a low code rate error correction and
sequence repetition to the information data.

4 11. A base station or other receiver for use in a wireless
communication system, the base station comprising:

6 a receiver for receiving a carrier signal and removing therefrom
encoded information data from plural information sources modulated with
8 respective different modulating codes and control data from plural control
sources with the one or more of the control data being modulated with a
10 respective different modulating code and with the encoded information data
from one information source being combined with the encoded control data;
12 a demodulator for demodulating the encoded information data and the
control data from their respective different modulating codes; and

14 a decoder for decoding the encoded information and control
data.

12. A method for transmitting control data, fundamental data, and
2 supplemental data from a first subscriber unit in a set of subscriber units to a
base station in communication with the set of subscriber units comprising:

4 a) modulating the supplemental data with a first Walsh
code;

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30

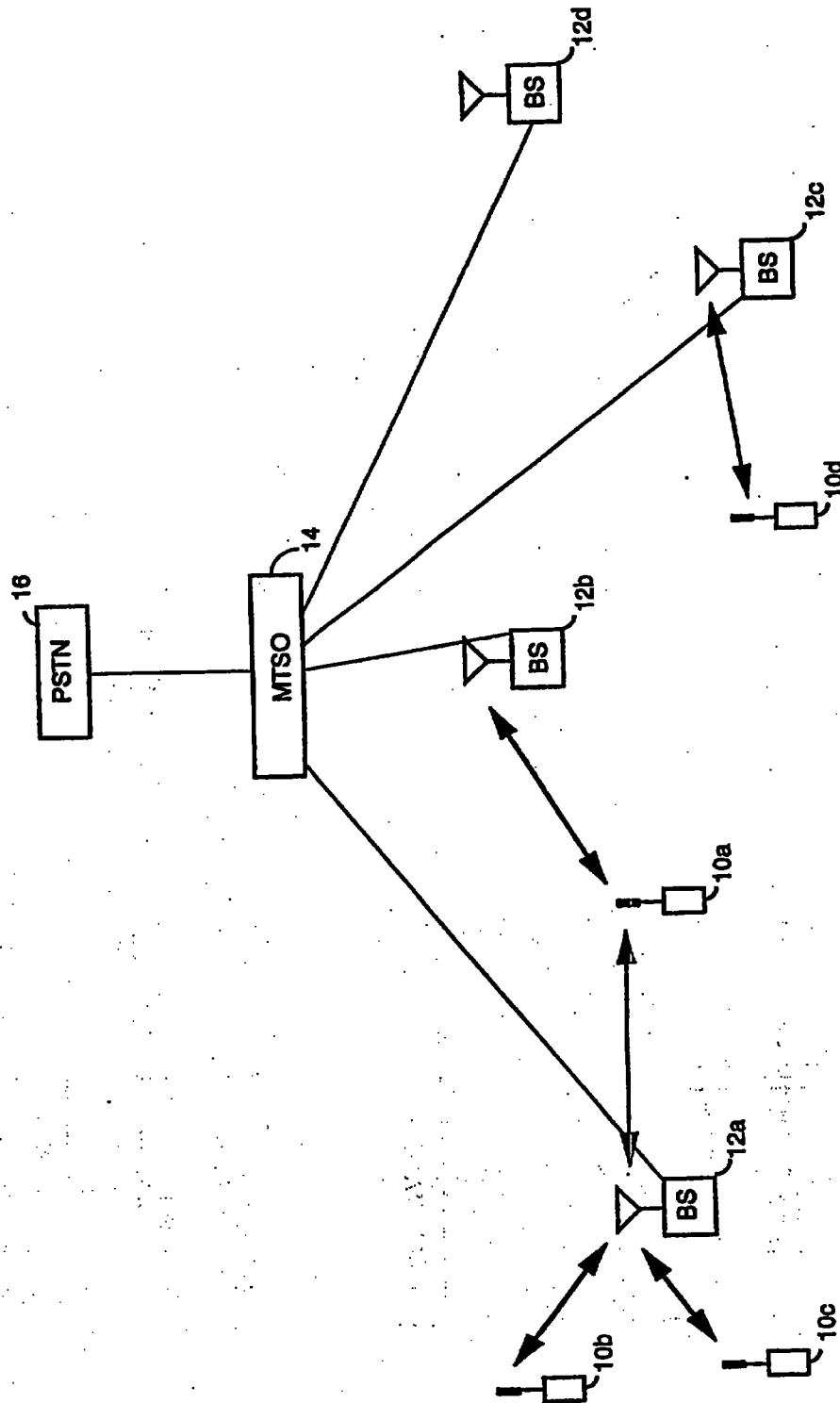
- 6 b) modulating the fundamental data with a second Walsh
code; and
8 c) modulating the control data with a third Walsh code,
where
10 said first Walsh code is shorter than said second Walsh code, and said
second Walsh code is shorter than said third Walsh code.

13. The method as set forth in claim 12, wherein said first Walsh
2 code is comprised of two chips, said second Walsh code is comprised of four
chips, and said third Walsh code is comprised of eight chips.

14. The method as set forth in claim 12 or 13, further comprising a
2 pilot channel, wherein said pilot channel is summed with the fundamental
data, the supplemental data is summed with said control data.

15. A method of transmitting data from a subscriber unit for use in
2 a wireless communication system, the method comprising:
acquiring information data from plural information sources;
4 encoding the information data;
acquiring control data from plural control sources; and
6 modulating the encoded information data and the control data from
one or more of the plural control sources with respective different
8 modulating codes for transmission on a carrier signal, wherein the encoded
information data from one information source is combined with the
10 encoded control data before the same is output for transmission.

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(PRIOR ART)
FIG. 1

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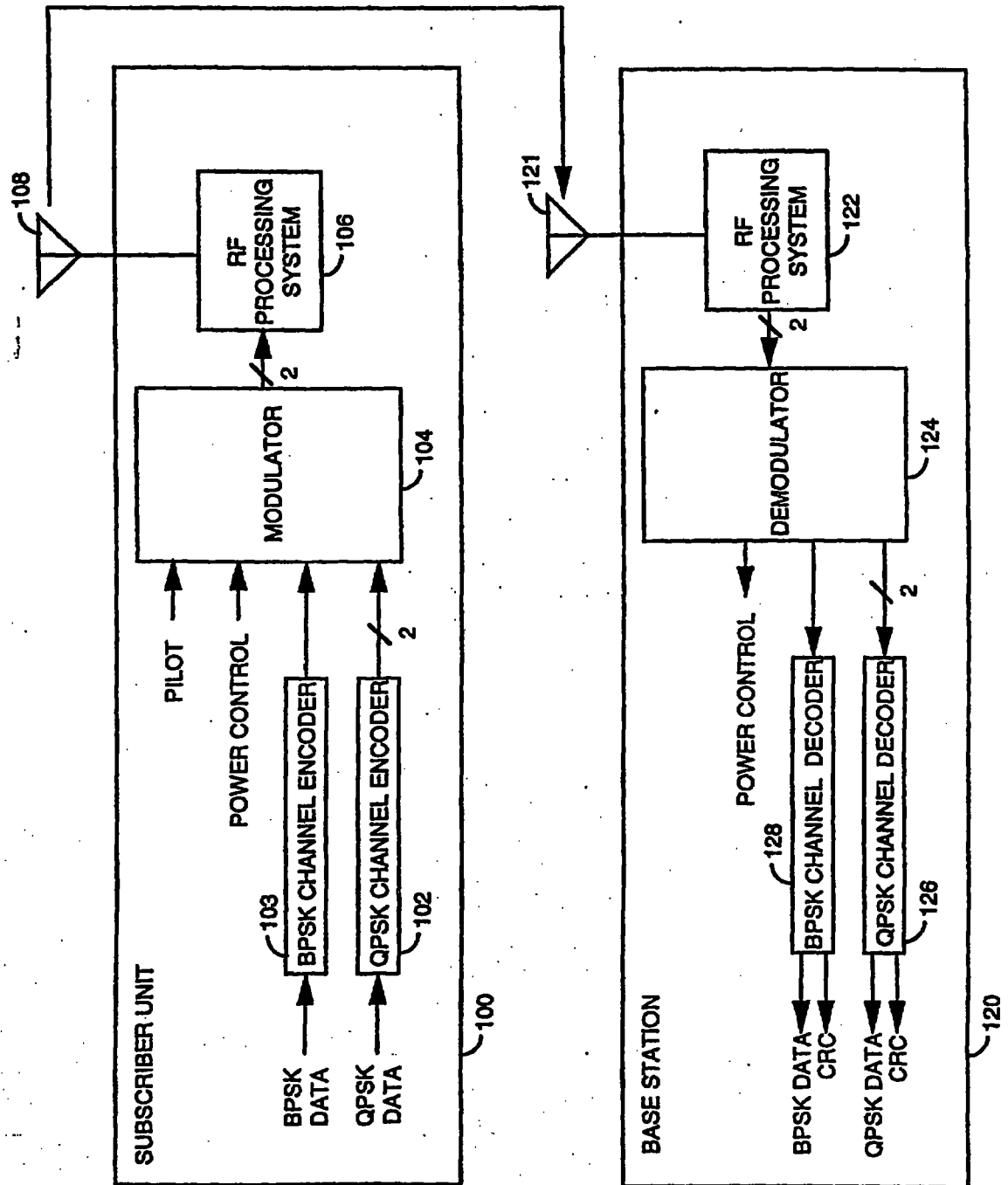


FIG. 2

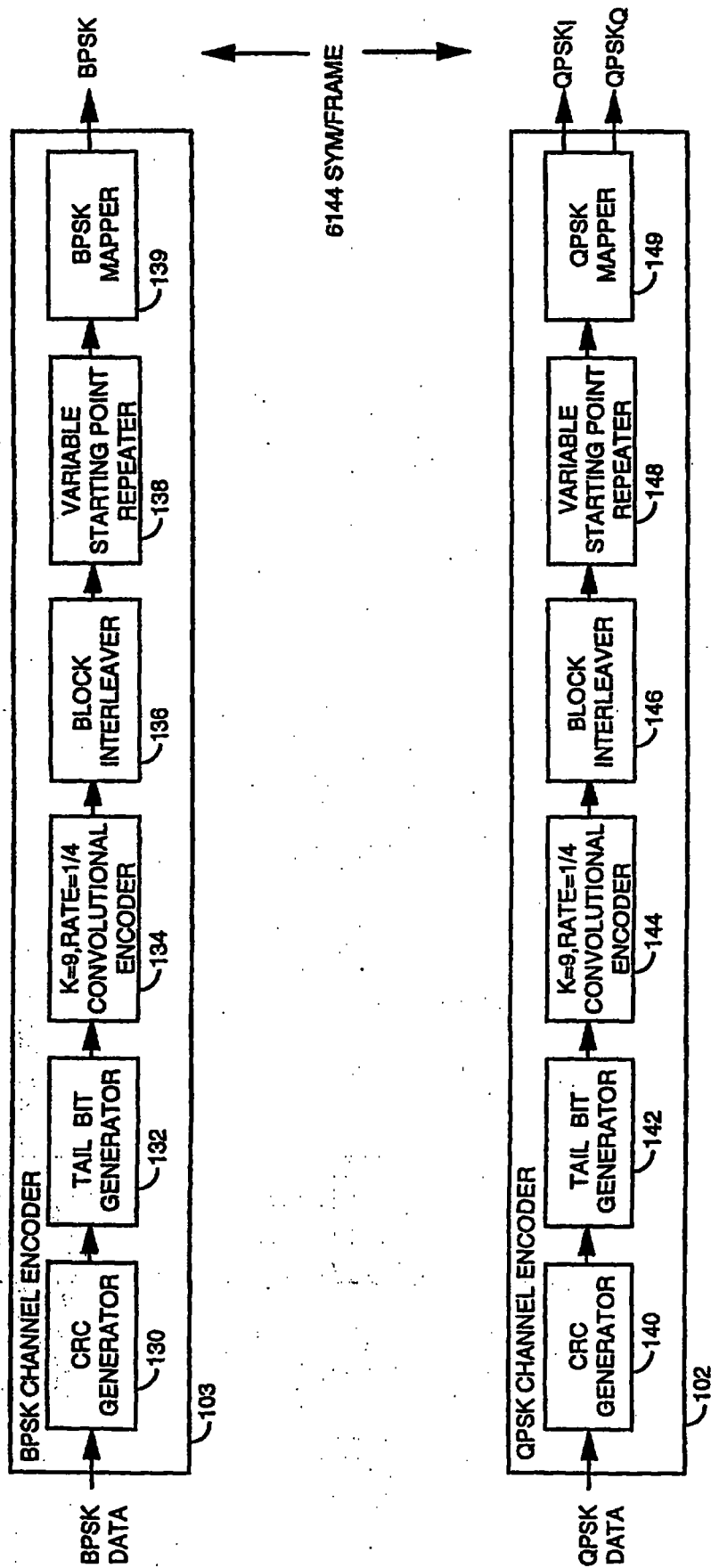
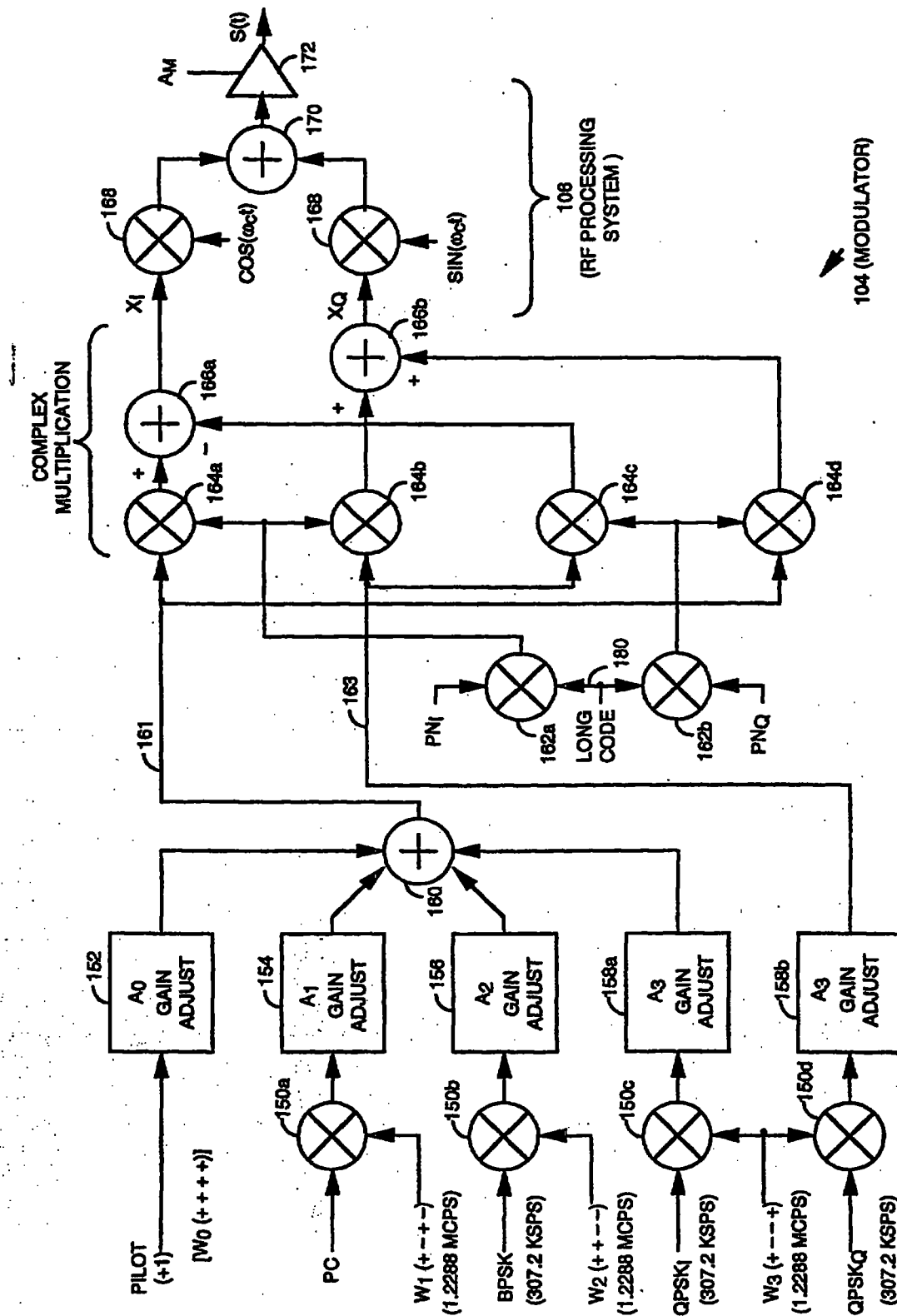


FIG. 3



104 (MODULATOR)

FIG. 4

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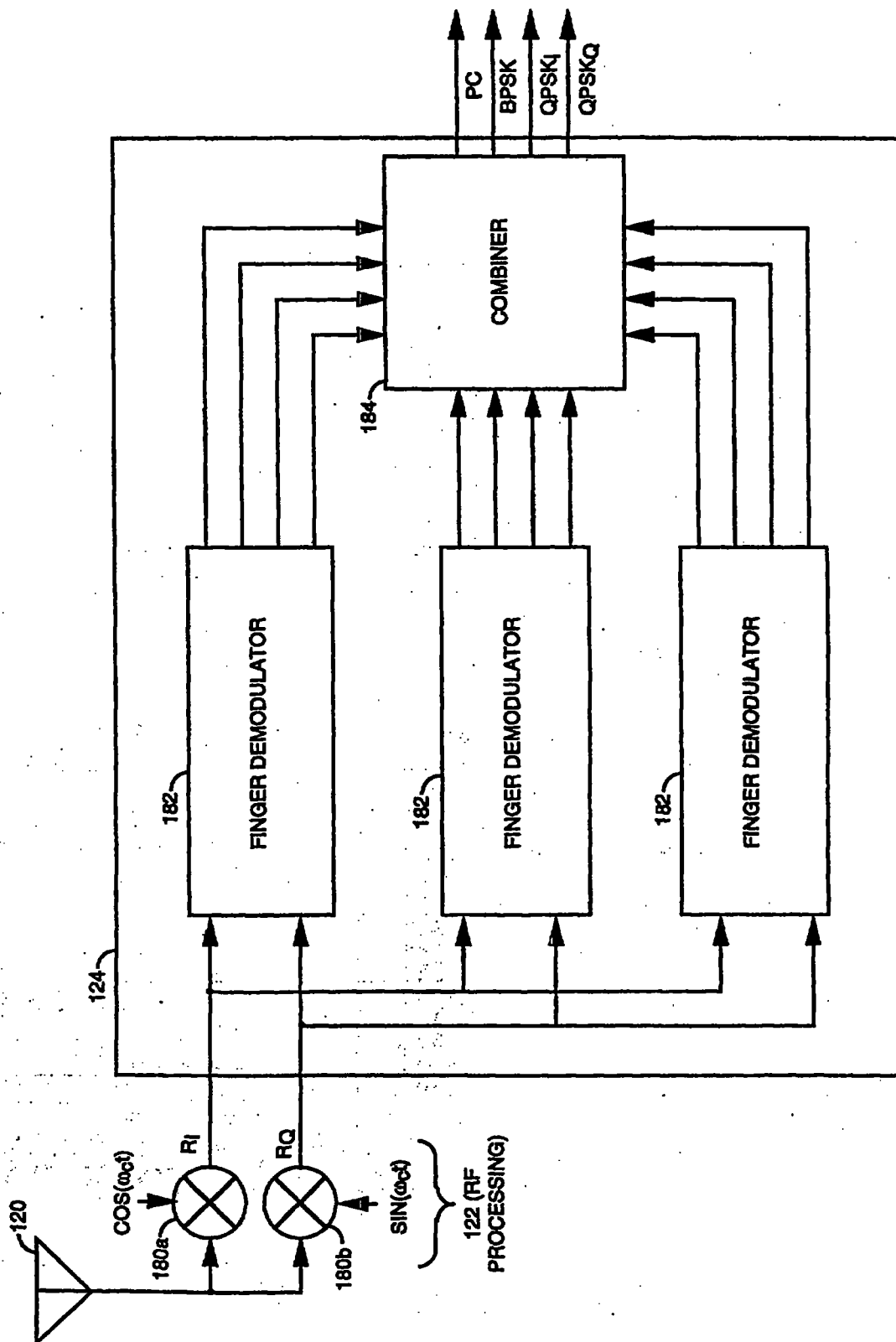


FIG. 5

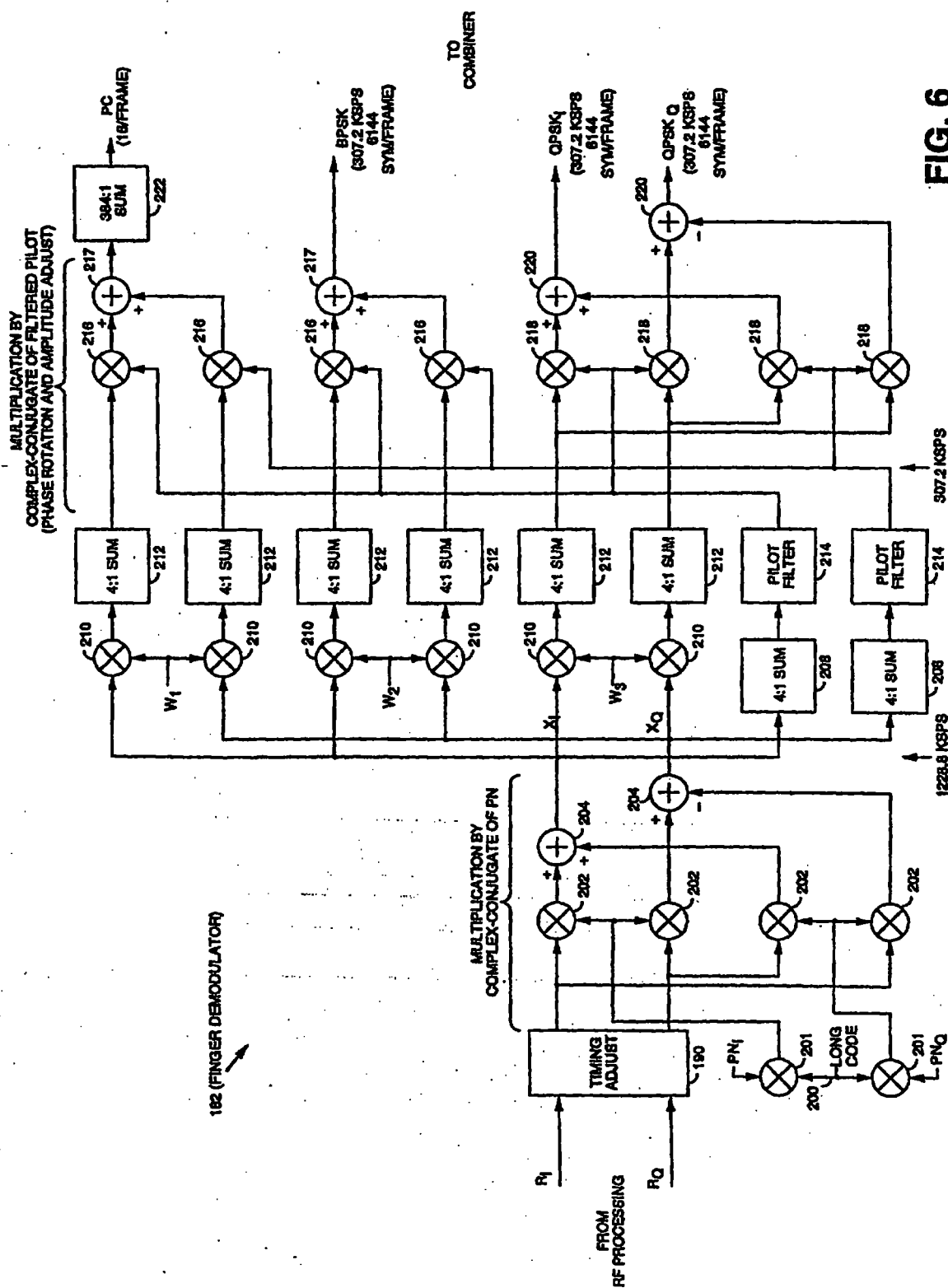


FIG. 6

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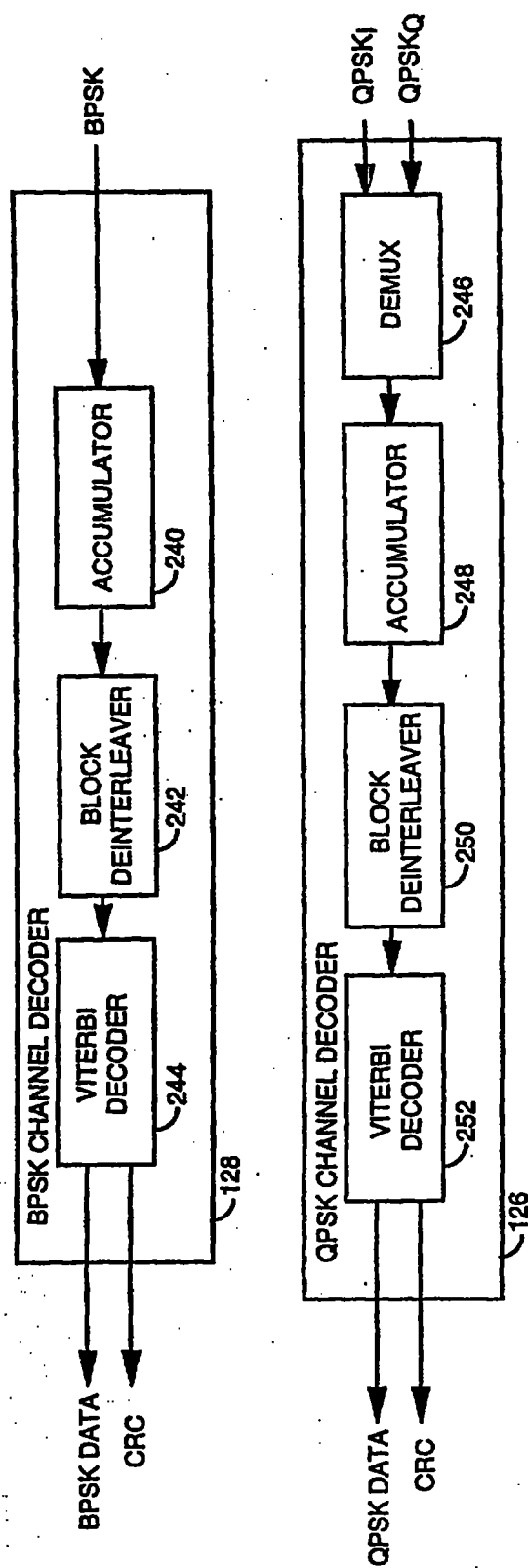


FIG. 7

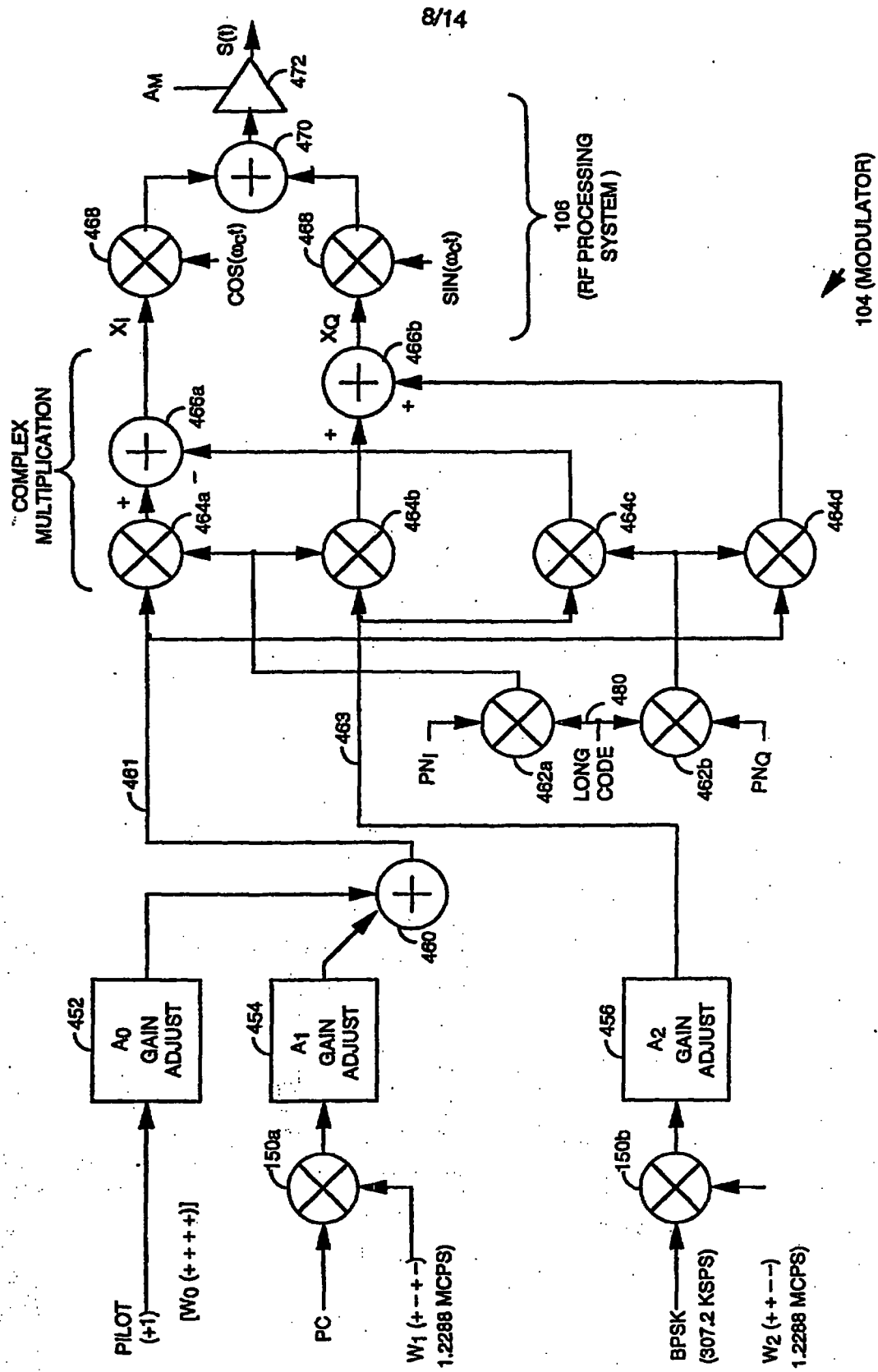


FIG. 8

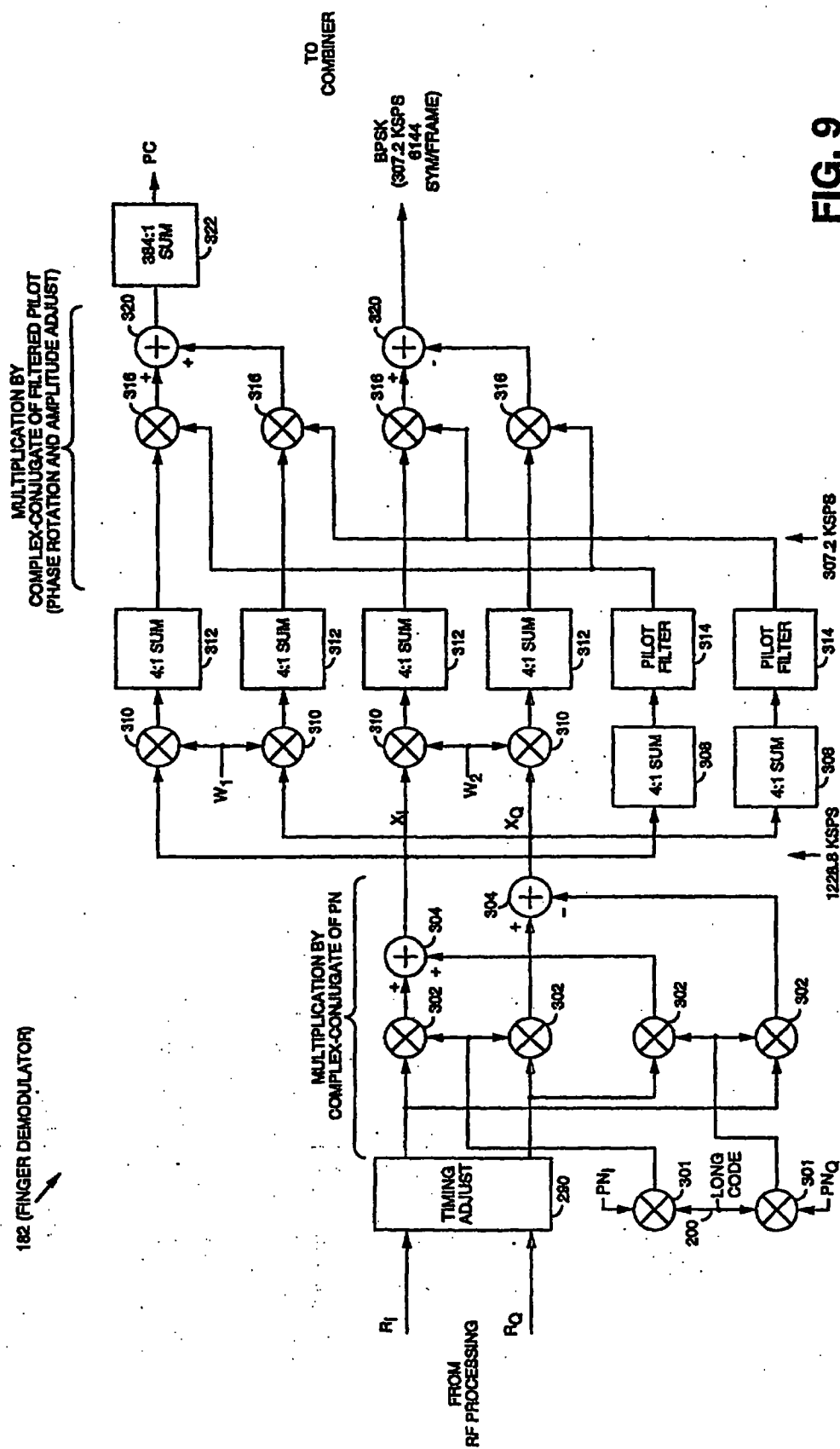


Fig. 9

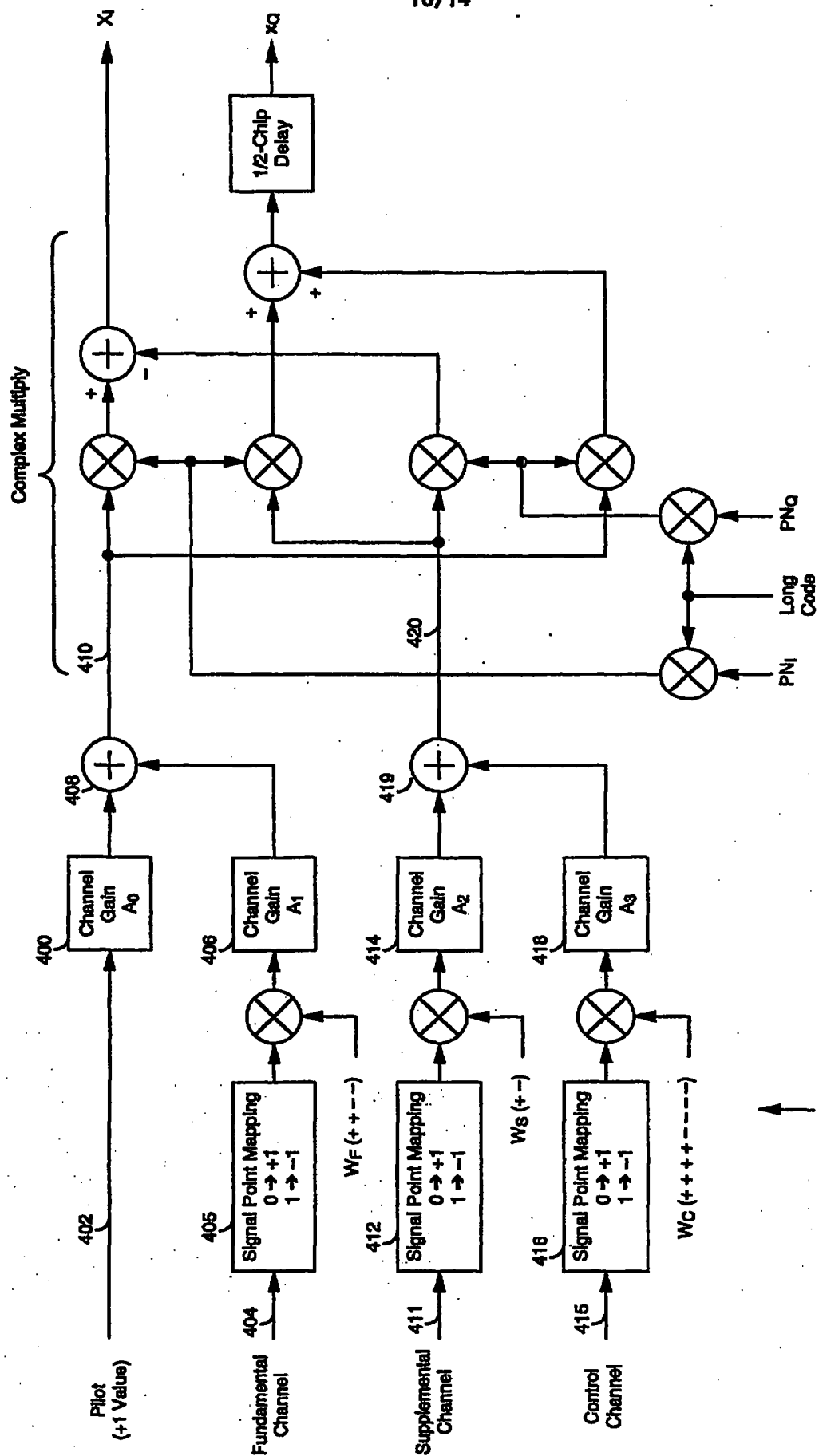


FIG. 10

± 1 Walsh Values

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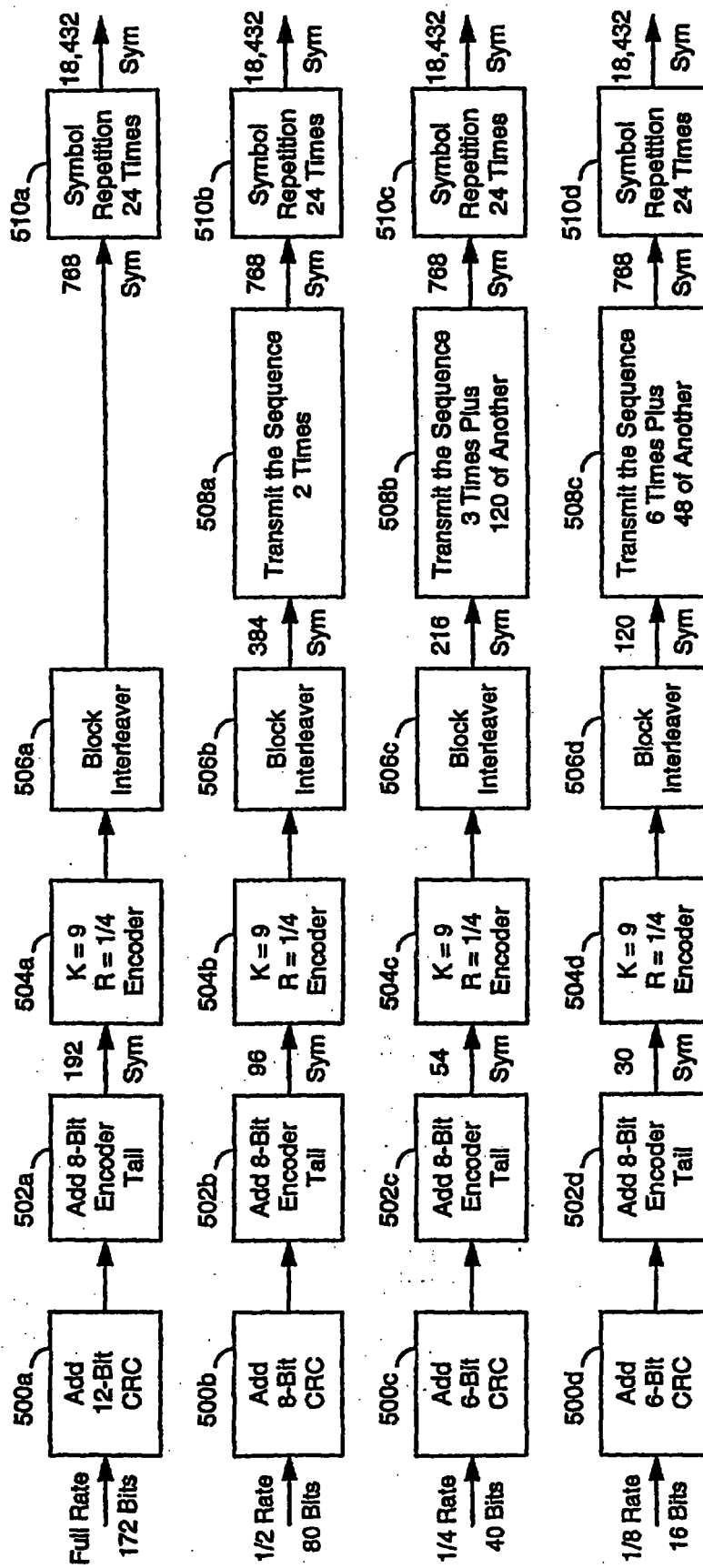


Fig. 11

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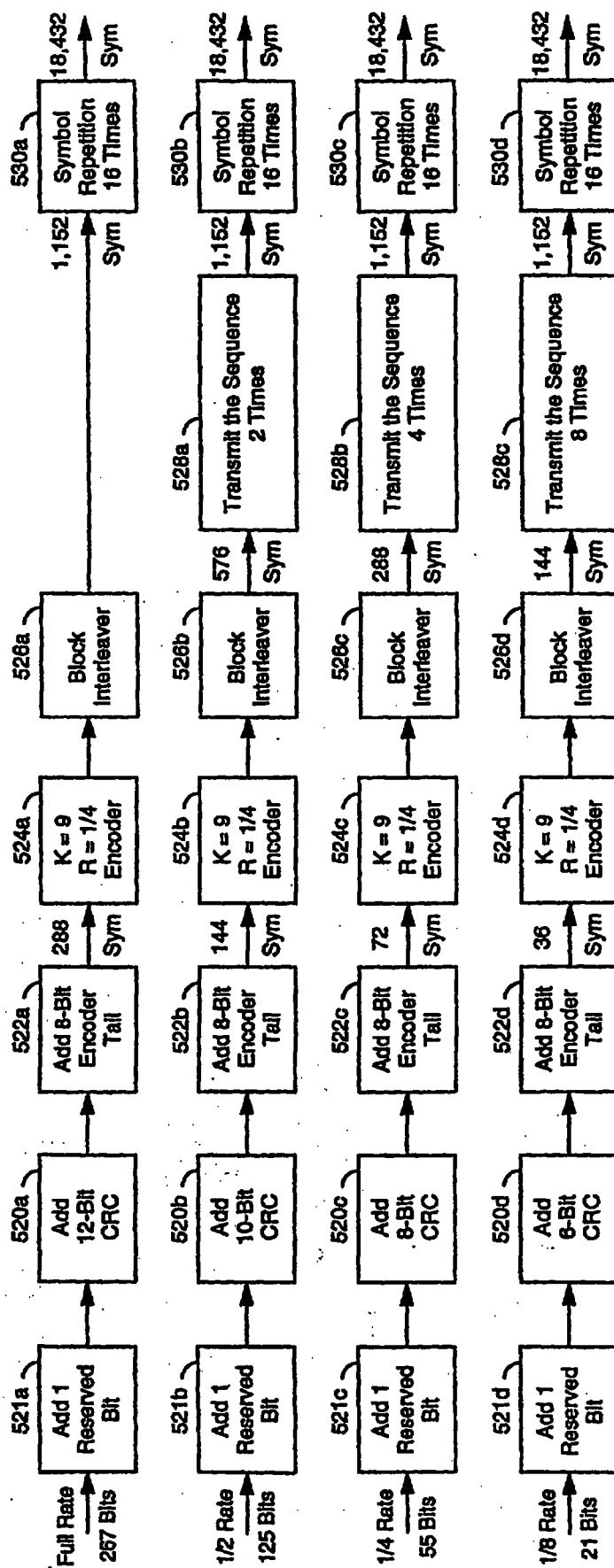


Fig. 12

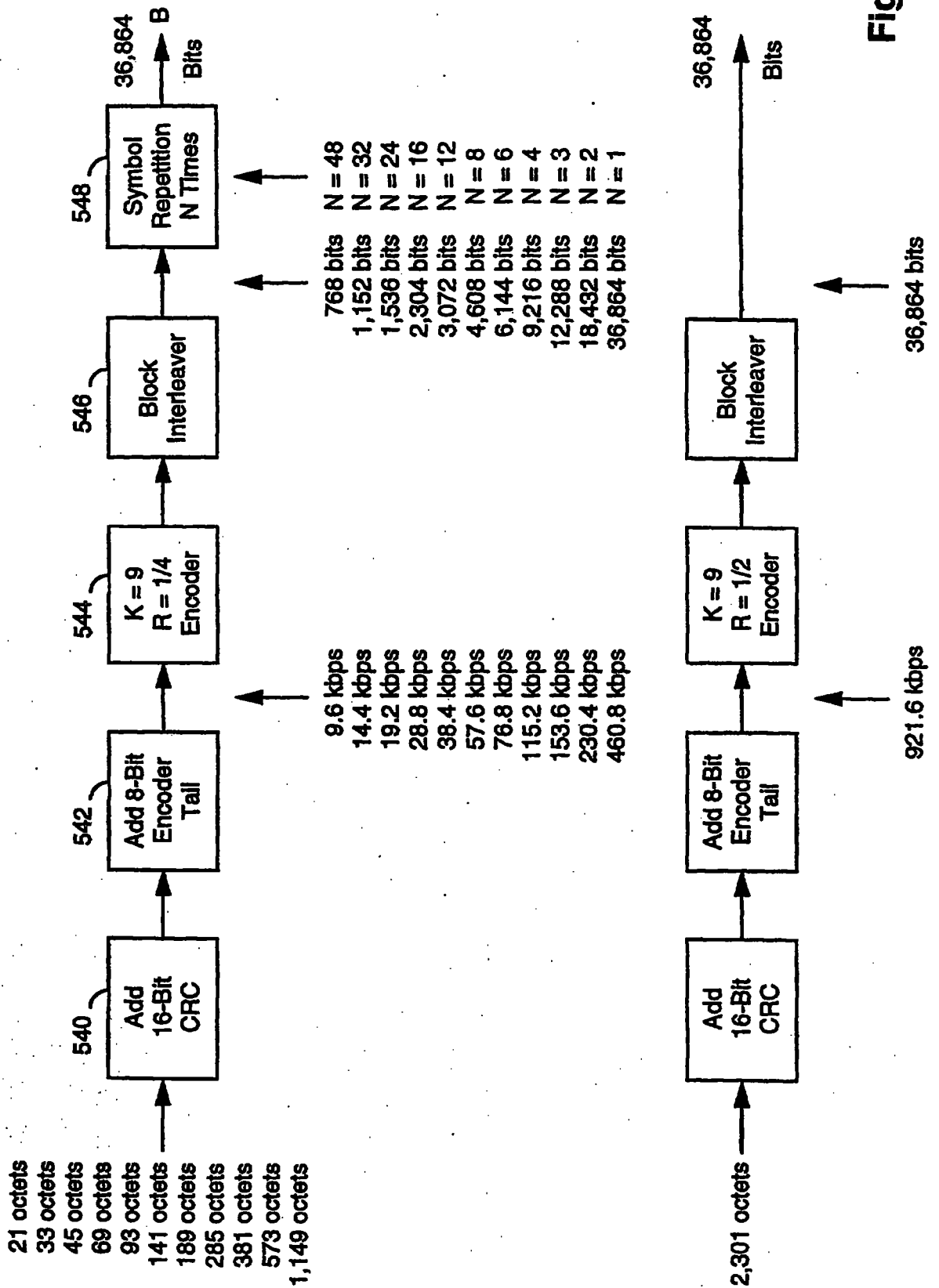


Fig. 13

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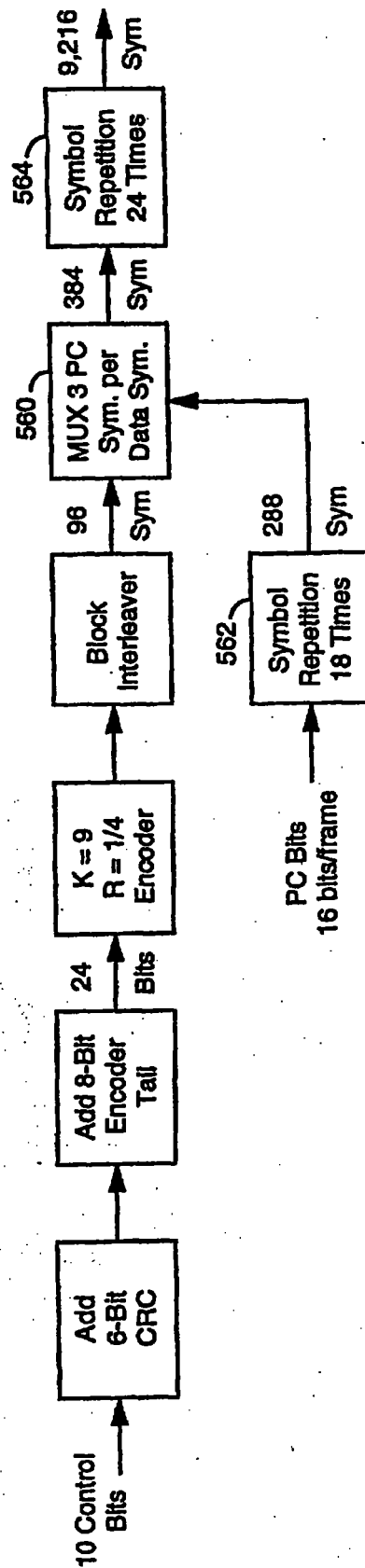


Fig. 14

CLAIMS

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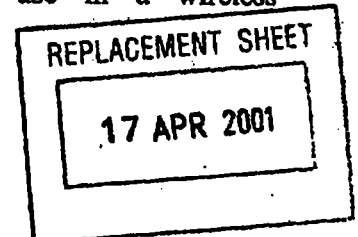
1. A subscriber unit or other transmitter for use in a wireless communication system, the subscriber unit comprising:

plural information sources of information data;

an encoder for encoding the information data;

plural control sources of control data; and

a modulator for modulating encoded information data and the control data from one or more of the plural control sources with respective different modulating codes for transmission on a carrier signal, wherein the modulator is arranged to combine encoded information data from one information source with the encoded control data before the same is output for transmission.



2. A subscriber unit as claimed in claim 1, wherein the control data comprises power control data and pilot data.

3. A subscriber unit as claimed in claim 2, wherein the modulator is operable to modulate the power control data with a modulation code.

4. A method for transmitting a pilot signal and a first channel signal comprising:

modulating a first channel signal using a first short Walsh cover having a duration of fewer than sixty-four chips per orthogonal waveform period to produce a modulated first channel signal; and

adding a modulated second channel signal to the pilot signal to produce a summed signal component;

multiplying the summed signal component and the modulated first channel signal by a complex PN code.

5. The method of claim 4 wherein the first short Walsh cover has a duration of two chips.

6. The method of claim 4 wherein the first short Walsh cover has a duration of four chips.

7. The method of claim 4 wherein the first short Walsh cover has a duration



2 of eight chips.

8. The method of claim 4 wherein the first short Walsh cover is equal to +,-.

2 9. The method of claim 4 wherein the first short Walsh cover is equal to
2 +,+,-,-.

2 10. The method of claim 4 wherein the first short Walsh cover is equal to +,-
2 ,+,-.

2 11. The method of claim 4 further comprising modulating a second channel
2 signal by a second short Walsh cover to produce the modulated second channel signal,
4 wherein the second short Walsh cover is orthogonal to the first short Walsh cover and
4 has a duration of fewer than sixty-four chips per orthogonal waveform period.

2 12. The method of claim 11 wherein the second short Walsh cover has a
2 duration of two chips.

2 13. The method of claim 11 wherein the second short Walsh cover has a
2 duration of four chips.

2 14. The method of claim 11 wherein the second short Walsh cover has a
2 duration of eight chips.

2 15. The method of claim 11 wherein the first short Walsh cover has a
2 different duration than the second short Walsh cover.

2 16. The method of claim 11 wherein the duration of the first short Walsh
2 cover is equal to the duration of the second short Walsh cover.

2 17. The method of claim 11 wherein the second short Walsh cover is equal to
2 +,-.

2 18. The method of claim 11 wherein the second short Walsh cover is equal to
2 +,+,-,-.

2 19. The method of claim 11 wherein the second short Walsh cover is equal to
2 +,-,+,-.

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20. The method of claim 4 wherein the first channel signal is a BPSK signal.

21. The method of claim 4 wherein the multiplying further comprises:

- 2 multiplying the summed signal component by an in-phase component of the complex PN code to produce a first complex product signal;
- 4 multiplying the modulated first channel signal by a quadrature-phase component of the complex PN code to produce a second complex product signal;
- 6 multiplying the summed signal component by a quadrature-phase component of the complex PN code to produce a third complex product signal;
- 8 multiplying the modulated first channel signal by an in-phase component of the complex PN code to produce a fourth complex product signal;
- 10 adding the first complex product signal to the second complex product signal to produce an in-phase complex product signal; and
- 12 adding the third complex product signal to the fourth complex product signal to produce a quadrature-phase complex product signal.

22. The method of claim 21 further comprising:

- 2 multiplying the in-phase complex product signal by an in-phase sinusoid; and
- 4 multiplying the quadrature-phase complex product signal by a quadrature-phase sinusoid.

23. A transmitter apparatus comprising:

- 2 a first channel multiplier configured to multiply a first channel signal by a first short Walsh cover having a duration of fewer than sixty-four chips per orthogonal
- 4 waveform period to produce a modulated first channel signal;
- a second channel multiplier configured to multiply a second channel signal by a
- 6 second short Walsh cover to produce a modulated second channel signal, wherein the second short Walsh cover is orthogonal to the first short Walsh cover and has a duration
- 8 of fewer than sixty-four chips per orthogonal waveform period;
- a first summer configured to add the modulated second channel signal to a pilot
- 10 signal to produce a summed signal component; and
- a complex multiplier configured to multiply the summed signal component and
- 12 the modulated first channel signal by a complex PN code.

24. The apparatus of claim 23 wherein the first short Walsh cover has a

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2 duration of two chips.

25. The apparatus of claim 23 wherein the first short Walsh cover has a
2 duration of four chips.

26. The apparatus of claim 23 wherein the first short Walsh cover has a
2 duration of eight chips.

27. The apparatus of claim 23 wherein the first short Walsh cover is equal to
2 +,-.

28. The apparatus of claim 23 wherein the first short Walsh cover is equal to
2 +,+,-,-.

29. The apparatus of claim 23 wherein the first short Walsh cover is equal to
2 +,-,+,-.

30. The apparatus of claim 23 wherein the first short Walsh cover has a
2 different duration than the second short Walsh cover.

31. The apparatus of claim 23 wherein the duration of the first short Walsh
2 cover is equal to the duration of the second short Walsh cover.

32. The apparatus of claim 23 wherein the second short Walsh cover has a
2 duration of two chips.

33. The apparatus of claim 23 wherein the second short Walsh cover has a
2 duration of four chips.

34. The apparatus of claim 23 wherein the second short Walsh cover has a
2 duration of eight chips.

35. The apparatus of claim 23 wherein the second short Walsh cover is equal
2 to +,-.

36. The apparatus of claim 23 wherein the second short Walsh cover is equal
2 to +,+,-,-.

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37. The apparatus of claim 23 wherein the second short Walsh cover is equal
2 to +,-,+,-.

38. The apparatus of claim 23 further comprising:
2 a first multiplier configured to multiply the summed signal component by an in-
phase component of the complex PN code to produce a first complex product signal;
4 a second multiplier configured to multiply the modulated first channel signal by
a quadrature-phase component of the complex PN code to produce a second complex
6 product signal;
a third multiplier configured to multiply the summed signal component by a
8 quadrature-phase component of the complex PN code to produce a third complex
product signal;
10 a fourth multiplier configured to multiply the modulated first channel signal by
an in-phase component of the complex PN code to produce a fourth complex product
12 signal;
a second summer configured to add the first complex product signal to the
14 second complex product signal to produce an in-phase complex product signal; and
a third summer configured to add the third complex product signal to the fourth
16 complex product signal to produce a quadrature-phase complex product signal.

39. The apparatus of claim 38 further comprising:
2 a first upconverter configured to multiply the in-phase complex product signal
by an in-phase sinusoid; and
4 a second upconverter configured to multiply the quadrature-phase complex
product signal by a quadrature-phase sinusoid.

40. A transmitter apparatus comprising:
2 means for modulating a first channel signal using a first short Walsh cover
having a duration of fewer than sixty-four chips per orthogonal waveform period to
4 produce a modulated first channel signal; and
means for adding a pilot signal to a modulated second channel signal to produce
6 a summed signal component;
means for multiplying the summed signal component and the modulated first
8 channel signal by a complex PN code.

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41. The transmitter of claim 40 further comprising means for multiplying the
2 summed signal component by a complex PN code to produce a complex product signal.

42. A method for demodulating a signal comprising:
2 multiplying an in-phase sample stream by a first short Walsh cover having a
duration of fewer than sixty-four chips per orthogonal waveform period to produce an
4 in-phase short Walsh despread symbol first channel stream;
multiplying a quadrature-phase sample stream by the first short Walsh cover to
6 produce a quadrature-phase short Walsh despread symbol first channel stream; and
adding the in-phase short Walsh despread symbol first channel stream to the
8 quadrature-phase short Walsh despread symbol first channel stream to produce a soft
decision first channel data stream.

43. The method of claim 42 further comprising:
2 summing the in-phase short Walsh despread symbol first channel stream based
on the duration of the first short Walsh cover; and
4 summing the quadrature-phase short Walsh despread symbol first channel stream
based on the duration of the first short Walsh cover.

44. The method of claim 42 further comprising:
2 multiplying the in-phase short Walsh despread symbol first channel stream by an
in-phase component of a pilot reference signal; and
4 multiplying the quadrature-phase short Walsh despread symbol first channel
stream by a quadrature-phase component of the complex pilot reference signal.

45. The method of claim 42 wherein the first short Walsh cover has a
2 duration of two chips.

46. The method of claim 42 wherein the first short Walsh cover has a
2 duration of four chips.

47. The method of claim 42 wherein the first short Walsh cover has a
2 duration of eight chips.

48. The method of claim 42 wherein the first short Walsh cover is equal to
2 +, -.

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49. The method of claim 42 wherein the first short Walsh cover is equal to
2 $+,+,-,-$.

50. The method of claim 42 wherein the first short Walsh cover is equal to
2 $+,+,-,-$.

51. The method of claim 42 further comprising:
2 multiplying the in-phase sample stream by a second short Walsh cover to
produce an in-phase short Walsh despread symbol second channel stream, wherein the
4 second short Walsh cover is orthogonal to the first short Walsh cover and has a duration
of fewer than sixty-four chips per orthogonal waveform period;
6 multiplying a quadrature-phase sample stream by the second short Walsh cover
to produce a quadrature-phase short Walsh despread symbol second channel stream; and
8 adding the in-phase short Walsh despread symbol second channel stream to the
quadrature-phase short Walsh despread symbol second channel stream to produce a soft
10 decision second channel data stream.

52. The method of claim 51 further comprising summing the soft decision
2 second channel stream to produce a summed soft decision second channel stream.

53. The method of claim 51 wherein the second short Walsh cover has a
2 duration of two chips.

54. The method of claim 51 wherein the second short Walsh cover has a
2 duration of four chips.

55. The method of claim 51 wherein the second short Walsh cover has a
2 duration of eight chips.

56. The method of claim 51 wherein the first short Walsh cover has a
2 different duration than the second short Walsh cover.

57. The method of claim 51 wherein the duration of the first short Walsh
2 cover is equal to the duration of the second short Walsh cover.

58. The method of claim 51 wherein the second short Walsh cover is equal to
2 $+,-$.

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59. The method of claim 51 wherein the second short Walsh cover is equal to
2 $+,-,-,-$.

60. The method of claim 51 wherein the second short Walsh cover is equal to
2 $+,-,+,-$.

61. A receiver apparatus comprising:
2 a first multiplier configured to multiply an in-phase sample stream by a first
short Walsh cover having a duration of fewer than sixty-four chips per orthogonal
4 waveform period to produce an in-phase short Walsh despread symbol first channel
stream;
6 a second multiplier configured to multiply a quadrature-phase sample stream by
the first short Walsh cover to produce a quadrature-phase short Walsh despread symbol
8 first channel stream; and
a first adder configured to add the in-phase short Walsh despread symbol first
10 channel stream to the quadrature-phase short Walsh despread symbol first channel
stream to produce a soft decision first channel data stream.

62. The apparatus of claim 61 further comprising:
2 a first summer configured to sum the in-phase short Walsh despread symbol first
channel stream based on the duration of the first short Walsh cover; and
4 a second summer configured to sum the quadrature-phase short Walsh despread
symbol first channel stream based on the duration of the first short Walsh cover.

63. The apparatus of claim 61 further comprising:
2 a third multiplier configured to multiply the in-phase short Walsh despread
symbol first channel stream by an in-phase component of a pilot reference signal; and
4 a fourth multiplier configured to multiply the quadrature-phase short Walsh
despread symbol first channel stream by a quadrature-phase component of the complex
6 pilot reference signal.

64. The apparatus of claim 61 wherein the first short Walsh cover has a
2 duration of two chips.

65. The apparatus of claim 61 wherein the first short Walsh cover has a

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2 duration of four chips.

66. The apparatus of claim 61 wherein the first short Walsh cover has a
2 duration of eight chips.

67. The apparatus of claim 61 wherein the first short Walsh cover is equal to
2 +,-.

68. The apparatus of claim 61 wherein the first short Walsh cover is equal to
2 +,+,-,-.

69. The apparatus of claim 61 wherein the first short Walsh cover is equal to
2 +,-,+,-.

70. The apparatus of claim 61 wherein the first short Walsh cover has a
2 different duration than the second short Walsh cover.

71. The apparatus of claim 61 wherein the duration of the first short Walsh
2 cover is equal to the duration of the second short Walsh cover.

72. The apparatus of claim 61 wherein the second short Walsh cover has a
2 duration of two chips.

73. The apparatus of claim 61 wherein the second short Walsh cover has a
2 duration of four chips.

74. The apparatus of claim 61 wherein the second short Walsh cover has a
2 duration of eight chips.

75. The apparatus of claim 61 wherein the second short Walsh cover is equal
2 to +,-.

76. The apparatus of claim 61 wherein the second short Walsh cover is equal
2 to +,+,-,-.

77. The apparatus of claim 61 wherein the second short Walsh cover is equal
2 to +,-,+,-.

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78. The apparatus of claim 61 further comprising:

2 a third multiplier configured to multiply the in-phase sample stream by a second
short Walsh cover to produce an in-phase short Walsh despread symbol second channel
4 stream, wherein the second short Walsh cover is orthogonal to the first short Walsh
cover and has a duration of fewer than sixty-four chips per orthogonal waveform period;

6 a fourth multiplier configured to multiply the quadrature-phase sample stream by
the second short Walsh cover to produce a quadrature-phase short Walsh despread
8 symbol second channel stream; and

a second adder configured to add the in-phase short Walsh despread symbol
10 second channel stream to the quadrature-phase short Walsh despread symbol second
channel stream to produce a soft decision second channel data stream.

79. The apparatus of claim 78 further comprising a summer configured to
2 sum the soft decision second channel stream to produce a summed soft decision second
channel stream

80. A receiver apparatus comprising:

2 means for multiplying an in-phase sample stream by a first short Walsh cover
having a duration of fewer than sixty-four chips per orthogonal waveform period to
4 produce an in-phase short Walsh despread symbol first channel stream;

means for multiplying a quadrature-phase sample stream by the first short Walsh
6 cover to produce a quadrature-phase short Walsh despread symbol first channel stream;
and

8 means for adding the in-phase short Walsh despread symbol first channel stream
to the quadrature-phase short Walsh despread symbol first channel stream to produce a
10 soft decision first channel data stream.

81. A method of transmitting a variable data rate signal comprising:

2 interleaving a frame of code symbols to produce a sequence of interleaved
symbols having a first predetermined number of symbols;

4 repeating the sequence of interleaved symbols one or more times; and

repeating a subset of the sequence of interleaved symbols, wherein the subset has
6 a second predetermined number of symbols, and wherein the second predetermined
number of symbols is less than the first predetermined number of symbols.

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82. The method of claim 90 wherein the first predetermined number of
2 symbols is 384.

83. The method of claim 90 wherein the first predetermined number of
2 symbols is 216.

84. The method of claim 90 wherein the first predetermined number of
2 symbols is 120.

85. The method of claim 90 wherein the first predetermined number of
2 symbols is 576.

86. The method of claim 90 wherein the first predetermined number of
2 symbols is 288.

87. The method of claim 90 wherein the first predetermined number of
2 symbols is 144.

88. The method of claim 90 wherein the second predetermined number of
2 symbols is 120.

89. The method of claim 90 wherein the first predetermined number of
2 symbols is 48.

90. A transmitter apparatus comprising:
2 an interleaver configured to interleave a frame of code symbols to produce a
sequence of interleaved symbols having a first predetermined number of symbols;
4 a repeater for repeating the sequence of interleaved symbols one or more times
and repeating a subset of the sequence of interleaved symbols, wherein the subset has a
6 second predetermined number of symbols, and wherein the second predetermined
number of symbols is less than the first predetermined number of symbols.

91. The apparatus of claim 90 wherein the first predetermined number of
2 symbols is 384.

92. The apparatus of claim 90 wherein the first predetermined number of
2 symbols is 216.

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93. The apparatus of claim 90 wherein the first predetermined number of
2 symbols is 120.

94. The apparatus of claim 90 wherein the first predetermined number of
2 symbols is 576.

95. The apparatus of claim 90 wherein the first predetermined number of
2 symbols is 288.

96. The apparatus of claim 90 wherein the first predetermined number of
2 symbols is 144.

97. The apparatus of claim 90 wherein the second predetermined number of
2 symbols is 120.

98. The apparatus of claim 90 wherein the first predetermined number of
2 symbols is 48.

99. A transmitter apparatus comprising:
2 means for interleaving a frame of code symbols to produce a sequence of
interleaved symbols having a first predetermined number of symbols; and
4 means for repeating the sequence of interleaved symbols one or more times and
for additionally repeating a subset of the sequence of interleaved symbols, wherein the
6 subset has a second predetermined number of symbols, and wherein the second
predetermined number of symbols is less than the first predetermined number of
8 symbols.

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CLAIMS:

1. A subscriber unit or other transmitter for use in a wireless communication system, the subscriber unit comprising:
 plural information sources of information data;
 an encoder for encoding the information data;
 plural control sources of control data; and
 a modulator for modulating encoded information data and the control data from one or more of the plural control sources with respective different modulating codes for transmission on a carrier signal, wherein the modulator is arranged to combine encoded information data from one information source with the encoded control data before the same is output for transmission.

2. A subscriber unit as claimed in claim 1, wherein the control data comprises power control data and pilot data.

3. A subscriber unit as claimed in claim 2, wherein the modulator is operable to modulate the power control data with a modulation code.

4. A subscriber unit as claimed in any preceding claim, wherein the modulation codes are Walsh codes.

5. A subscriber unit as claimed in claim 4, wherein the Walsh code used to modulate the information data from a first one of the information sources is longer than the Walsh code used to modulate the information data from a second one of the information sources.

6. A subscriber unit as claimed in claim 5, wherein the Walsh code used to modulate the encoded control is longer than the Walsh code used to modulate the data information data from the second one of the information sources.

7. A subscriber unit as claimed in claim 6, wherein the Walsh code used to modulate the encoded control data comprises eight chips, the Walsh code used to modulate the information data from the first one of the information sources comprises four chips, and the Walsh code used to

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6 modulate the information data from the second one of the information
sources comprises two chips.

2 8. A subscriber unit as claimed in any preceding claim, further
comprising a combiner for combining the modulated data from the
4 modulator with each other and with a spreading code for transmission on a
carrier signal.

2 9. A subscriber unit as claimed in claim 8, further comprising a
transmitter circuit for transmitting the carrier signal carrying the spread,
combined, modulated data.

2 10. A subscriber unit as claimed in any preceding claim, wherein
the encoder is arranged to effect a low code rate error correction and
sequence repetition to the information data.

4 11. A base station or other receiver for use in a wireless
communication system, the base station comprising:

6 a receiver for receiving a carrier signal and removing therefrom
encoded information data from plural information sources modulated with
8 respective different modulating codes and control data from plural control
sources with the one or more of the control data being modulated with a
10 respective different modulating code and with the encoded information data
from one information source being combined with the encoded control data;
12 a demodulator for demodulating the encoded information data and the
control data from their respective different modulating codes; and

14 a decoder for decoding the encoded information and control
data.

2 12. A method for transmitting control data, fundamental data, and
supplemental data from a first subscriber unit in a set of subscriber units to a
base station in communication with the set of subscriber units comprising:

4 a) modulating the supplemental data with a first Walsh
code;

- 6 b) modulating the fundamental data with a second Walsh
code; and
- 8 c) modulating the control data with a third Walsh code,
where
- 10 said first Walsh code is shorter than said second Walsh code, and said
second Walsh code is shorter than said third Walsh code.

13. The method as set forth in claim 12, wherein said first Walsh
2 code is comprised of two chips, said second Walsh code is comprised of four
chips, and said third Walsh code is comprised of eight chips.

14. The method as set forth in claim 12 or 13, further comprising a
2 pilot channel, wherein said pilot channel is summed with the fundamental
data, the supplemental data is summed with said control data.

15. A method of transmitting data from a subscriber unit for use in
2 a wireless communication system, the method comprising:

 acquiring information data from plural information sources;

4 encoding the information data;

 acquiring control data from plural control sources; and

6 modulating the encoded information data and the control data from
one or more of the plural control sources with respective different
8 modulating codes for transmission on a carrier signal, wherein the encoded
information data from one information source is combined with the
10 encoded control data before the same is output for transmission.

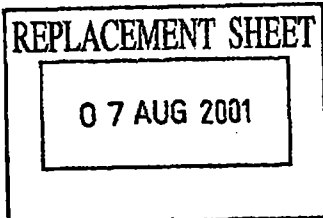
16. A method for transmitting a pilot signal and a first channel signal
2 comprising:
modulating a first channel signal using a first short Walsh cover having a
4 duration of fewer than sixty-four chips per orthogonal waveform period to
produce a modulated first channel signal; and
6 adding a modulated second channel signal to the pilot signal to produce
a summed signal component;
8 multiplying the summed signal component and the modulated first
channel signal by a complex PN code.
17. The method of claim 16 wherein the first short Walsh cover has a
2 duration of two chips.
18. The method of claim 16 wherein the first short Walsh cover has a
2 duration of four chips.
19. The method of claim 16 wherein the first short Walsh cover has a
2 duration of eight chips.
20. The method of claim 16 wherein the first short Walsh cover is equal to +,
2 -.
21. The method of claim 16 wherein the first short Walsh cover is equal to +,
2 +, -, -.
22. The method of claim 16 wherein the first short Walsh cover is equal to +,
2 -, +, -.
23. The method of claim 16 further comprising modulating a second channel
2 signal by a second short Walsh cover to produce the modulated second channel
signal, wherein the second short Walsh cover is orthogonal to the first short
4 Walsh cover and has a duration of fewer than sixty-four chips per orthogonal
waveform period.
24. The method of claim 23 wherein the second short Walsh cover has a
2 duration of two chips.
25. The method of claim 23 wherein the second short Walsh cover has a
2 duration of four chips.

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26. The method of claim 23 wherein the second short Walsh cover has a
2 duration of eight chips.
27. The method of claim 23 wherein the first short Walsh cover has a
2 different duration than the second short Walsh cover.
28. The method of claim 23 wherein the duration of the first short Walsh
2 cover is equal to the duration of the second short Walsh cover.
29. The method of claim 23 wherein the second short Walsh cover is equal to
2 +, -.
30. The method of claim 23 wherein the second short Walsh cover is equal to
2 +, +, -, -.
31. The method of claim 23 wherein the second short Walsh cover is equal to
2 +, -, +, -.
32. The method of claim 16 wherein the first channel signal is a BPSK signal.
33. The method of claim 16 wherein the multiplying further comprises:
2 multiplying the summed signal component by an in-phase component of
the complex PN code to produce a first complex product signal;
4 multiplying the modulated first channel signal by a quadrature-phase
component of the complex PN code to produce a second complex product
6 signal;
multiplying the summed signal component by a quadrature-phase
8 component of the complex PN code to produce a third complex product signal;
multiplying the modulated first channel signal by an in-phase
10 component of the complex PN code to produce a fourth complex product
signal;
12 subtracting the second complex product signal from the first complex
product signal to produce an in-phase complex product signal; and
14 adding the third complex product signal to the fourth complex product
signal to produce a quadrature-phase complex product signal.
34. The method of claim 33 further comprising:

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- 2 multiplying the in-phase complex product signal by an in-phase
sinusoid; and
- 4 multiplying the quadrature-phase complex product signal by a
quadrature-phase sinusoid.
35. A transmitter apparatus comprising:
- 2 a first channel multiplier configured to multiply a first channel signal by
a first short Walsh cover having a duration of fewer than sixty-four chips per
4 orthogonal waveform period to produce a modulated first channel signal;
a second channel multiplier configured to multiply a second channel
6 signal by a second short Walsh cover to produce a modulated second channel
signal, wherein the second short Walsh cover is orthogonal to the first short
8 Walsh cover and has a duration of fewer than sixty-four chips per orthogonal
waveform period;
- 10 a first summer configured to add the modulated second channel signal
to a pilot signal to produce a summed signal component; and
- 12 a complex multiplier configured to multiply the summed signal
component and the modulated first channel signal by a complex PN code.
36. The apparatus of claim 35 wherein the first short Walsh cover has a
2 duration of two chips.
37. The apparatus of claim 35 wherein the first short Walsh cover has a
2 duration of four chips.
38. The apparatus of claim 35 wherein the first short Walsh cover has a
2 duration of eight chips.
39. The apparatus of claim 35 wherein the first short Walsh cover is equal to
2 +, -.
40. The apparatus of claim 35 wherein the first short Walsh cover is equal to
2 +, +, -, -.
41. The apparatus of claim 35 wherein the first short Walsh cover is equal to
2 +, -, +, -.
42. The apparatus of claim 35 wherein the first short Walsh cover has a
2 different duration than the second short Walsh cover.



43. The apparatus of claim 35 wherein the duration of the first short Walsh
2 cover is equal to the duration of the second short Walsh cover.
44. The apparatus of claim 35 wherein the second short Walsh cover has a
2 duration of two chips.
45. The apparatus of claim 35 wherein the second short Walsh cover has a
2 duration of four chips.
46. The apparatus of claim 35 wherein the second short Walsh cover has a
2 duration of eight chips.
47. The apparatus of claim 35 wherein the second short Walsh cover is equal
2 to +, -.
48. The apparatus of claim 35 wherein the second short Walsh cover is equal
2 to +, +, -, -.
49. The apparatus of claim 35 wherein the second short Walsh cover is equal
2 to +, -, +, -.
50. The apparatus of claim 35 further comprising:
2 a first multiplier configured to multiply the summed signal component
by an in-phase component of the complex PN code to produce a first complex
4 product signal;
a second multiplier configured to multiply the modulated first channel
6 signal by a quadrature-phase component of the complex PN code to produce a
second complex product signal;
8 a third multiplier configured to multiply the summed signal component
by a quadrature-phase component of the complex PN code to produce a third
10 complex product signal;
a fourth multiplier configured to multiply the modulated first channel
12 signal by an in-phase component of the complex PN code to produce a fourth
complex product signal;
14 a second summer configured to subtract the second complex product
signal from the first complex product signal to produce an in-phase complex
16 product signal; and

18 a third summer configured to add the third complex product signal to the fourth complex product signal to produce a quadrature-phase complex product signal.

51. The apparatus of claim 50 further comprising:

2 a first upconverter configured to multiply the in-phase complex product signal by an in-phase sinusoid; and

4 a second upconverter configured to multiply the quadrature-phase complex product signal by a quadrature-phase sinusoid.

52. A transmitter apparatus comprising:

2 means for modulating a first channel signal using a first short Walsh cover having a duration of fewer than sixty-four chips per orthogonal waveform period to produce a modulated first channel signal; and

4 means for adding a pilot signal to a modulated second channel signal to produce a summed signal component;

6 means for multiplying the summed signal component and the modulated first channel signal by a complex PN code.

53. The transmitter of claim 52 further comprising means for multiplying the summed signal component by a complex PN code to produce a complex product signal.

54. An method for demodulating a signal comprising:

2 multiplying an in-phase sample stream by a first short Walsh cover having a duration of fewer than sixty-four chips per orthogonal waveform period to produce an in-phase short Walsh despread symbol first channel stream;

4 multiplying a quadrature-phase sample stream by the first short Walsh cover to produce a quadrature-phase short Walsh despread symbol first channel stream; and

6 adding the in-phase short Walsh despread symbol first channel stream to the quadrature-phase short Walsh despread symbol first channel stream to produce a soft decision first channel data stream.

55. The method of claim 54 further comprising:

2 summing the in-phase short Walsh despread symbol first channel stream based on the duration of the first short Walsh cover; and

- 4 ~~summing the quadrature-phase short Walsh despread symbol first~~
channel stream based on the duration of the first short Walsh cover.

56. The method of claim 54 further comprising:
2 multiplying the in-phase short Walsh despread symbol first channel
stream by an in-phase component of a pilot reference signal; and
4 multiplying the quadrature-phase short Walsh despread symbol first
channel stream by a quadrature-phase component of the complex pilot
6 reference signal.

57. The method of claim 54 wherein the first short Walsh cover has a
2 duration of two chips.

58. The method of claim 54 wherein the first short Walsh cover has a
2 duration of four chips.

59. The method of claim 54 wherein the first short Walsh cover has a
2 duration of eight chips.

60. The method of claim 54 wherein the first short Walsh cover is equal to +,
2 -.

61. The method of claim 54 wherein the first short Walsh cover is equal to +,
2 +, -, -.

62. The method of claim 54 wherein the first short Walsh cover is equal to +,
2 -, +, -.

63. The method of claim 54 further comprising:
2 multiplying the in-phase sample stream by a second short Walsh cover
to produce an in-phase short Walsh despread symbol second channel stream,
4 wherein the second short Walsh cover is orthogonal to the first short Walsh
cover and has a duration of fewer than sixty-four chips per orthogonal
6 waveform period;
multiplying a quadrature-phase sample stream by the second short
8 Walsh cover to produce a quadrature-phase short Walsh despread symbol
second channel stream; and

- 10 adding the in-phase short Walsh despread symbol second channel
stream to the quadrature-phase short Walsh despread symbol second channel
12 stream to produce a soft decision second channel data stream.
64. The method of claim 63 further comprising summing the soft decision
2 second channel stream to produce a summed soft decision second channel
stream.
65. The method of claim 63 wherein the second short Walsh cover has a
2 duration of two chips.
66. The method of claim 63 wherein the second short Walsh cover has a
2 duration of four chips.
67. The method of claim 63 wherein the second short Walsh cover has a
2 duration of eight chips.
68. The method of claim 63 wherein the first short Walsh cover has a
2 different duration than the second short Walsh cover.
69. The method of claim 63 wherein the duration of the first short Walsh
2 cover is equal to the duration of the second short Walsh cover.
70. The method of claim 63 wherein the second short Walsh cover is equal to
2 +, -.
71. The method of claim 63 wherein the second short Walsh cover is equal to
2 +, +, -, -.
72. The method of claim 63 wherein the second short Walsh cover is equal to
2 +, -, +, -.
73. A receiver apparatus comprising:
2 a first multiplier configured to multiply an in-phase sample stream by a
first short Walsh cover having a duration of fewer than sixty-four chips per
4 orthogonal waveform period to produce an in-phase short Walsh despread
symbol first channel stream;

- 6 a second multiplier configured to multiply a quadrature-phase sample
stream by the first short Walsh cover to produce a quadrature-phase short
8 Walsh despread symbol first channel stream; and
a first adder configured to add the in-phase short Walsh despread
10 symbol first channel stream to the quadrature-phase short Walsh despread
symbol first channel stream to produce a soft decision first channel data stream.
74. The apparatus of claim 73 further comprising:
2 a first summer configured to sum the in-phase short Walsh despread
symbol first channel stream based on the duration of the first short Walsh
4 cover; and
a second summer configured to sum the quadrature-phase short Walsh
6 despread symbol first channel stream based on the duration of the first short
Walsh cover.
75. The apparatus of claim 73 further comprising:
2 a third multiplier configured to multiply the in-phase short Walsh
despread symbol first channel stream by an in-phase component of a pilot
4 reference signal; and
a fourth multiplier configured to multiply the quadrature-phase short
6 Walsh despread symbol first channel stream by a quadrature-phase component
of the complex pilot reference signal.
76. The apparatus of claim 73 wherein the first short Walsh cover has a
2 duration of two chips.
77. The apparatus of claim 73 wherein the first short Walsh cover has a
2 duration of four chips.
78. The apparatus of claim 73 wherein the first short Walsh cover has a
2 duration of eight chips.
79. The apparatus of claim 73 wherein the first short Walsh cover is equal to
2 +, -.
80. The apparatus of claim 73 wherein the first short Walsh cover is equal to
2 +, +, -, -.

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81. The apparatus of claim 73 wherein the first short Walsh cover is equal to
2 +, -, +, -.
82. The apparatus of claim 73 wherein the first short Walsh cover has a
2 different duration than the second short Walsh cover.
83. The apparatus of claim 73 wherein the duration of the first short Walsh
2 cover is equal to the duration of the second short Walsh cover.
84. The apparatus of claim 73 wherein the second short Walsh cover has a
2 duration of two chips.
85. The apparatus of claim 73 wherein the second short Walsh cover has a
2 duration of four chips.
86. The apparatus of claim 73 wherein the second short Walsh cover has a
2 duration of eight chips.
87. The apparatus of claim 73 wherein the second short Walsh cover is equal
2 to +, -.
88. The apparatus of claim 73 wherein the second short Walsh cover is equal
2 to +, +, -, -.
89. The apparatus of claim 73 wherein the second short Walsh cover is equal
2 to +, -, +, -.
90. The apparatus of claim 73 further comprising:
2 a third multiplier configured to multiply the in-phase sample stream by
a second short Walsh cover to produce an in-phase short Walsh despread
4 symbol second channel stream, wherein the second short Walsh cover is
orthogonal to the first short Walsh cover and has a duration of fewer than sixty-
6 four chips per orthogonal waveform period;
a fourth multiplier configured to multiply the quadrature-phase sample
8 stream by the second short Walsh cover to produce a quadrature-phase short
Walsh despread symbol second channel stream; and
10 a second adder configured to subtract the quadrature-phase short Walsh
despread symbol second channel stream from the in-phase short Walsh

12 ~~despread symbol second channel stream to produce a soft decision second~~
channel data stream.

91. The apparatus of claim 90 further comprising a summer configured to
2 sum the soft decision second channel stream to produce a summed soft decision
second channel stream.

92. A receiver apparatus comprising:

2 means for multiplying an in-phase sample stream by a first short Walsh
cover having a duration of fewer than sixty-four chips per orthogonal
4 waveform period to produce an in-phase short Walsh despread symbol first
channel stream;

6 means for multiplying a quadrature-phase sample stream by the first
short Walsh cover to produce a quadrature-phase short Walsh despread symbol
8 first channel stream; and

means for adding the in-phase short Walsh despread symbol first
10 channel stream to the quadrature-phase short Walsh despread symbol first
channel stream to produce a soft decision first channel data stream.

93. A method of transmitting a variable data rate signal comprising:

2 interleaving a frame of code symbols to produce a sequence of
interleaved symbols having a first predetermined number of symbols;

4 repeating the sequence of interleaved symbols one or more times; and

repeating a subset of the sequence of interleaved symbols, wherein the
6 subset has a second predetermined number of symbols, and wherein the second
predetermined number of symbols is less than the first predetermined number
8 of symbols.

94. The method of claim 93 wherein the first predetermined number of
2 symbols is 384.

95. The method of claim 93 wherein the first predetermined number of
2 symbols is 216.

96. The method of claim 93 wherein the first predetermined number of
2 symbols is 120.

97. The method of claim 93 wherein the first predetermined number of
2 symbols is 576.

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98. The method of claim 93 wherein the first predetermined number of
2 symbols is 288.
99. The method of claim 93 wherein the first predetermined number of
2 symbols is 144.
100. The method of claim 93 wherein the second predetermined number of
2 symbols is 120.
101. The method of claim 93 wherein the first predetermined number of
2 symbols is 48.
102. A transmitter apparatus comprising:
2 an interleaver configured to interleave a frame of code symbols to
produce a sequence of interleaved symbols having a first predetermined
4 number of symbols;
a repeater for repeating the sequence of interleaved symbols one or more
6 times and repeating a subset of the sequence of interleaved symbols, wherein
the subset has a second predetermined number of symbols, and wherein the
8 second predetermined number of symbols is less than the first predetermined
number of symbols.
103. The apparatus of claim 102 wherein the first predetermined number of
2 symbols is 384.
104. The apparatus of claim 102 wherein the first predetermined number of
2 symbols is 216.
105. The apparatus of claim 102 wherein the first predetermined number of
2 symbols is 120.
106. The apparatus of claim 102 wherein the first predetermined number of
2 symbols is 576.
107. The apparatus of claim 102 wherein the first predetermined number of
2 symbols is 288.

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108. The apparatus of claim 102 wherein the first predetermined number of
2 symbols is 144.
109. The apparatus of claim 102 wherein the second predetermined number
2 of symbols is 120.
110. The apparatus of claim 102 wherein the first predetermined number of
2 symbols is 48.
111. A transmitter apparatus comprising:
- 2 means for interleaving a frame of code symbols to produce a sequence of
interleaved symbols having a first predetermined number of symbols; and
- 4 means for repeating the sequence of interleaved symbols one or more
times and for additionally repeating a subset of the sequence of interleaved
6 symbols, wherein the subset has a second predetermined number of symbols,
and wherein the second predetermined number of symbols is less than the first
8 predetermined number of symbols.