

Description

The present invention relates to restoring and clocking pulse width modulated data in a data recording channel during either writing or reading of the data.

Pulse width modulated data is a form of data where transitions in the representation of data on a storage medium correspond to a bit of data, either a "1" or a "0". No transition thus corresponds to a "0" or a "1" respectively. Since the condition of no transition carries information, it is necessary to use a clock signal to define data cell width in order to detect how many no transitions have occurred between transitions. This clock signal must be synchronized with the data-in signal, and this is usually done with a phase-lock loop. A good example is taught in U.S.-A 3,804,992 entitled "Digital Time Sampling Phase Comparator With Noise Rejection," issued to Fiorino et al.

In a typical environment, magnetic recording for example, the pulse width modulated recordings tend to be uniform, i.e. have similar precision and definition throughout a recording. The data transitions tend to shift in the same direction and in small amounts relative to the clock and thus the phase-lock loop can control a clock to track the time shifts of the data-in signal.

In optical storage systems, many factors lead to variations in the size of the representation of data in a single recording whether the representation is the leading and trailing edges of a pit which reflects light, or the size of the area of changed magnetic or opto-magnetic properties. As a result, the transitions in pulse width modulated data may move a significant amount due to recording properties. This recording shift has nothing to do with the information content of the data.

The recording shift of the leading and trailing edge data transitions is in opposite directions. In effect the pulse width has changed due to variations in recording phenomenon rather than due to changes in timing or information. These unintended pulse width variations make it difficult for the phase-lock loop to correctly synchronize the clock to the data and thus align timing windows, i.e. data cells, used in a recording channel to decode the data from the transitions.

Some of the potential factors causing the leading and trailing edges to shift significantly in opposite directions are variable laser power, focus error, tracking error, media composition, laser dwell time and media sensitivity. Variations in these factors lead to timing variations in both the leading edge of the pit or mark and the trailing edge of the pit. If laser power is too great, or the media is slightly more sensitive than expected, the leading edge of the pit may be encountered sooner than expected because the pit is larger than desired. Under the same condition of the media,

the trailing edge of the pit may be encountered later than expected, thus leading to timing difficulties in detection of both the leading and trailing edges. Merely moving the timing windows (data cells) one direction or the other by adjusting the clock in the phase lock loop can not compensate for these unintended pulse width variations and can lead to a larger number of errors in detecting the data.

EP-A-0 213 623 discloses an optical disc apparatus provided with a record correcting system for controlling the variation in the record data length. The data is represented by the timing of leading and/or trailing edges of pulses which are compared with clock pulses in order to control the laser beam power when the recorded data pulse width is different from a predetermined width. This apparatus does not allow to dynamically correct pulse width of optically recorded data by using a phase locked loop. This document is cited according to Article 54.3 of the European Patent Convention.

Therefore, according to this invention, it is provided a method for correcting width distortions in a pulse width modulated data of the type in which transitions in the representation of data correspond to bits of the first polarity whereas no transitions correspond to bits of the second polarity, and wherein bits of data are recovered from the timing of leading and trailing edges of pulses, the method comprising the steps of detecting the leading edges of data pulses; detecting the trailing edges of data pulses; providing signals representative of the detected leading and trailing edges; generating a clock signal in response to the leading and trailing edge signals, the clock signal following the shifts of the edge signals, that occur in the same direction, in order to provide an expected time for the detection of said edges; measuring the amount of shift of the actual leading and trailing edge time from the expected leading and trailing edge time as established by the preceding step; and shifting the relative timing between leading and trailing edge signals in response to the width correction signal to restore the width of the data pulses to the expected width between the leading and trailing edge signals.

To accomplish the restoring function, a phase locked loop is used to establish the expected positions for the leading and trailing edge transitions of the pulse. With these expected transitions defined, phase differential logic detects variations of the actual pulse width from the expected pulse width. Feedback of this variation information is used to adjust the position of the leading and trailing edge signals to restore the pulse width to the expected pulse width.

In one preferred embodiment, a first single shot multivibrator is triggered on the leading edge of a pulse, and a second single shot multivibrator is triggered on the trailing edge of a pulse. A width error is generated as the summation of alternate speed-up and slow-down signals from a conventional phase-

locked loop used in data detection. The error signal is then used to vary the timing of one of the single shots operating on the leading or trailing edge of the pulse to define the pulse edges in the phase lock loop.

In a further preferred embodiment, leading and trailing edge transitions are supplied to a counter which counts clock pulses between leading and trailing edge transitions. The number of expected pulses is subtracted from the actual number of pulses counted. This value is filtered by a low pass filter to average the value over several leading to trailing edge transitions. The filtered value is then supplied to a single shot as in the above embodiment or supplied to plus and a minus phase shifters which provides phase shifts for use by separate leading and trailing edge data recovery clocks.

In yet a further preferred embodiment, data is effectively restored as it is written. Using a read while write technique to detect recording shift of data being written on the media, the phase of the data being written is intentionally shifted in the opposite direction of detected recording shift. Thus, when data is read, it will appear not to be shifted. This preshifting is accomplished by detecting the leading edges and trailing edges of the data while it is being written, and comparing them to the timing of the actual data before it is written. The preshifting is then performed as a function of that comparison.

Pre-restoration of the width of data leads to greater precision in the positioning of leading and trailing edges of data representations on recording media. The physical density of such edges on the media may then be increased because of associated timing tolerance reduction. When coupled with the improvement in reading reliability given by width restoring data while reading, linear bit densities realizable are almost double that of conventional representation of bits by pits or spots.

More particularly, the object of the invention is to provide a method and a device as defined in claims 1 and 8 respectively.

Fig. 1 is a detailed block diagram of a phase locked loop with width restoration in accordance with the present invention,

Fig. 2A is a detailed block diagram of an alternate circuit for generating the width correction signals in accordance with the present invention,

Figs. 2B and 2C are detailed block diagrams indicating how width correction signals generated by the circuits in Fig. 2A could be connected to and used in the circuits of Fig. 1.

Fig. 3 is a detailed block diagram of a read while write circuit for reading data while it is written and providing feedback to minimize recording shift resulting from writing the data.

In Fig. 1, data read from a pulse width modulated recorded signal on a recording media is provided on line 10 to NAND gate 12. The inverse of the data is

also provided on line 14 to NAND gate 16. NAND's 12 and 16 are enabled to pass the data signals by flip flop 32. Thus only one NAND is enabled at a time; NAND 12 is enabled to pass the leading edge and NAND 16 is enabled to pass the trailing edge.

The NAND gates 12 and 16 are coupled to single shots 18 and 20 respectively. The trailing edge of the signal from the single shots is passed by NOR 28 to trigger flip flop 32. In effect the single shots delay both the leading and trailing edge signals before those signals trigger the flip flop. Further since single shot 20 is variable in the duration of its pulse, the timing between the leading and trailing edges may be adjusted. This function will be used as explained later to make the width restoration.

The complimentary outputs on lines 34 and 36 of flip-flop 32 are provided to a second output flip flop 40. This flip flop provides complimentary data out signals on lines 42 and 44 and is triggered by the data clock out signal to receive the contents from flip flop 32. The data clock out signal is generated by the phase locked loop which will now be described.

The output 28 of NOR 26 is also provided to a phase detector 50 which compares the phase timing differences of data on input line 28 and timing signals from a timing clock signal on line 68 from voltage controlled oscillator (VCO 66). Phase detector 50 provides a speed-up signal on line 56 and a slow-down signal on line 58 to a charge pump 62. The charge pump passes more or less current to filter 64 in accordance with the speed-up or slow-down signals respectively. Filter 64 accumulates and averages the speed up and slow down signals; it contains a capacitor to convert the current to a voltage. The voltage out of filter 64 controls the frequency of the signal from VCO 66.

This configuration of elements 50, 62, 64 and 66 is well known as a charge-pump phase locked loop. The loop is a reference means generating a data clock signal 68 to track concurrent time shifts by the leading and trailing edge transitions in the data signal. Data clock out on line 68 provides a trigger for flip flop 40. The data out signals and the data clock signal are used by decoding circuits (not shown) to convert the pulse width modulated data out signal into binary data.

To accomplish width restoration, the speed up and slow down signals on lines 56 and 58, respectively, are passed to a differential phase detector 60. The function of this detector is to measure the amount of recording shift of the actual leading and trailing edge timing from the expected leading and trailing edge timing as established by the phase locked loop. As explained earlier recording shift will cause the edges to move in opposite directions relative to the expected edge positions. In effect the differential phase detector 60 is detecting unintended width variations in the pulse width modulated data.

Data-in line 10, and its complement on line 14 in conjunction with the AND and NOR gates alternate the polarity sense of the speed up and slow down signals to the summing junction 82. For each speed up and slow down pulse, the polarity is controlled by whether the data indicates it was due to a leading or trailing edge. The difference of each, then is indicative of the amount of recording shift between actual leading and trailing edge and expected leading and trailing edge.

Viewed as detection of pulse width variation AND's 70 and 72 with NOR 78 will pass the speed up and slow down pulses to difference amplifier 82 when the pulse is wider than expected. AND's 74 and 76 with NOR 80 do the same thing when the pulse is narrower than expected. Since the difference amplifier receives the speed up and slow down signals serially there is no difference function. Amplifier 82 acts to pass signals from NOR 78 and to invert and pass signals from NOR 80. The duration of the speed up and slow down signals control the duration of current flow from the charge pump 88 to the filter 90. In effect the duration of the speed up and slow down signals for a number of leading and trailing edges is summed and averaged by filter 90 to produce a signal that is a measure of the unintended width variation in the recorded pulses.

Single shots 18 and 20 nominally are adjusted to 1/2 clock cell time (1/2 the difference in time between consecutive data clock out pulses) for proper phase comparison by phase detector 50. Low pass filter 90 provides a control voltage on a line 92 to single shot 20. The control voltage effectively controls the timing of single shot 20 in response to the complement of the data-in signal on line 14 provided through NAND gate 16. In other words, the phase locked loop keeps the leading edge in proper phase with the data clock out signal and the width correction signal from filter 90 to single shot 20 causes the single shot to move the trailing edge signal into proper phase relationship with the data clock. This controls pulse width -- the timing between signals for leading and trailing edges on lines 34 and 36 during which the data clock out signal on line 68 strobes the clear input of flip flop 40 to provide the data out signals on lines 42 and 44.

A further preferred embodiment of a width restoration circuit is shown in Fig. 2A. Signals chronologically representative of detected leading edges and trailing edges of pulses from data recorded in pulse width modulated form are provided on a line 110 to a counter 112. A clock signal on line 114 provides a signal to be counted by counter 112. Counter 112 counts the number of clock pulses received between successive leading and trailing edges. The count of clock pulses, n , is provided to a subtract circuit 118, which subtracts an expected number of clock pulses, m , from the actual count, n . Subtract circuit 118 supplies the $n-m$ count or value to a valid count logic circuit

124. The valid count logic circuit 124 ensures that only leading to trailing edge counts of minimum duration are measured, such as result from a data stream corresponding to consecutive transitions. The $n-m$ count is converted to an analog error signal representative of the difference in size of a spot or pit on a medium on line 126 by a digital to analog converter 128. The error signal on line 126 is provided to a low pass filter and scaler 130, which is designed with a frequency response representative of the causes of variable spot size. In general, if the transition rate is " x " transitions per second then the frequency response should be limited to less than or equal to x divided by 50 transitions per second to insure an adequate number of samples. A faster technique comprises increasing the counting logic to sample more than just the shortest leading to trailing edge time, and then using these multiple times to derive an error signal with a higher sampling rate.

The output of the filter 130 is a measure of the width variation of the actual edges from the expected edges. This signal on line 132 is applied to a double phase locked loop arrangement. The phase locked loops are referred to as a plus phase shifter 134 and a minus phase shifter 136. These phase shifters then provide phase shifts for independent clocks in their loops. These clocks are used to detect the leading and trailing edges of pulses respectively.

Alternatively in Fig. 2B, the signal on line 132 could be provided to single shot 20 in Figure 1 to correct for width variations in the same manner as the signal from filter 90 (FIG. 1) was provided to single shot 20 to correct for width variations.

In yet another implementation, both single shots 18 and 20 in Fig. 1 could be variable as shown in Fig. 2C. The width variation could be divided in two by the scaler in filter and scaler 130. The half correction signal is then applied to variable single shots 18' and 20. Variable single shot 18' is substituted for fixed single shot 18 in Figure 1.

The circuits of Fig. 1 and Fig. 2A provide for width restoration of data already written, preferably by laser, in pulse width modulated form. The concept of pre-distortion is employed in the circuit of Fig. 3 to correct the width of actual pulses as they are written on the media.

The data to be written is provided to a data signal source 150 on a line 152. The data signal source is a reference means providing a write signal having the correct timing for ideal data detection. Data signal source 150 provides encoding, synchronization and error correction coding to generate a write signal on a line 154 to a variable edge control block 156. Edge control block 156 changes or predistorts the pulse width, the timing relationship between leading and trailing edges of the write signal data. When writing data on an optical recording medium the pulse width of the write signal controls the laser dwell time, the

length of time the laser is at a current level to write a pit in the medium.

Edge control 156 changes the pulse width as a function of nonlinearities in the optical recording process. Control 156 comprises a voltage variable single shot and an AND gate. The AND gate receives the write signal and the output of the single shot. The single shot receives the write signal and the duration of the single shot output pulse is controlled by the signals from filter 188 and DAC 210 whose functions will be described hereinafter. In operation, the single shot conditions the AND gate so that it has an output pulse with one edge of the pulse corresponding to one coincident with one edge of the data pulse and with the other edge lengthened or shortened by the single shot duration relative to the other edge of the data pulse. In other words, the edge control 156 consists of timing logic that will lengthen or shorten the write signal data pulse. Ideally, the predistortion to the data is such that after writing and readback, the detected signal from the media contains the timing existing prior to the predistortion.

As data is written on a medium by a radiant energy beam generator such as GaAs laser 160, a photodiode array 164 detects the reflected light from the media and provides a readback signal summed by junction 166 to a divider 168. Divider 168 also receives a measure of laser intensity from a monitor diode 170 on a line 172. The division of the readback signal by the measure of laser intensity from the monitor diode provides a normalized read signal on a line 174 as taught in the U.S.A 4631706 entitled "System for preventing the overwriting of previously optically recorded data and for reading optically recorded data during writing". The normalized read signal is processed by equalization and filtering circuitry 178 and data detection circuitry 180. The data signal from data detection 180 is the conventional data-in signal as used in Figure 1.

The write data signal on line 154 is subtracted from the read data signal on line 186 by difference amplifier 194. The write signal and the read signal are binary signals having the same amplitude. Thus the difference between them is a measure of the difference in their widths. The differences are accumulated by filter 188 which generates a voltage indicative of the average variation between edge signals in the read data and edge signals in the write data. The filter 188 is a low pass filter for the purpose of smoothing out the error signals. The filter response is chosen for the resultant transient response of the read signal control loop to react to recording shift changes such as from mis-focus transients and recording media variations. Thus, filter 188 provides a pulse width or edge correction signal representative of the comparison of the write data and detected read signal.

The edge correction signal on line 196 is connected to the voltage variable single shot in edge control

156. The edge correction voltage controls the timing of single shot 156 to change the width of the pulse signal to write driver 216. In other words, the edge control shifts the leading and trailing edges relative to each other to predistort the write signal on line 218 relative to the signal on line 154. However, as a result of this predistortion, the read signal on line 186 will closely match the write signal on line 154. Write data predistortion is also provided as a function of track radius. Because the medium usually rotates at a constant velocity, the surface velocity of the laser spot varies as a function of its distance from the center of rotation of the medium. A digital to analog converter 210 provides a control voltage to the variable single shot in edge control 156 to adjust the pulse width as a function of track location represented by the most significant bits of its address.

The actual dwell time of the laser 160 (time during which write current is on) is controlled by a write driver 216 which receives the signal from edge control 156 on line 218. The write current control circuit 220 provides the write current to the write driver current switch 216. Feedback control to the write current control circuit 220 is achieved by sensing output from the monitor diode 170.

The monitor diode output on line 172 is also provided to a read current control block 222 which provides an output on a line 224 along with the output of write driver 216 on a line 226 to a summing node 228 to control the current flowing to laser 160 during write. The constant current from the read control 222 provides laser current for normal read operations, typically 2 milliwatt power from the laser. During write, the write driver switch 216 directs the additional current from the write current control through the laser for higher power required to melt a pit into the media.

Data edge restoration and clocking can be performed on the read data signal on line 186 by the circuits in Figs 1 or 2. While the width restoration during write removes much of the previous unintended variations in written data, the restoration circuit used again while reading data still provides even higher reliability because it centers the transitions in the data cells or detection windows (as provided by data clock out on line 68 in Fig. 1). Pre-restoration of data leads to greater precision in the positioning of leading and trailing edges of data representations on recording media. The physical density of such edges on the media may then be increased because of associated timing tolerance reduction. When coupled with the improvement in reading reliability given by width restoring data while reading, linear bit densities realizable are almost double that of conventional representation of bits by pits or spots.

While the invention has been described in terms of preferred embodiments, it will be recognized by those skilled in the art that further embodiments are within the scope of the invention as defined by the

claims. All forms of data representations, be it burned pits, opto-magnetic or other less known forms can take advantage of the deskewing of signals taught herein.

Claims

1. A method for correcting width distortions in a pulse width modulated data of the type in which transitions in the representation of data correspond to bits of the first polarity whereas no transitions correspond to bits of the second polarity, and wherein bits of data are recovered from the timing of leading and trailing edges of pulses, the method comprising the steps of:

- (a) detecting the leading edges of data pulses (12, 18);
- (b) detecting the trailing edges of data pulses (16, 20);
- (c) providing pulse signals (22, 24) in response to the detected leading and in response to the detected trailing edges;
- (d) generating a clock signal (50, 62, 64, 66) in response to the combined leading and trailing edge signals, said clock signal following the shifts of said combined edge signals, that occur in the same direction, in order to provide an expected time for the detection of said edges ;
- (e) measuring (60, 88, 90) the amount of shift of the actual leading and trailing edge time from the expected leading and trailing edge time as established by the preceding step for providing a width correction signal ; and
- (f) shifting the relative timing between leading and trailing edge signals by changing the pulse timing of one of said leading or trailing edge signals in response to the width correction signal to restore the width of the data pulses to the expected width between the leading and trailing edge signals.

2. The method of claim 1 wherein said clock generating step further comprises the steps of: detecting (50) the phase difference between the clock signal and the leading and trailing edge signals; speeding up (56) or slowing down (58) the clock signal in response to the phase difference between the clock signal and the leading and trailing edge signals.

3. The method of claim 2 wherein said measuring step comprises : detecting (70 or 76) a first phase difference between the leading edge signals and the clock signal;

detecting (72 or 74) a second phase difference between the trailing edge signals and the clock signal;
combining (78 or 80) the first and second phase differences to generate the width correction signal.

4. The method of claim 3 wherein said shifting step comprises:
delaying (20) the trailing edge signals relative to the leading edge signals ;
adjusting (92) the delay in the previous step in response to the width correction signal ; and
said combining step further comprises the step of :
averaging (88, 90) the first and second phase differences over several leading and trailing edge detections.

5. The method of any of claims 1 to 4 being applied to the correction of data signals read from optically recorded data.

6. A method according to claim 1 wherein said measuring step includes :
comparing (60) said detected leading and trailing edges with said expected time for the detection of said edges ; and,
restoring (88, 90) the leading and trailing edges to their expected timing.

7. A method according to claim 6 wherein said comparing step (60) includes :
measuring (70, 76) the time difference between the leading edge pulse transitions and their expected timing ;
measuring (72, 74) the time difference between the trailing edge pulse transitions and their expected timing ; and,
accumulating (78, 80) these differences to provide an edge correction signal,
wherein said restoring (88, 90) is responsive to the edge correction signal to shift the timing of the leading or trailing edge relative to each other, and,
wherein the clock signal is provided by means (50, 62, 64, 66) comprising phase lock clocking means for providing, as the expected timing, a clock signal that tracks variations in the timing of leading and trailing edge transitions shifting in the same direction.

8. An apparatus for correcting width distortions in a pulse width modulated data of the type in which transitions in the representation of data correspond to bits of the first polarity whereas no transitions correspond to bits of the second polarity, and wherein bits of data are recovered from the



claims. All forms of data representations, be it burned pits, opto-magnetic or other less known forms can take advantage of the deskewing of signals taught herein.

Claims

1. A method for correcting width distortions in a pulse width modulated data of the type in which transitions in the representation of data correspond to bits of the first polarity whereas no transitions correspond to bits of the second polarity, and wherein bits of data are recovered from the timing of leading and trailing edges of pulses, the method comprising the steps of:
 - (a) detecting the leading edges of data pulses (12, 18);
 - (b) detecting the trailing edges of data pulses (16, 20);
 - (c) providing pulse signals (22, 24) in response to the detected leading and in response to the detected trailing edges;
 - (d) generating a clock signal (50, 62, 64, 66) in response to the combined leading and trailing edge signals, said clock signal following the shifts of said combined edge signals, that occur in the same direction, in order to provide an expected time for the detection of said edges ;
 - (e) measuring (60, 88, 90) the amount of shift of the actual leading and trailing edge time from the expected leading and trailing edge time as established by the preceding step for providing a width correction signal ; and
 - (f) shifting the relative timing between leading and trailing edge signals by changing the pulse timing of one of said leading or trailing edge signals in response to the width correction signal to restore the width of the data pulses to the expected width between the leading and trailing edge signals.
2. The method of claim 1 wherein said clock generating step further comprises the steps of:
 - detecting (50) the phase difference between the clock signal and the leading and trailing edge signals;
 - speeding up (56) or slowing down (58) the clock signal in response to the phase difference between the clock signal and the leading and trailing edge signals.
3. The method of claim 2 wherein said measuring step comprises :
 - detecting (70 or 76) a first phase difference between the leading edge signals and the clock signal;
 - detecting (72 or 74) a second phase difference between the trailing edge signals and the clock signal;
 - combining (78 or 80) the first and second phase differences to generate the width correction signal.
4. The method of claim 3 wherein said shifting step comprises:
 - delaying (20) the trailing edge signals relative to the leading edge signals ;
 - adjusting (92) the delay in the previous step in response to the width correction signal ; and
 - said combining step further comprises the step of :
 - averaging (88, 90) the first and second phase differences over several leading and trailing edge detections.
5. The method of any of claims 1 to 4 being applied to the correction of data signals read from optically recorded data.
6. A method according to claim 1 wherein said measuring step includes :
 - comparing (60) said detected leading and trailing edges with said expected time for the detection of said edges ; and,
 - restoring (88, 90) the leading and trailing edges to their expected timing.
7. A method according to claim 6 wherein said comparing step (60) includes :
 - measuring (70, 76) the time difference between the leading edge pulse transitions and their expected timing ;
 - measuring (72, 74) the time difference between the trailing edge pulse transitions and their expected timing ; and,
 - accumulating (78, 80) these differences to provide an edge correction signal,
 - wherein said restoring (88, 90) is responsive to the edge correction signal to shift the timing of the leading or trailing edge relative to each other, and,
 - wherein the clock signal is provided by means (50, 62, 64, 66) comprising phase lock clocking means for providing, as the expected timing, a clock signal that tracks variations in the timing of leading and trailing edge transitions shifting in the same direction.
8. An apparatus for correcting width distortions in a pulse width modulated data of the type in which transitions in the representation of data correspond to bits of the first polarity whereas no transitions correspond to bits of the second polarity, and wherein bits of data are recovered from the



timing of leading and trailing edges of pulses, the apparatus comprising :

- (a) means for detecting the leading edges of data pulses (12, 18) ;
- (b) means for detecting the trailing edges of data pulses (16, 20) ;
- (c) means for providing pulse signals (22, 24) in response to the detected leading and in response to the detected trailing edges ;
- (d) means for generating a clock signal (50, 62, 64, 66) in response to the combined leading and trailing edge signals, said clock signal following the shifts of said combined edge signals, that occur in the same direction, in order to provide an expected time for the detection of said edges ;
- (e) means for measuring (60, 88, 90) the amount of shift of the actual leading and trailing edge time from the expected leading and trailing edge time as established by the means for generating a clock signal for providing a width correction signal ; and
- (f) means for shifting the relative timing between leading and trailing edge signals by changing the pulse timing of one of said leading or trailing edge signal in response to the width correction signal to restore the width of the data pulses to the expected width between the leading and trailing edge signals.

9. An apparatus for correcting pulse width distortions in a pulse width modulated data as claimed in claim 8, said apparatus further including predistortion means for an optical recording system having a beam generator (160) for generating a radiant energy beam for reading and writing pulse width modulated data on a storage medium, said predistorsion means including :
- means (164, 166) for detecting the data recorded on the storage medium and generating a read signal indicating the pulse width modulated data recorded on the storage medium ;
 - a data signal source (150) for providing a write signal indicative of the pulse width modulated data to be written on the storage medium ;
 - means (194, 188) for comparing the same pulse in the read signal and write signal, while the data is being written on the medium, and generating a second width correction signal (196) ;
 - control means (156, 216) in response to the second width correction signal for controlling the width of the pulse in the write signal to provide a width corrected write signal ;
 - means (170, 228) for driving said beam generator (160) in response to the corrected write signal to write the pulse with a width so that the width of the read signal pulse is close to the width of the write signal pulse irrespective of recording phenom-

on distortions ;

means (168, 178, 180) connected to said beam driving means (170), for normalizing said read signal indicating the pulse width modulated data recorded on the storage medium ;
whereby said normalized data is used to feed said means for detecting the leading edges of data pulses (12, 18) and to be complemented and feed said means for detecting the trailing edges of data pulses (16, 20).

10. The apparatus of claim 9 wherein :
- said detecting means (164, 166) generates a read signal indicating the leading and trailing edges of the data pulses ;
 - said comparing means (194, 188) takes the difference between the read signal and write signal indicating the difference in timing between the leading and trailing edges of the same pulse in the two signals.
11. The device of claim 10 further comprising :
- means for averaging the differences in pulse width between the same pulses in the write and read signals in order to generate the width correction signal.
12. The device of claim 11 wherein ;
- said control means (156, 216) shifts one edge of the pulse being corrected to restore the width of the pulse in the read signal to the width of the pulse in the write signal, and
 - said beam generator (160) is a laser and the dwell time of the laser is controlled by said control means.

Patentansprüche

1. Verfahren zur Korrektur von Breitenverzerrungen in impulsbreitenmodulierten Daten des Typs, in dem Übergänge bei der Darstellung von Daten Bits der ersten Polarität entsprechen, während keine Übergänge Bits der zweiten Polarität entsprechen, und wobei Datenbits aus der zeitlichen Zuordnung der Vorder- und der Rückflanken von Impulsen rückgewonnen werden und das Verfahren folgende Schritte umfaßt:
- (a) Erfassen der Vorderflanken der Datenimpulse (12, 18) ;
 - (b) Erfassen der Rückflanken der Datenimpulse (16, 20) ;
 - (c) Bereitstellen von Impulssignalen (22, 24) in Reaktion auf die erfaßten Vorder- und in Reaktion auf die erfaßten Rückflanken ;
 - (d) Erzeugen eines Taktsignals (50, 62, 64, 66) in Reaktion auf die kombinierten Vorder- und Rückflankensignale, wobei das Taktsi-



- gnal den Verschiebungen der kombinierten Flankensignale folgt, die in der gleichen Richtung auftreten, um eine erwartete Zeit für die Erfassung der Flanken bereitzustellen;
- (e) Messen (60, 88, 90) der Größe der Abweichung der tatsächlichen Vorder- und Rückflankenzeit von der erwarteten Vorder- und Rückflankenzeit, wie sie durch den vorhergehenden Schritt für das Bereitstellen eines Breitenkorrektursignals nachgewiesen wurde; und
- (f) Verschieben der relativen zeitlichen Zuordnung zwischen Vorder- und Rückflankensignalen durch Änderung der zeitlichen Impulszuordnung des Vorder- oder des Rückflankensignale in Reaktion auf das Breitenkorrektursignal, um die Breite der Datenimpulse mit den erwarteten Breiten zwischen Vorder- und Rückflankensignalen wiederherzustellen.
2. Verfahren nach Anspruch 1, wobei der den Takt erzeugende Schritt weiterhin die Schritte umfaßt:
Erfassen (50) der Phasendifferenz zwischen dem Taktsignal und den Vorder- und Rückflankensignalen;
Beschleunigen (56) oder Verlangsamen (58) des Taktsignals in Reaktion auf die Phasendifferenz zwischen dem Taktsignal und den Vorder- und Rückflankensignalen.
 3. Verfahren nach Anspruch 2, wobei der Meßschritt umfaßt:
Erfassen (70 oder 76) einer ersten Phasendifferenz zwischen den Vorderflankensignalen und dem Taktsignal;
Erfassen (72 oder 74) einer zweiten Phasendifferenz zwischen den Rückflankensignalen und dem Taktsignal;
Kombinieren (78 oder 80) der ersten und zweiten Phasendifferenzen, um das Breitenkorrektursignal zu erzeugen.
 4. Verfahren nach Anspruch 3, wobei der Verschiebungsschritt umfaßt:
Verzögern (20) der Rückflankensignale relativ zu den Vorderflankensignalen;
Anpassen (92) der Verzögerung in dem vorangehenden Schritt in Reaktion auf das Breitenkorrektursignal; und
wobei der Kombinationsschritt weiterhin den Schritt umfaßt:
Mitteln (88, 90) der ersten und zweiten Phasendifferenzen von mehreren Vorder- und Rückflankenerfassungen.
 5. Verfahren nach irgendeinem der Ansprüche 1 bis 4, das zur Korrektur von Datensignalen, die von optisch aufgezeichneten Daten gelesen werden, verwendet wird.
 6. Verfahren gemäß Anspruch 1, wobei der Meßschritt umfaßt:
Vergleichen (60) der erfaßten Vorder- und Rückflanken mit der erwarteten Zeit für die Erfassung der Flanken; und
Wiederherstellen (88, 90) der Vorder- und Rückflanken in ihrer erwarteten zeitlichen Zuordnung.
 7. Verfahren gemäß Anspruch 6, wobei der Vergleichsschritt (60) einschließt:
Messen (70, 76) der Zeitdifferenz zwischen den Vorderflanken-Impulsübergängen und ihrer erwarteten zeitlichen Zuordnung;
Messen (72, 74) der Zeitdifferenz zwischen den Rückflanken-Impulsübergängen und ihrer erwarteten zeitlichen Zuordnung; und
Summieren (78, 80) dieser Differenzen, um ein Flankenkorrektursignal bereitzustellen, wobei die Wiederherstellung (88, 90) auf das Flankenkorrektursignal reagiert, um die zeitliche Zuordnung der Vorder- und Rückflanke relativ zueinander zu verschieben, und wobei das Taktsignal durch Mittel (50, 62, 64, 66) bereitgestellt wird, die ein Phasenverriegelungsmittel für ein Taktgeben umfassen, um ein Taktsignal, wie die erwartete zeitliche Zuordnung, bereitzustellen, das sich mit den sich in gleicher Richtung verschiebenden Änderungen in der zeitlichen Zuordnung der Vorder- und Rückflankenübergänge im Gleichlauf befindet.
 8. Vorrichtung zur Korrektur von Breitenverzerrungen in impulsbreitenmodulierten Daten des Typs, in dem Übergänge bei der Darstellung von Daten Bits der ersten Polarität entsprechen, während keine Übergänge Bits der zweiten Polarität entsprechen, und wobei Datenbits aus der zeitlichen Zuordnung der Vorder- und der Rückflanken von Impulsen rückgewonnen werden und die Vorrichtung umfaßt:
(a) Mittel zum Erfassen der Vorderflanken der Datenimpulse (12, 18);
(b) Mittel zum Erfassen der Rückflanken der Datenimpulse (16, 20);
(c) Mittel zum Bereitstellen von Impulssignalen (22, 24) in Reaktion auf die erfaßten Vorder- und in Reaktion auf die erfaßten Rückflanken;
(d) Mittel zum Erzeugen eines Taktsignals (50, 62, 64, 66) in Reaktion auf die kombinierten Vorder- und Rückflankensignale, wobei das Taktsignal den Verschiebungen der kombinierten Flankensignale folgt, die in der gleichen Richtung auftreten, um eine erwartete Zeit für die Erfassung der Flanken bereitzustellen.



stellen;

(e) Mittel zum Messen (60, 88, 90) der Größe der Abweichung der tatsächlichen Vorder- und Rückflankenzeit von der erwarteten Vorder- und Rückflankenzeit, wie sie durch die vorhergehenden Mittel zum Erzeugen eines Taktsignals für das Bereitstellen eines Breitenkorrektursignals nachgewiesen werden; und

(f) Mittel zum Verschieben der relativen zeitlichen Zuordnung zwischen Vorder- und Rückflankensignalen durch Änderung der zeitlichen Impulszuordnung des Vorder- oder des Rückflankensignals in Reaktion auf das Breitenkorrektursignal, um die Breite der Datenimpulse mit den erwarteten Breiten zwischen Vorder- und Rückflankensignalen wiederherzustellen.

9. Vorrichtung zur Korrektur von Breitenverzerrungen in Impulsbreitenmodulierten Daten, wie in Anspruch 8 beansprucht, wobei die Vorrichtung weiterhin Vorverzerrungsmittel für ein optisches Aufzeichnungssystem einschließt, das einen Strahlengenerator (160) zur Erzeugung eines strahlenden Energiestrahls zum Lesen und Schreiben von impulsbreitenmodulierten Daten auf einem Speichermedium aufweist und wobei das Vorverzerrungsmittel einschließt: Mittel (164, 166) zum Erfassen der Daten, die auf dem Speichermedium aufgezeichnet sind, und zum Erzeugen eines Lesesignals, das die Impulsbreitenmodulierten Daten anzeigt, die auf dem Speichermedium aufgezeichnet sind; eine Datensignalquelle (150) zum Bereitstellen eines Schreibsignals, das als Anzeige für die Impulsbreitenmodulierten Daten, die auf dem Speichermedium zu schreiben sind, dient; Mittel (194, 188) zum Vergleichen des gleichen Impulses als Lesesignal und als Schreibsignal, während die Daten auf dem Medium geschrieben werden, und zum Erzeugen eines zweiten Breitenkorrektursignals (196); Steuerungsmittel (156, 216), die auf das zweite Breitenkorrektursignal zum Steuern der Impulsbreite im Schreibsignal ansprechen, um ein breitenkorrigiertes Schreibsignal bereitzustellen; Mittel (170, 228) zum Ansteuern des Strahlengenerators (160) in Reaktion auf das korrigierte Schreibsignal, um den Impuls mit einer Breite so zu schreiben, daß die Breite des Lesesignalimpulses nahe der Breite des Schreibsignalimpulses ist, ohne Rücksicht auf Verzerrungen bei dem Aufzeichnungsphänomen; Mittel (168, 178, 180), die mit den Strahlsteuerungsmitteln (170) verbunden sind, zur Normierung des Lesesignals, das die impulsbreitenmodulierten auf dem Speichermedium aufzeich-

neten Daten anzeigt;

wobei die normierten Daten verwendet werden, um die Mittel zur Erfassung der Vorderflanken der Datenimpulse (12, 18) zu versorgen und um vervollständigt zu werden und um die Mittel zum Erfassen der Rückflanken der Datenimpulse (16, 20) zu versorgen.

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10. Vorrichtung nach Anspruch 9, wobei: das Erfassungsmittel (164, 166) ein Lesesignal erzeugt, das die Vorder- und Rückflanken der Datenimpulse anzeigt; das Vergleichsmittel (194, 188) die Differenz zwischen dem Lesesignal und dem Schreibsignal bildet, was die Differenz in der zeitlichen Zuordnung zwischen der Vorder- und der Rückflanke des gleichen Impulses in den zwei Signalen anzeigt.

11. Vorrichtung nach Anspruch 10, die weiterhin umfaßt: Mittel zum Mitteln der Differenzen in der Impulsbreite zwischen gleichen Impulsen in den Schreib- und Lesesignalen, um das Breitenkorrektursignal zu erzeugen.

12. Vorrichtung nach Anspruch 11, wobei: das Steuermittel (156, 216) eine Flanke des Impulses, der korrigiert wurde, um die Impulsbreite in dem Lesesignal zur Impulsbreite des Schreibsignals wiederherzustellen, verschiebt, und der Strahlengenerator (160) ein Laser ist, und die Verweilzeit des Lasers durch die Steuerungsmittel gesteuert wird.

Revendications

1. Procédé pour corriger des distorsions dans des données modulées en largeur d'impulsion du type dans lequel des transitions dans la représentation de données correspondent à des bits de première polarité tandis que l'absence de transitions correspond à des bits de deuxième polarité, et dans lequel des bits de données sont récupérés à partir de la synchronisation de fronts avant et arrière d'impulsions, le procédé comprenant les étapes de:
- (a) détecter les fronts avant d'impulsions de données (12, 18);
 - (b) détecter les fronts arrière d'impulsions de données (16, 20);
 - (c) fournir des signaux d'impulsion (22, 24) en réponse au front avant détecté et en réponse au front arrière détecté;
 - (d) engendrer un signal d'horloge (50, 62, 64, 66) en réponse aux signaux de fronts avant et arrière, ledit signal d'horloge suivant les déca-



- lages desdits signaux de fronts combinés qui apparaissent dans la même direction de manière à fournir un temps prévu à la détection desdits fronts;
- (e) mesurer (60, 88, 90) la quantité du décalage du temps véritable des fronts avant et arrière à partir du temps des fronts avant et arrière prévu tel qu'il est établi par l'étape précédente de fournir un signal de correction de largeur; et
- (f) décaler la synchronisation relative entre les signaux de fronts avant et arrière en changeant la synchronisation d'impulsion d'un desdits signaux de fronts avant ou arrière en réponse au signal de correction de largeur afin de restaurer la largeur des impulsions de données à la largeur prévue entre les signaux de fronts avant et arrière.
2. Procédé selon la revendication 1, dans lequel ladite étape d'engendrer une horloge comprend en outre les étapes de:
 - détecter (50) le déphasage entre le signal d'horloge et les signaux de fronts avant et arrière;
 - accélérer (56) ou ralentir (58) le signal d'horloge en réponse au déphasage entre le signal d'horloge et les signaux de fronts avant et arrière.
 3. Procédé selon la revendication 2, dans lequel ladite étape de mesurer comprend:
 - détecter (70 ou 76) un premier déphasage entre les signaux de front avant et le signal d'horloge;
 - détecter (72 ou 74) un deuxième déphasage entre les signaux de front arrière et le signal d'horloge;
 - combinaison (78) ou (80) les premier et deuxième déphasages pour engendrer le signal de correction de largeur.
 4. Procédé selon la revendication 3, dans lequel ladite étape de décaler comprend:
 - retarder (20) les signaux de front arrière par rapport aux signaux de front avant;
 - régler (92) le retard dans l'étape précédente en réponse aux signaux de correction de largeur; et
 - ladite étape de combiner comprend en outre l'étape de:
 - faire la moyenne (88, 90) des premier et deuxième déphasages sur plusieurs détections de fronts avant et arrière.
 5. Procédé selon l'une quelconque des revendications 1 à 4, ledit procédé étant appliqué à la correction de signaux de données extraits de données enregistrées optiquement.
 6. Procédé selon la revendication 1, dans lequel ladite étape de mesurer comprend:
 - comparer (60) lesdits front avant et arrière avec ledit temps prévu pour la détection desdits fronts; et
 - restaurer (88, 90) les fronts avant et arrière à leur synchronisation prévue.
 7. Procédé selon la revendication 6, dans lequel ladite étape de comparer (60) comprend:
 - mesurer (70, 76) la différence des temps entre les transitions d'impulsion de front avant et leur synchronisation prévue;
 - mesurer (72, 74) la différence des temps entre les transitions d'impulsion de front arrière et leur synchronisation prévue; et
 - accumuler (78, 80) ces différences pour fournir un signal de correction de front;
 - dans lequel ladite restauration (88, 90) est sensible au signal de correction de front pour décaler la synchronisation du front avant ou du front arrière l'un par rapport à l'autre, et dans lequel le signal d'horloge est fourni par des moyens (50, 62, 64, 66) comprenant des moyens d'horlogerie de blocage de phase pour fournir, comme synchronisation prévue, un signal d'horloge qui trace les variations de la synchronisation des transitions de fronts avant et arrière décalées dans la même direction.
 8. Appareil pour corriger des distorsions de largeur d'impulsion dans des données modulées en largeur d'impulsion du type dans lequel des transitions dans la représentation de données correspondent à des bits de première polarité tandis que l'absence de transitions correspond à des bits de deuxième polarité, et dans lequel des bits de données sont récupérés à partir de la synchronisation des fronts avant et arrière d'impulsions, l'appareil comprenant:
 - (a) des moyens pour détecter les fronts avant d'impulsions de données (12, 18);
 - (b) des moyens pour détecter les fronts arrière d'impulsions de données (16, 20);
 - (c) des moyens pour fournir des signaux d'impulsion (22, 24) en réponse au front avant détecté et en réponse au front arrière détecté;
 - (d) des moyens pour engendrer un signal d'horloge (50, 62, 64, 66) en réponse aux signaux de fronts avant et arrière, ledit signal d'horloge suivant les décalages desdits signaux de fronts combinés qui apparaissent dans la même direction de manière à fournir un temps prévu à la détection desdits fronts;
 - (e) des moyens pour mesurer (60, 88, 90) la quantité du décalage du temps véritable des



fronts avant et arrière à partir du temps des fronts avant et arrière prévu tel qu'il est établi par les moyens pour engendrer un signal d'horloge pour fournir un signal de correction de largeur; et

(f) des moyens pour décaler la synchronisation relative entre les signaux de fronts avant et arrière en changeant la synchronisation d'impulsion d'un desdits signaux de fronts avant ou arrière en réponse au signal de correction de largeur afin de restaurer la largeur des impulsions de données à la largeur voulue entre les signaux de fronts avant et arrière.

9. Appareil pour corriger des distorsions de largeur d'impulsion dans des données modulées en largeur d'impulsion selon la revendication 8, ledit appareil comprenant en outre des moyens de pré-distorsion pour un système d'enregistrement optique ayant un générateur de faisceau (160) pour engendrer un faisceau d'énergie rayonnante pour lire et écrire des données modulées en largeur d'impulsion sur un support d'emmagasinage, lesdits moyens de pré-distorsion comprenant:

des moyens (164, 166) pour détecter les données enregistrées sur le support d'emmagasinage et engendrer un signal réel indiquant les données modulées en largeur d'impulsion enregistrées sur le support d'emmagasinage;

une source de signal de données (150) pour fournir un signal d'écriture indiquant les données modulées en largeur d'impulsion à écrire sur le support d'emmagasinage;

des moyens (194, 188) pour comparer la même impulsion dans le signal de lecture et le signal d'écriture, tandis que les données sont écrites sur le support, et engendrer un deuxième signal de correction de largeur (196);

des moyens de contrôle (156, 216) en réponse au deuxième signal de correction de largeur pour contrôler la largeur de l'impulsion dans le signal d'écriture afin de fournir un signal d'écriture de largeur corrigée;

des moyens d'excitation (170, 228) pour exciter ledit générateur de faisceau (160) en réponse au signal d'écriture corrigé afin d'écrire l'impulsion avec une largeur telle que la largeur de l'impulsion de signal de lecture soit proche de la largeur de l'impulsion de signal d'écriture indépendamment des distorsions du phénomène d'enregistrement;

des moyens (168, 178, 180) connectés auxdits moyens d'excitation de faisceau (170) pour normaliser ledit signal de lecture indiquant les données modulées en largeur d'impulsion enregistrées sur le support d'emmagasinage;

ce qui fait que lesdites données normalisées sont utilisées pour alimenter lesdits moyens pour détecter les fronts avant d'impulsions de données (12, 18) et pour être complétées et alimenter lesdits moyens pour détecter les fronts arrière d'impulsions de données (16, 20).

10. Appareil selon la revendication 9, dans lequel:
lesdits moyens de détection (164, 166) engendrent un signal réel indiquant les fronts avant et arrière des impulsions de données;
lesdits moyens de comparaison (194, 188) prennent la différence existant entre le signal réel et le signal d'écriture indiquant la différence de synchronisation entre les fronts avant et arrière de la même impulsion dans les deux signaux.

11. Appareil selon la revendication 10, comprenant en outre:

des moyens pour faire la moyenne entre les différences de largeur d'impulsion existant entre les mêmes impulsions dans les signaux d'écriture et de lecture de manière à engendrer le signal de correction de largeur.

12. Appareil selon la revendication 11, dans lequel:
lesdits moyens de contrôle (156, 216) décalent un front de l'impulsion alors corrigée pour restaurer la largeur de l'impulsion dans le signal de lecture à la largeur de l'impulsion dans le signal d'écriture; et

ledit générateur de faisceau (160) est un laser et le temps d'arrêt momentané du laser est contrôlé par lesdits moyens de contrôle.