

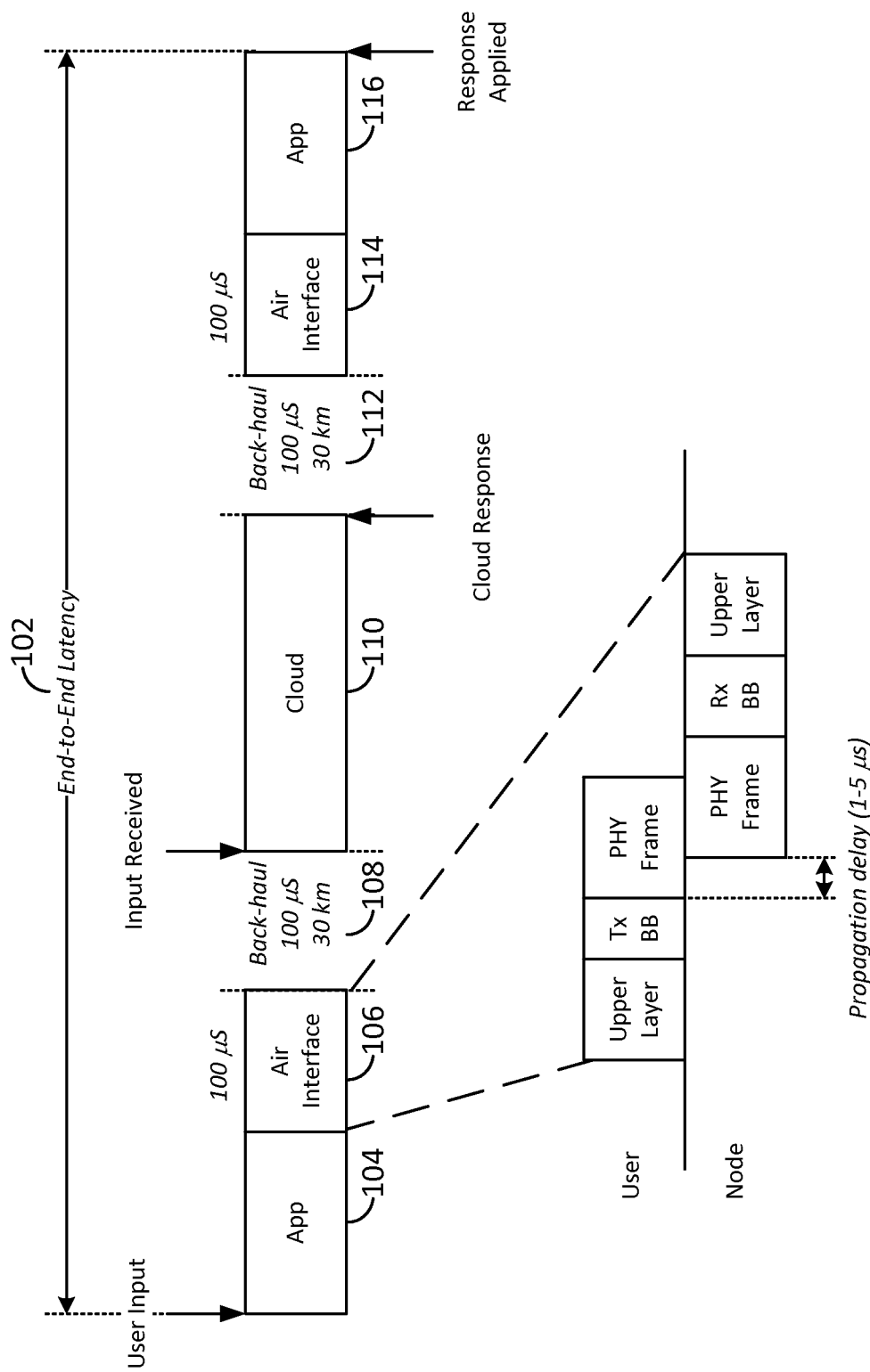


FIG. 1

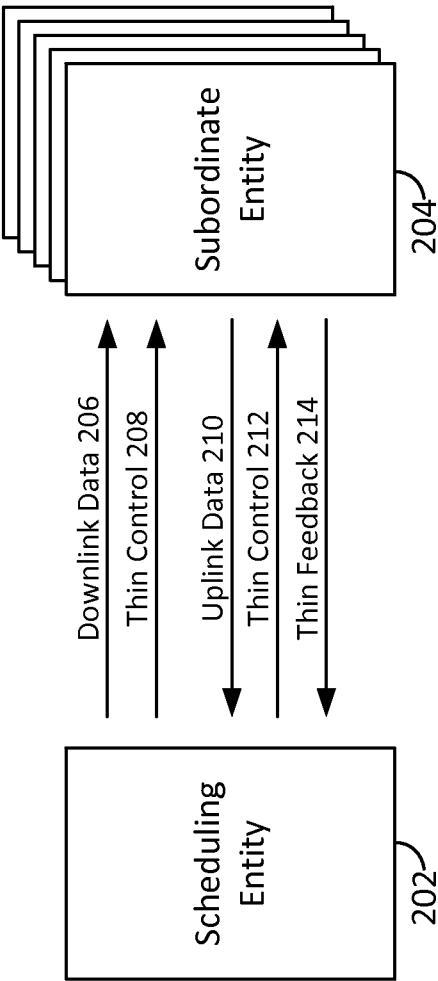


FIG. 2

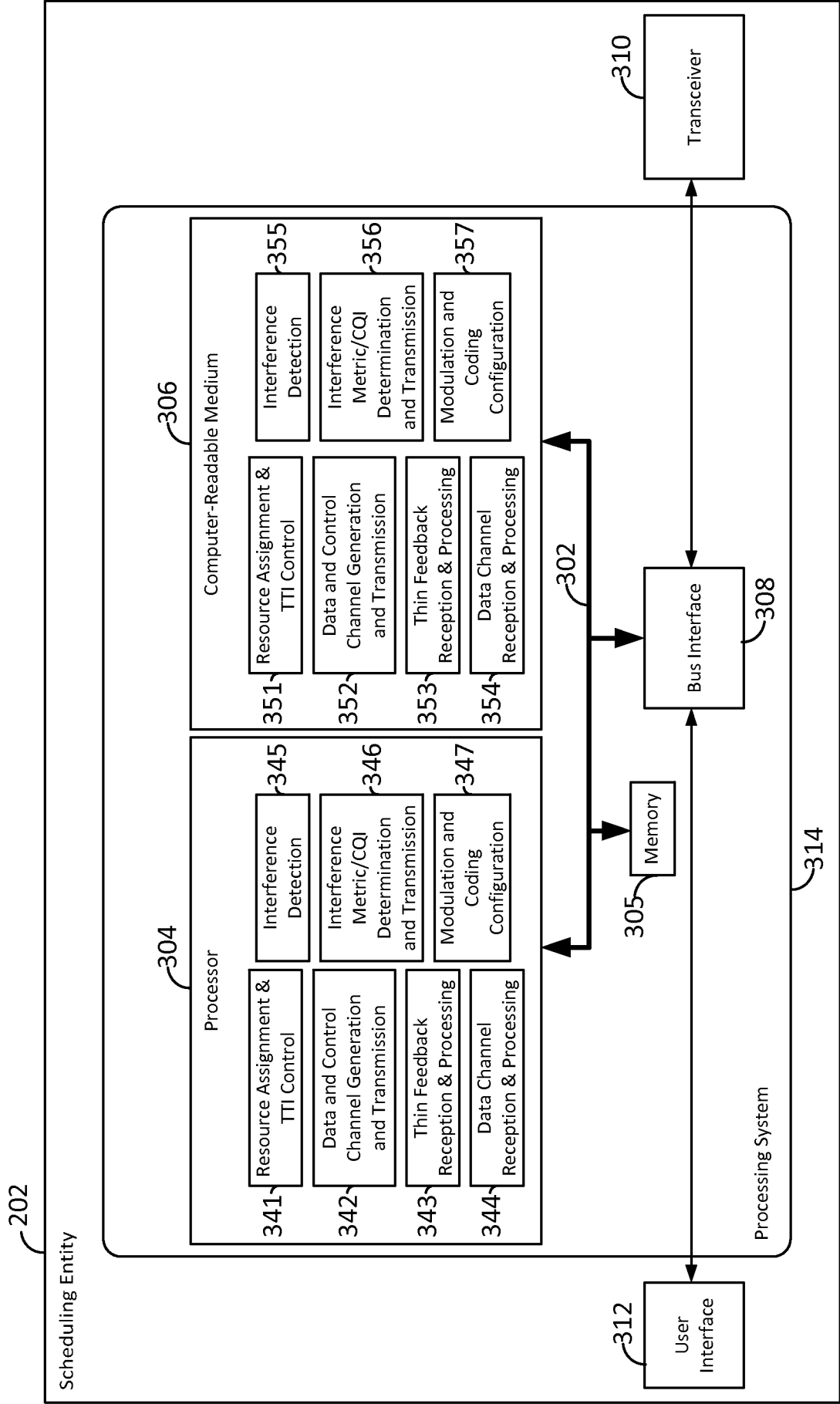


FIG. 3

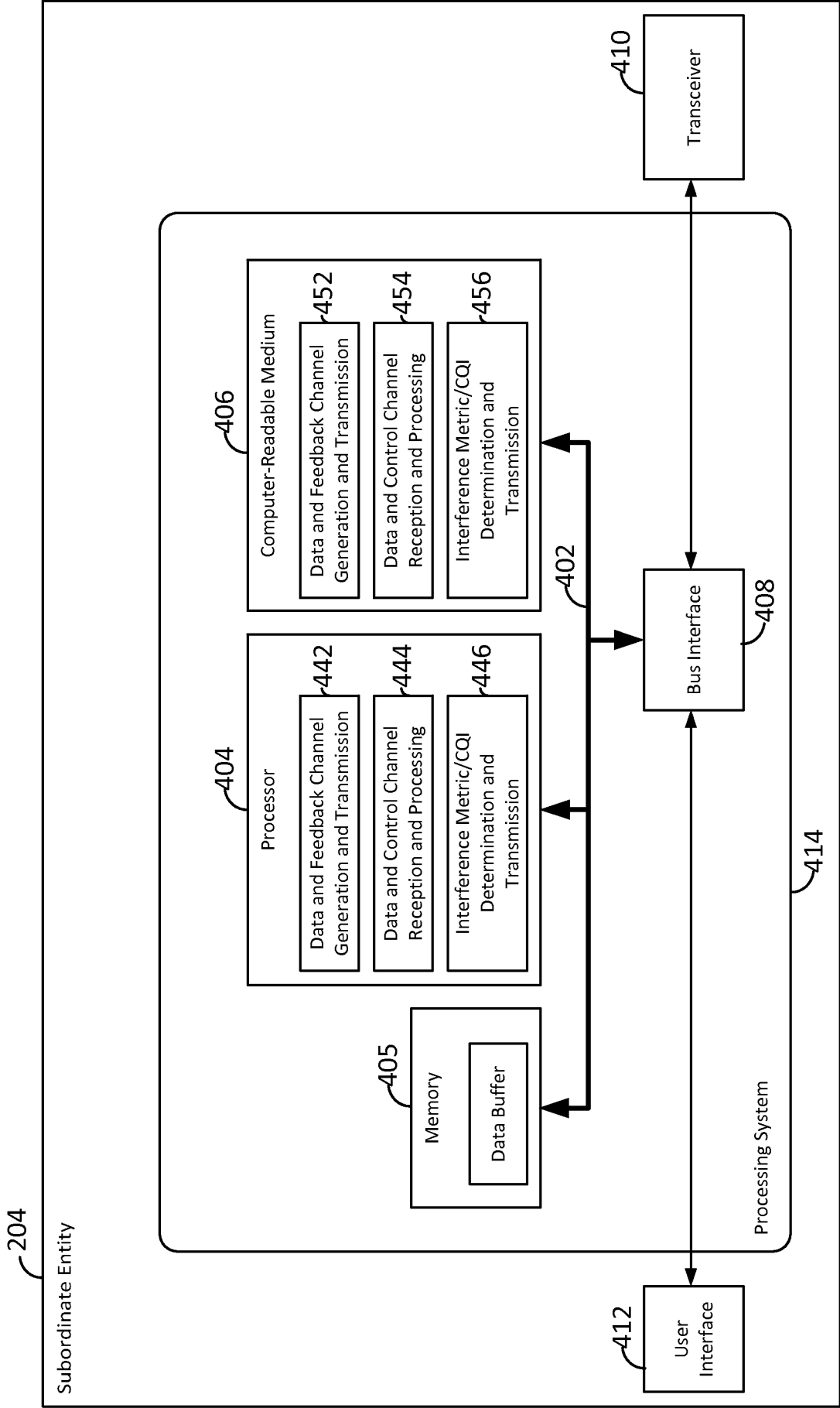


FIG. 4

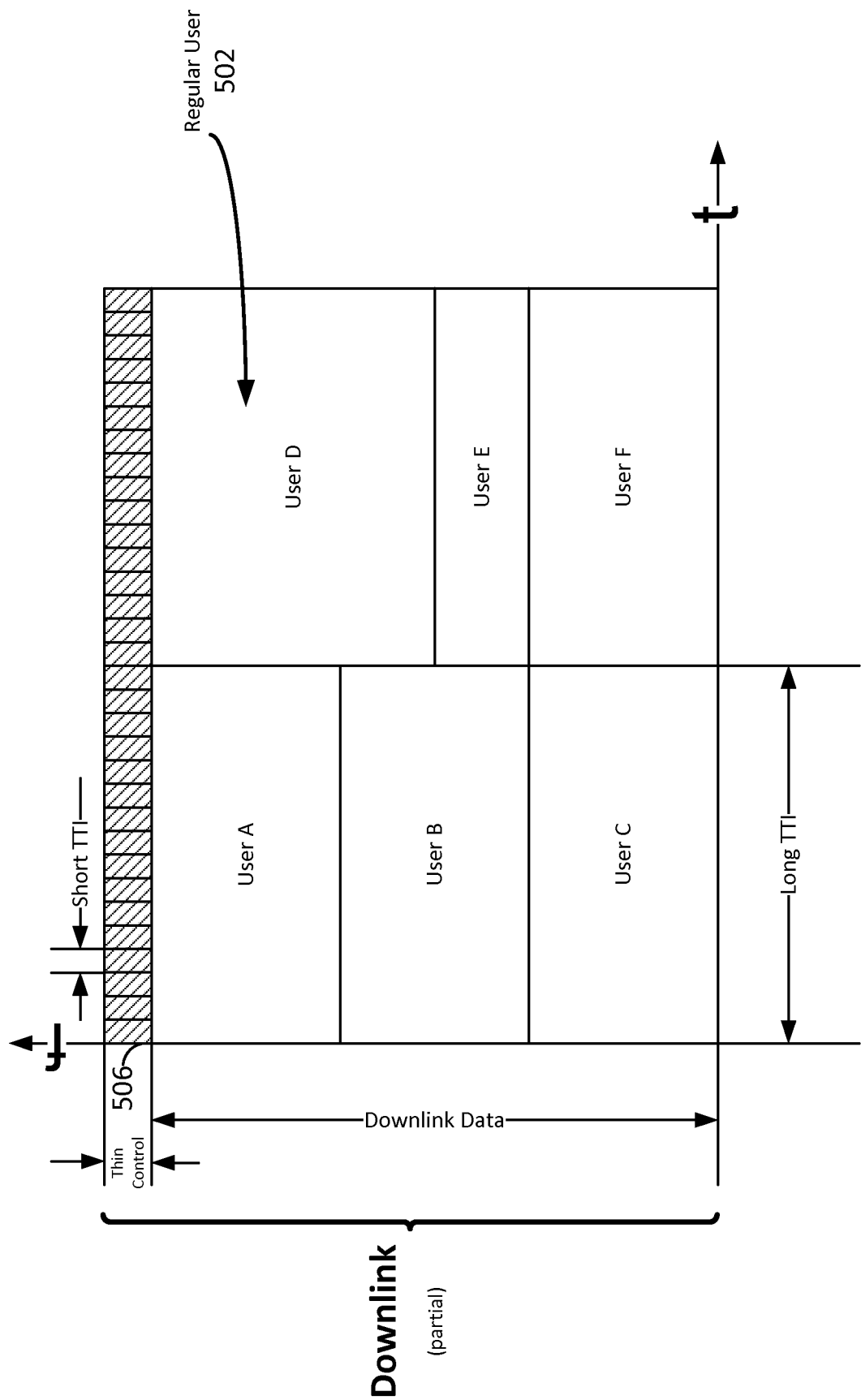


FIG. 5
DL Channel Structure

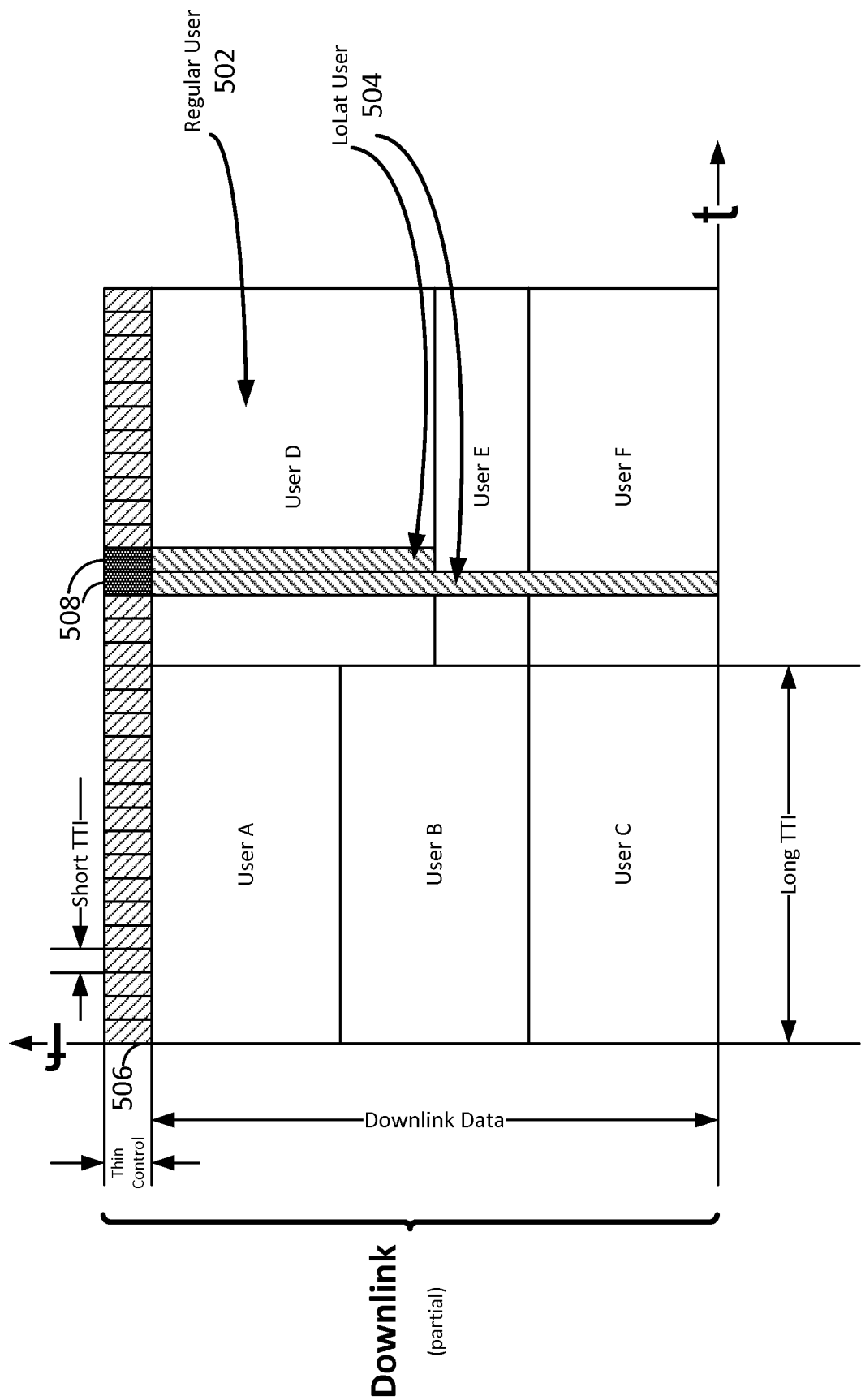


FIG. 6
DL/UL Multiplexing

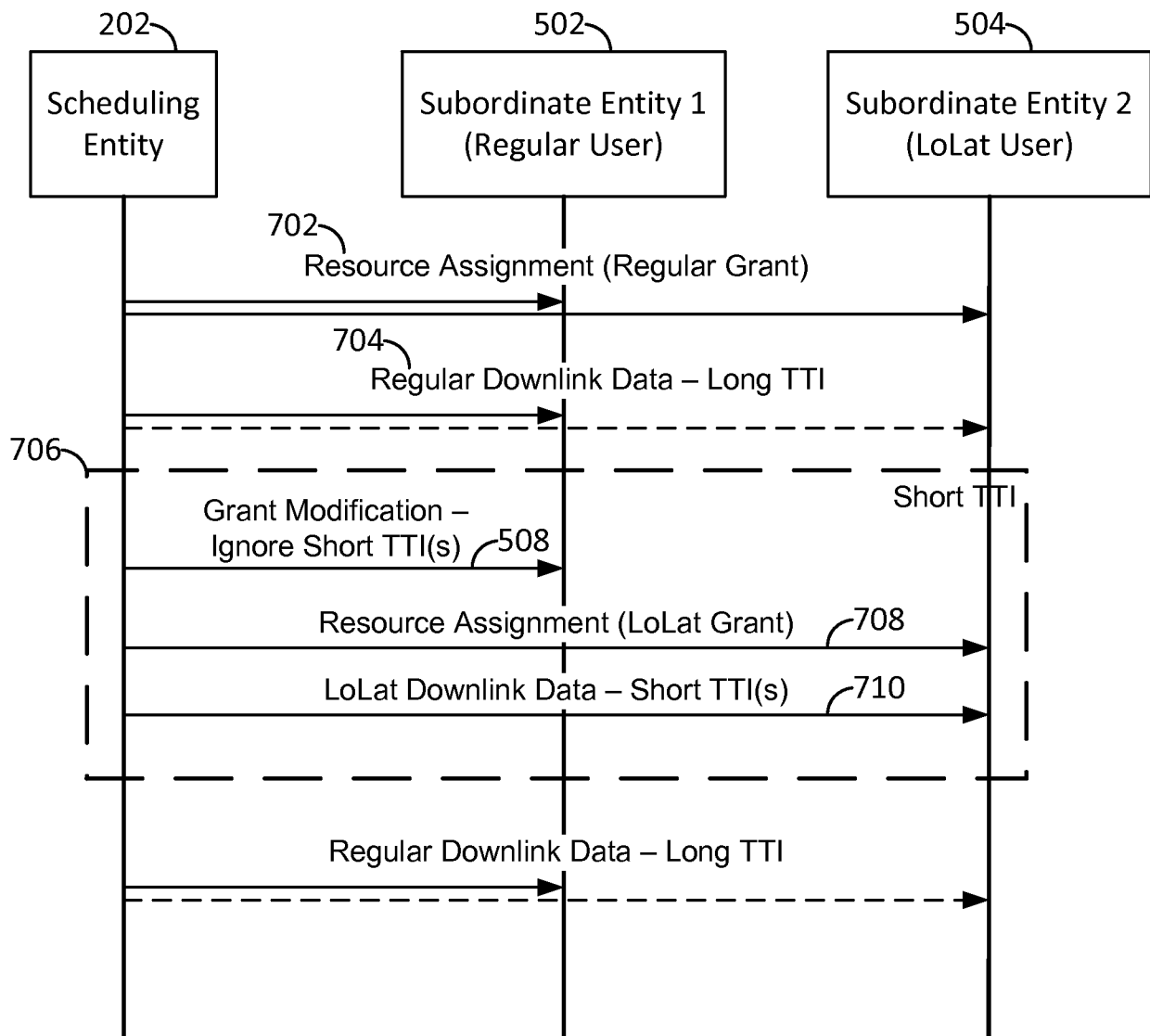


FIG. 7
DL/DL Multiplexing

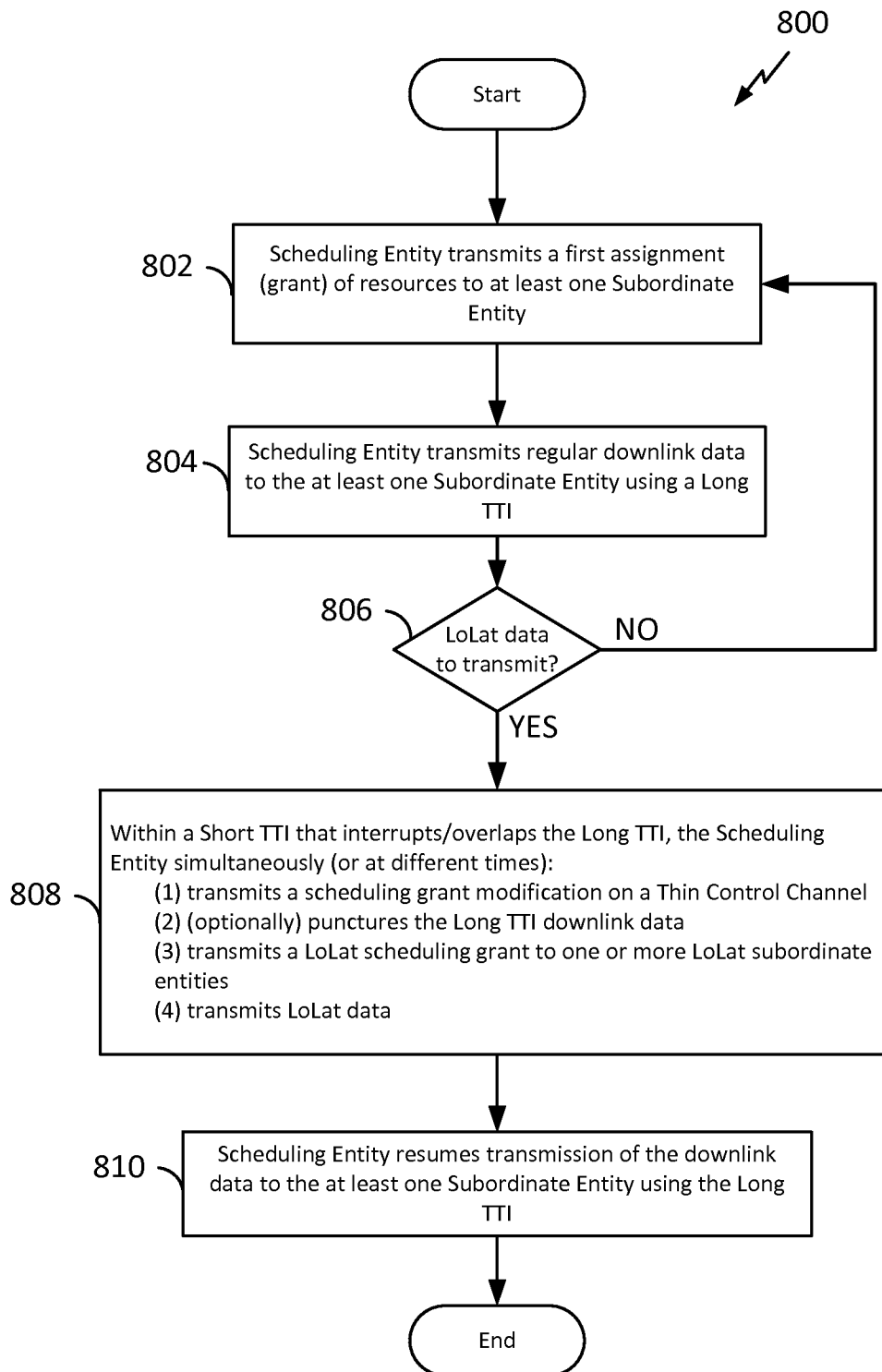


FIG. 8
DL/UL Multiplexing,
Scheduling Entity POV

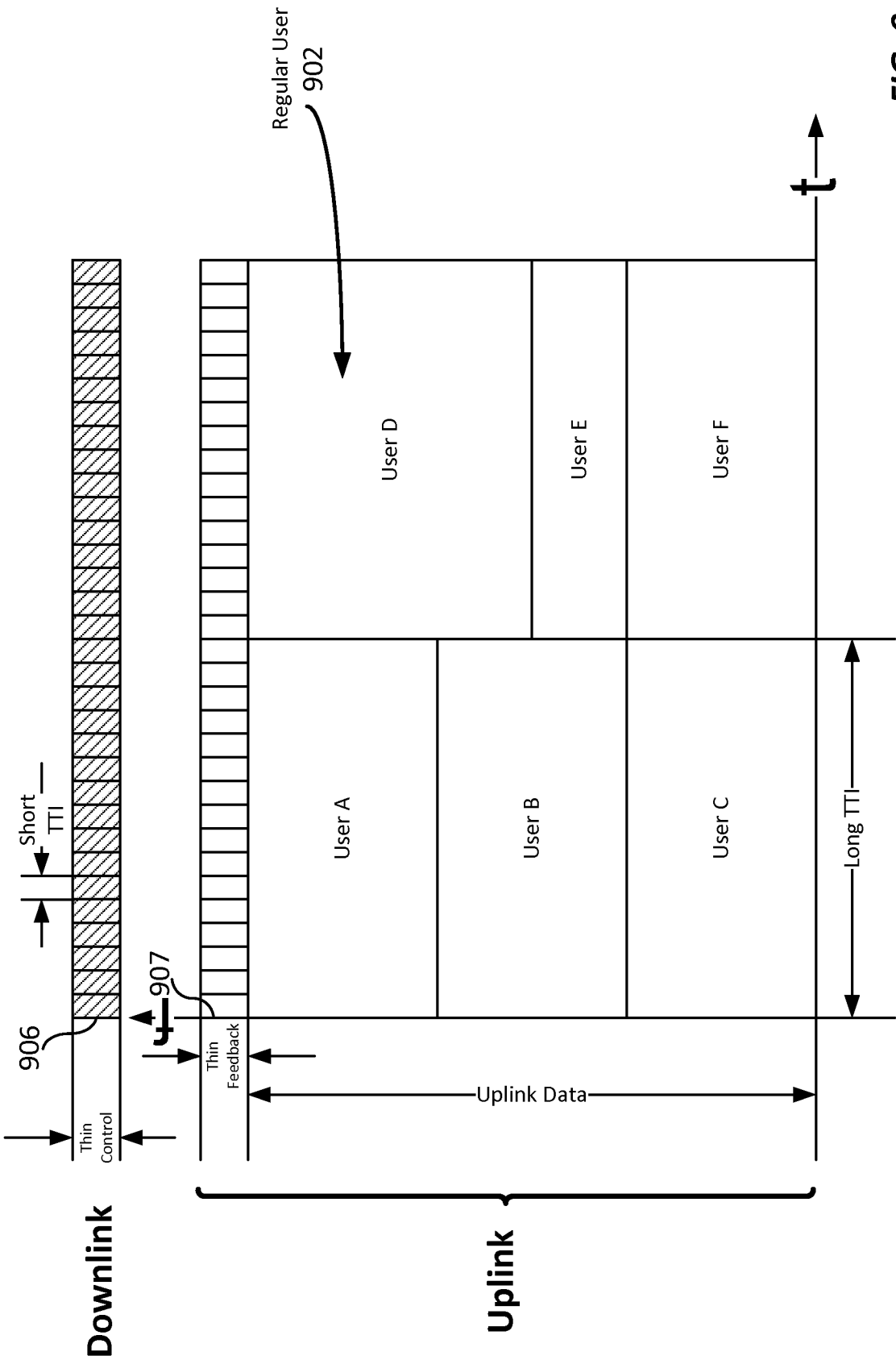


FIG. 9
UL Channel Structure

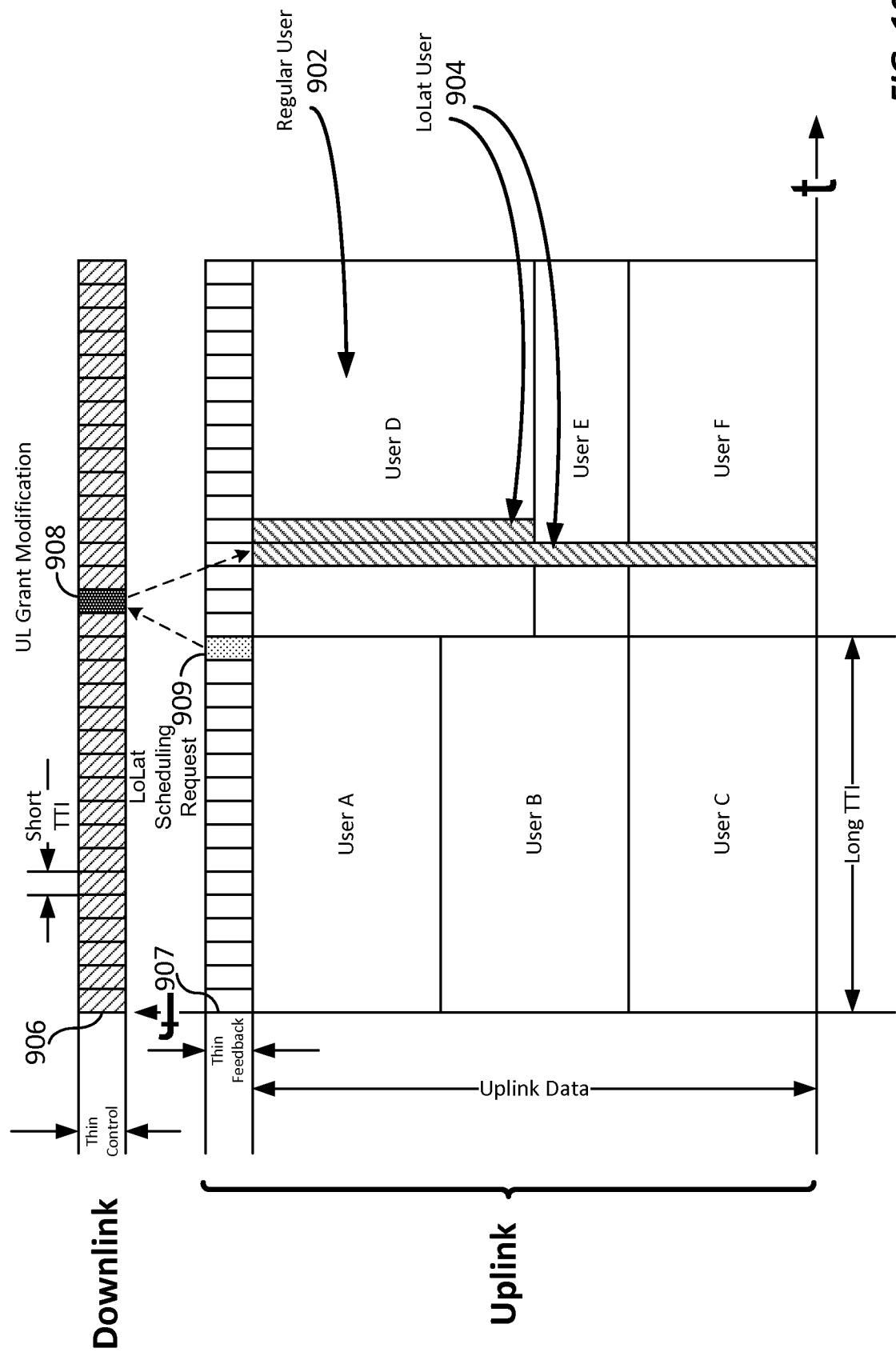


FIG. 10
UL/UL Multiplexing

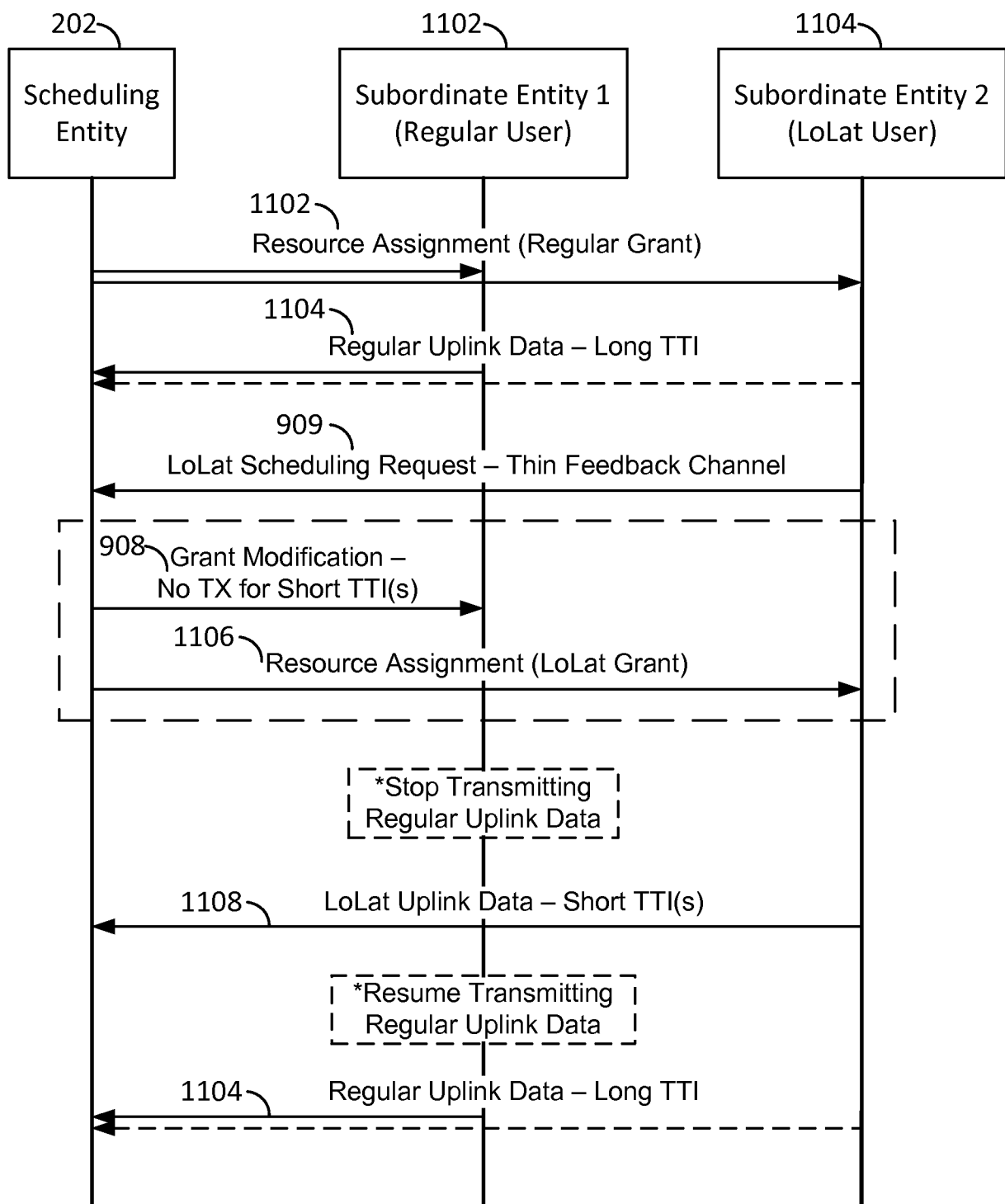


FIG. 11
UL/UL Multiplexing

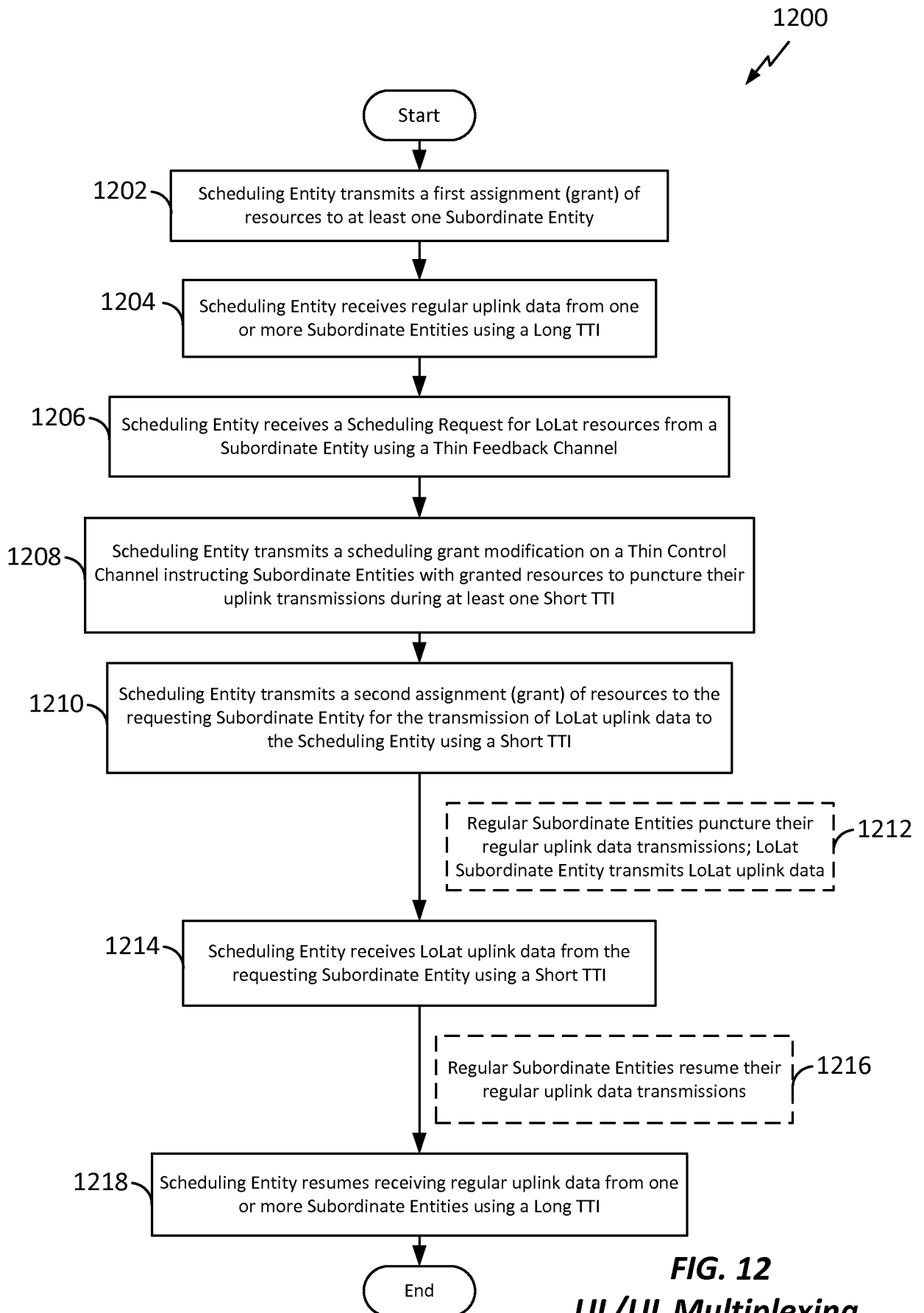


FIG. 12
UL/UL Multiplexing,
Scheduling Entity POV

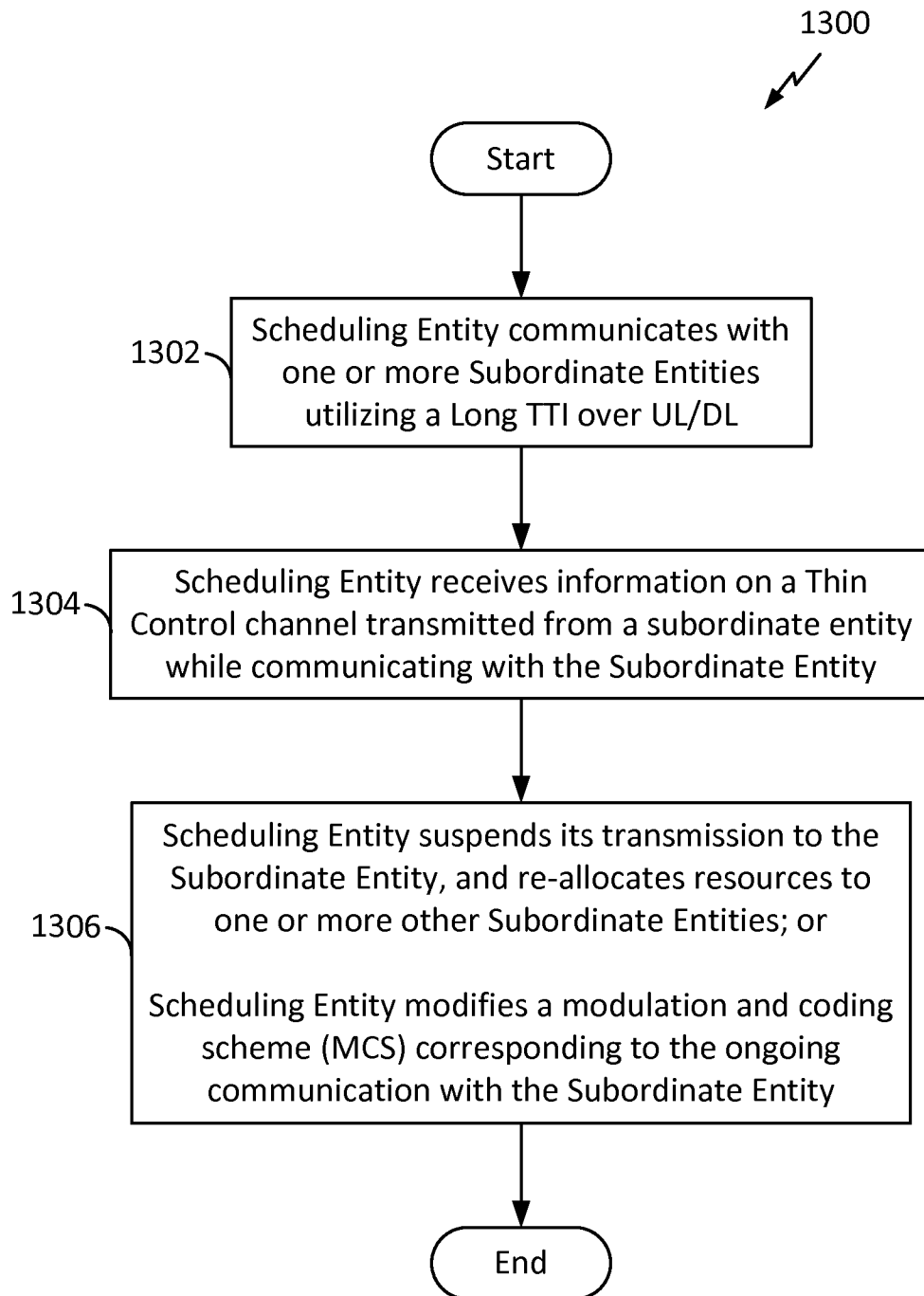


FIG. 13
Interference Management