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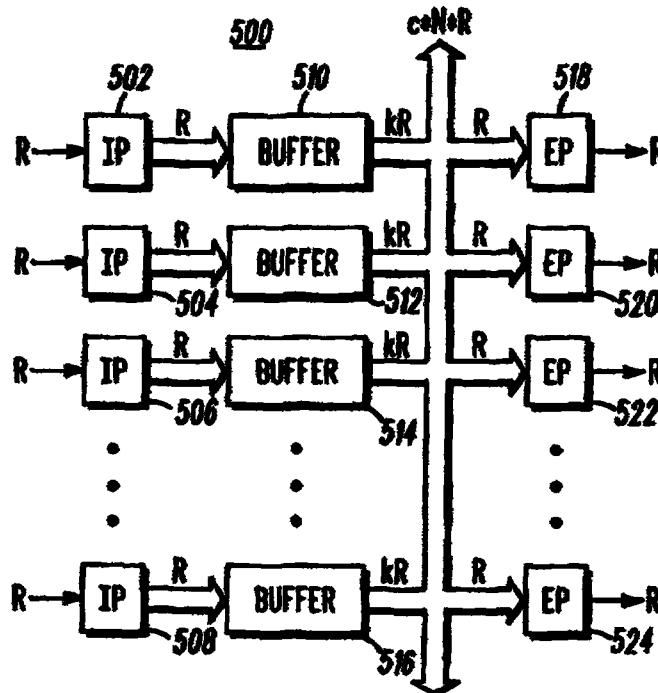
INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

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| (21) International Application Number: ✓ PCT/US97/03920<br>(22) International Filing Date: ✓ 13 March 1997 (13.03.97)<br>(30) Priority Data: ✓ 08/616,024 ✓ 14 March 1996 (14.03.96) US ✓<br>(71) Applicant: MOTOROLA INC. [US/US]; 1303 East Algonquin Road, Schaumburg, IL 60196 (US).<br>(72) Inventors: HOLT, Craig, Sheppard; 40 Brewster Road, Cohasset, MA 02025 (US); KLINE, Richard; 11 N. Lewis Park Drive, E. Walpole, MA 02032 (US). RAMAKRISHNAN, Krishnan; Quincy House, A-8, 71 Messenger Street, Plainville, MA 02762 (US).<br>(74) Agents: WOOD, J., Ray et al.; Motorola Inc., Intellectual Property Dept., 1303 East Algonquin Road, Schaumburg, IL 60196 (US). |  | (81) Designated States: AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CU, CZ, DE, DK, EE, ES, FI, GB, GE, GH, HU, IL, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, TJ, TM, TR, TT, UA, UG, UZ, VN, YU, ARIPO patent (GH, KE, LS, MW, SD, SZ, UG), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, ML, MR, NE, SN, TD, TG).<br><br>Published<br>With international search report.<br>Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments. |                                                                   |

(54) Title: EFFICIENT OUTPUT-REQUEST PACKET SWITCH AND METHOD ✓

(57) Abstract

A method (1200, 1500, 1600, 1700, 1800) and packet switch (500, 600, 700, 1400, 1900, 2000) for efficient switching of a plurality of received packets from a plurality of ingress ports to a plurality of egress ports, using the steps of: A) storing the packets in memory; B) sending arrival information for each packet to a destination egress port for the packet; C) storing, in memory at each destination egress port, the arrival information; D) requesting, by each destination egress port, the packets from the packet memory in accordance with a predetermined scheme; and E) sending, by the packet memory, to the destination egress ports, the packets requested.



—PRIOR ART—