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ABSTRACT OF THE DISCLOSURE

DYNAMIC RECONFIGURATION OF PCI EXPRESS LINKS

A method and circuitry for dynamically reconfiguring the links of a PCI Express bus. A computer system has been initially configured with PCI Express bus links to various endpoints, using the scaling features of the PCI Express standard. During operation of the computer system, the status of the endpoints is detected and unused links (or unused portions of links) are rerouted to other endpoints.

FIG. 1