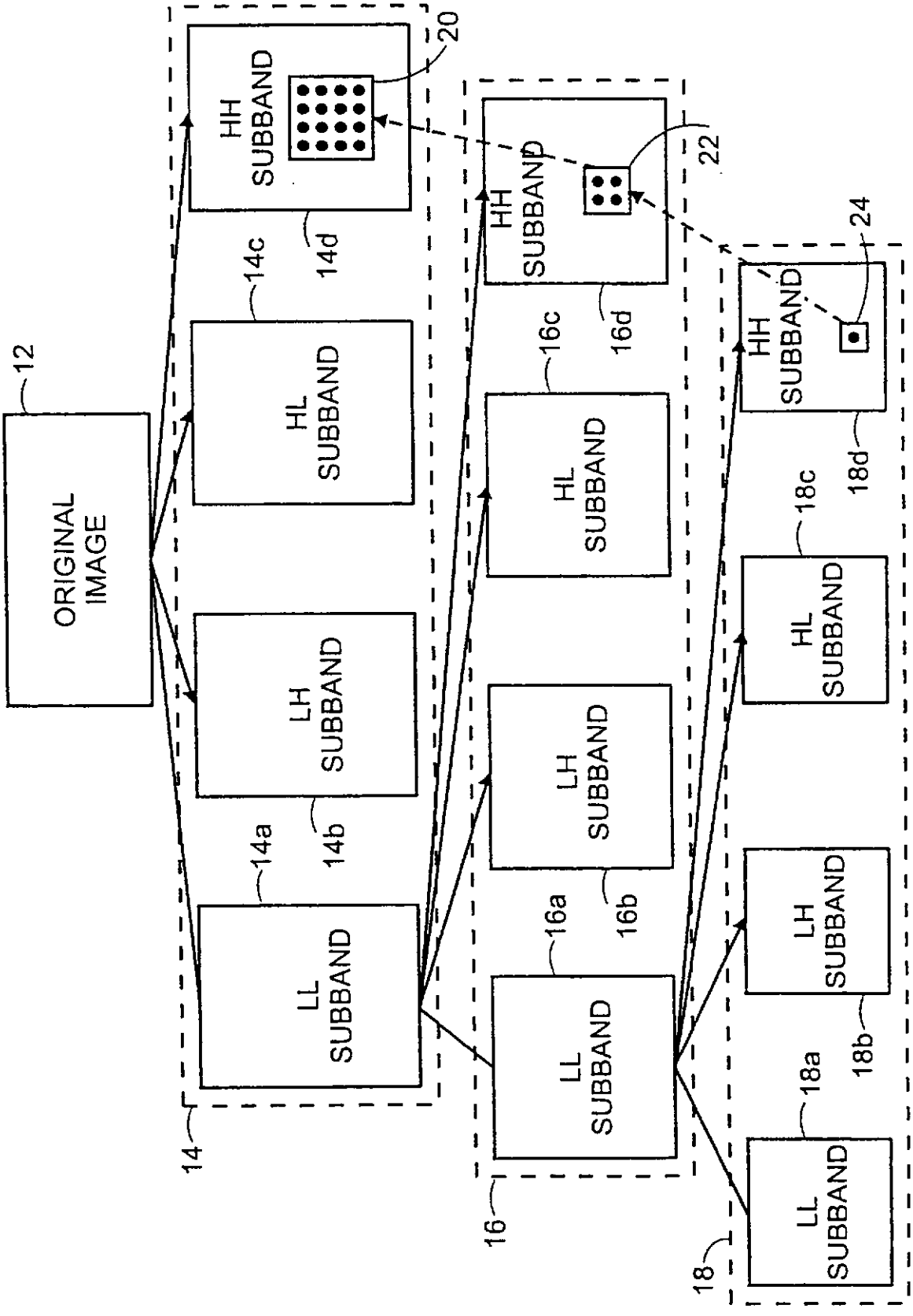


FIG. 1 (PRIOR ART)

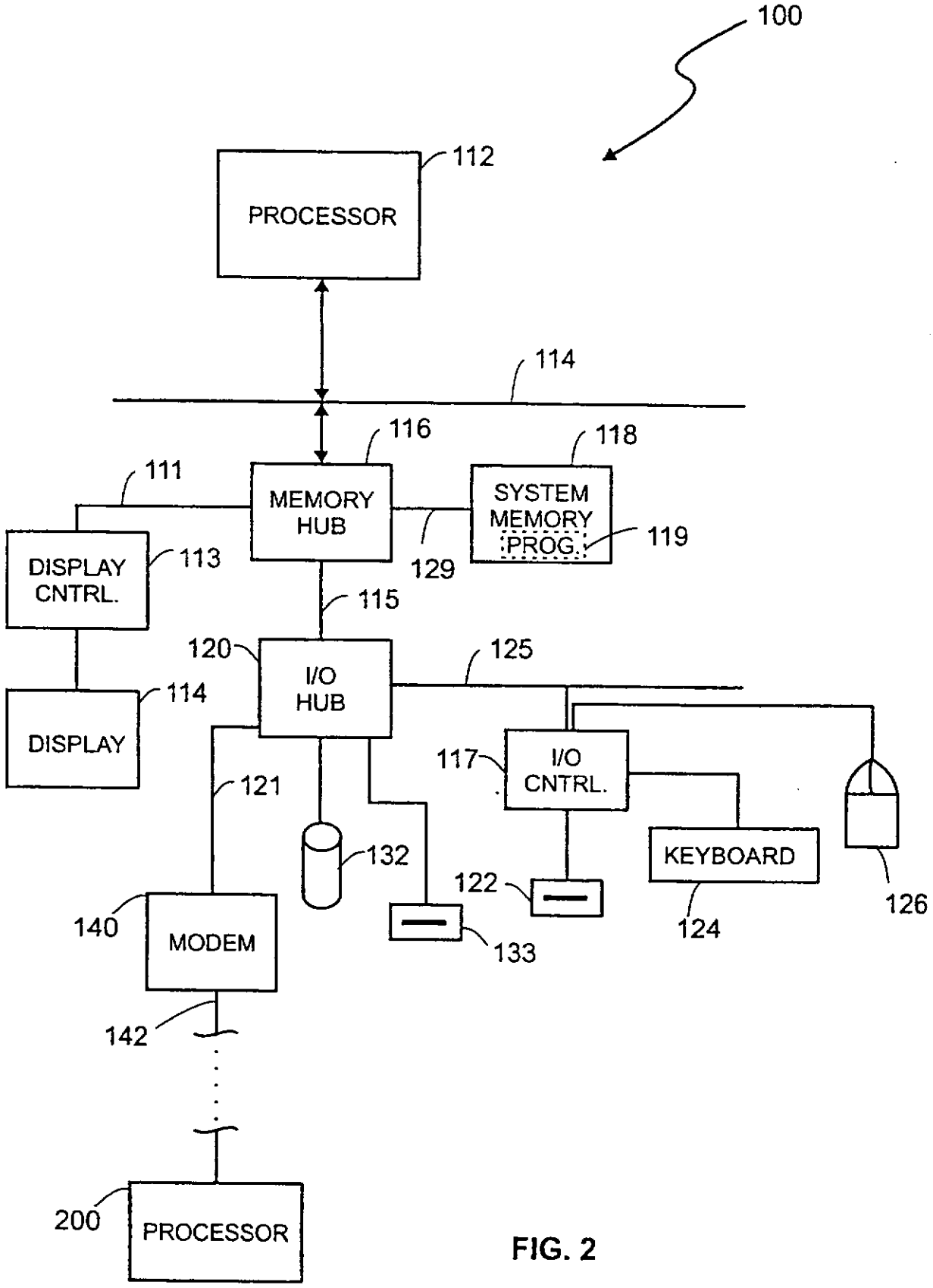

100
PROCESSOR
112
114
116
118
111
MEMORY HUB
SYSTEM MEMORY PROG.
119
DISPLAY CNTRL.
113
129
115
120
I/O HUB
125
DISPLAY
114
117
I/O CNTRL.
121
132
KEYBOARD
122
126
140
MODEM
124
133
142
200
PROCESSOR

FIG. 2

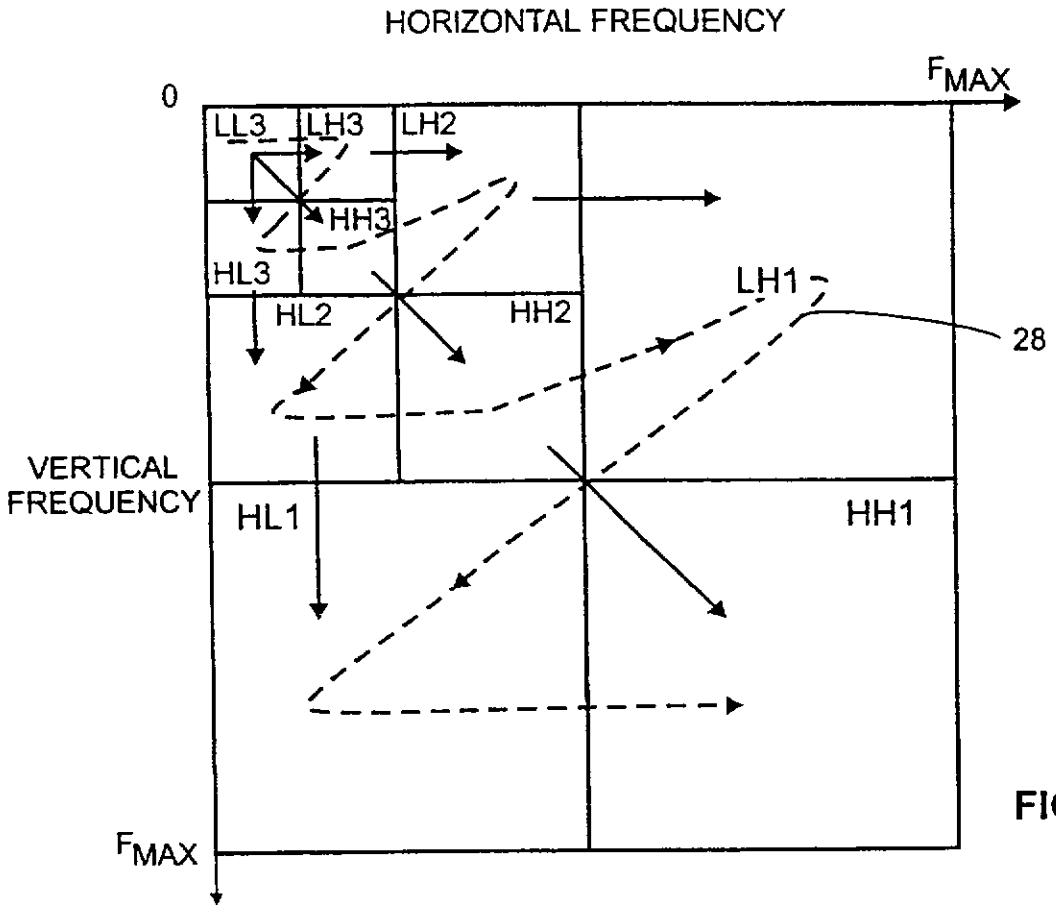

HORIZONTAL FREQUENCY
0
F_{MAX}
LL3
LH3
LH2
HH3
HL3
HL2
HH2
LH1
28
VERTICAL FREQUENCY
HL1
HH1
F_{MAX}

FIG. 3

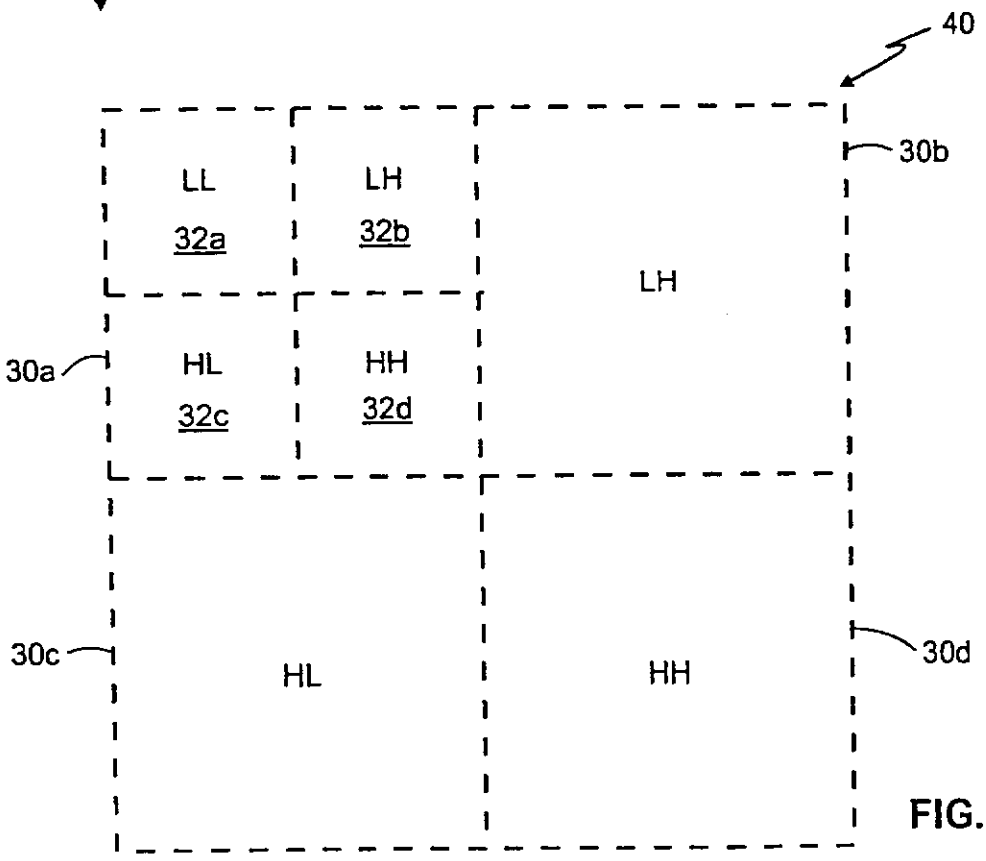

40
30b
LL
32a
LH
32b
LH
30a
HL
32c
HH
32d
30c
HL
HH
30d

FIG. 4

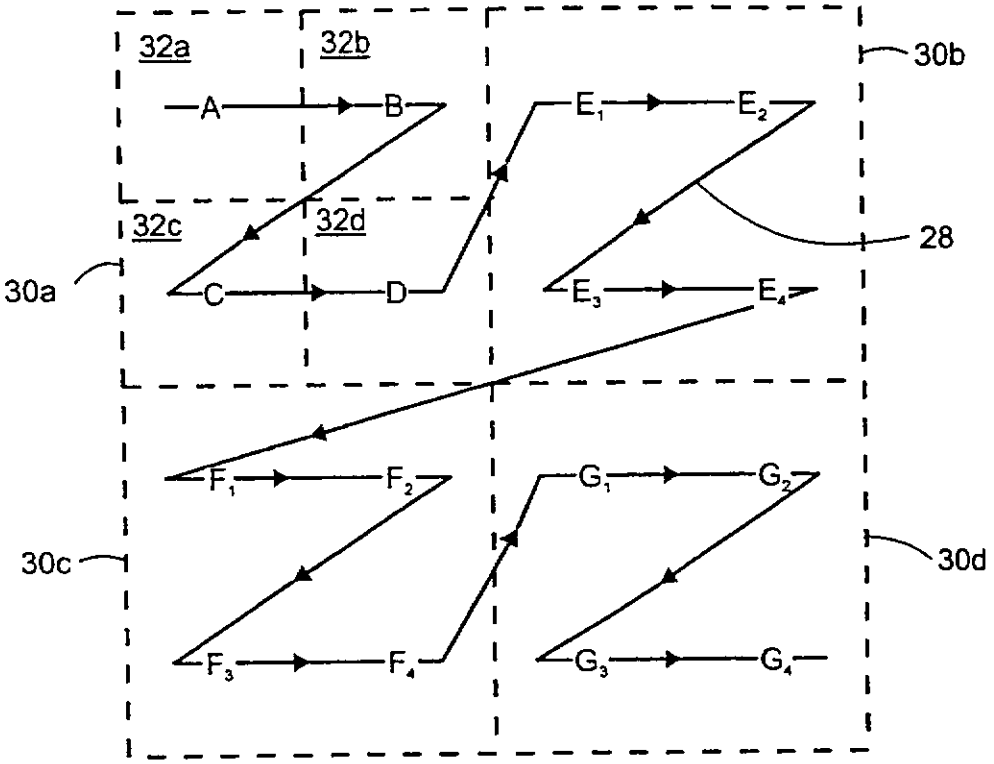


FIG. 5

$$D(1) \longrightarrow \{ G_1(1), G_2(1), G_3(1), G_4(1) \}$$

FIG. 6

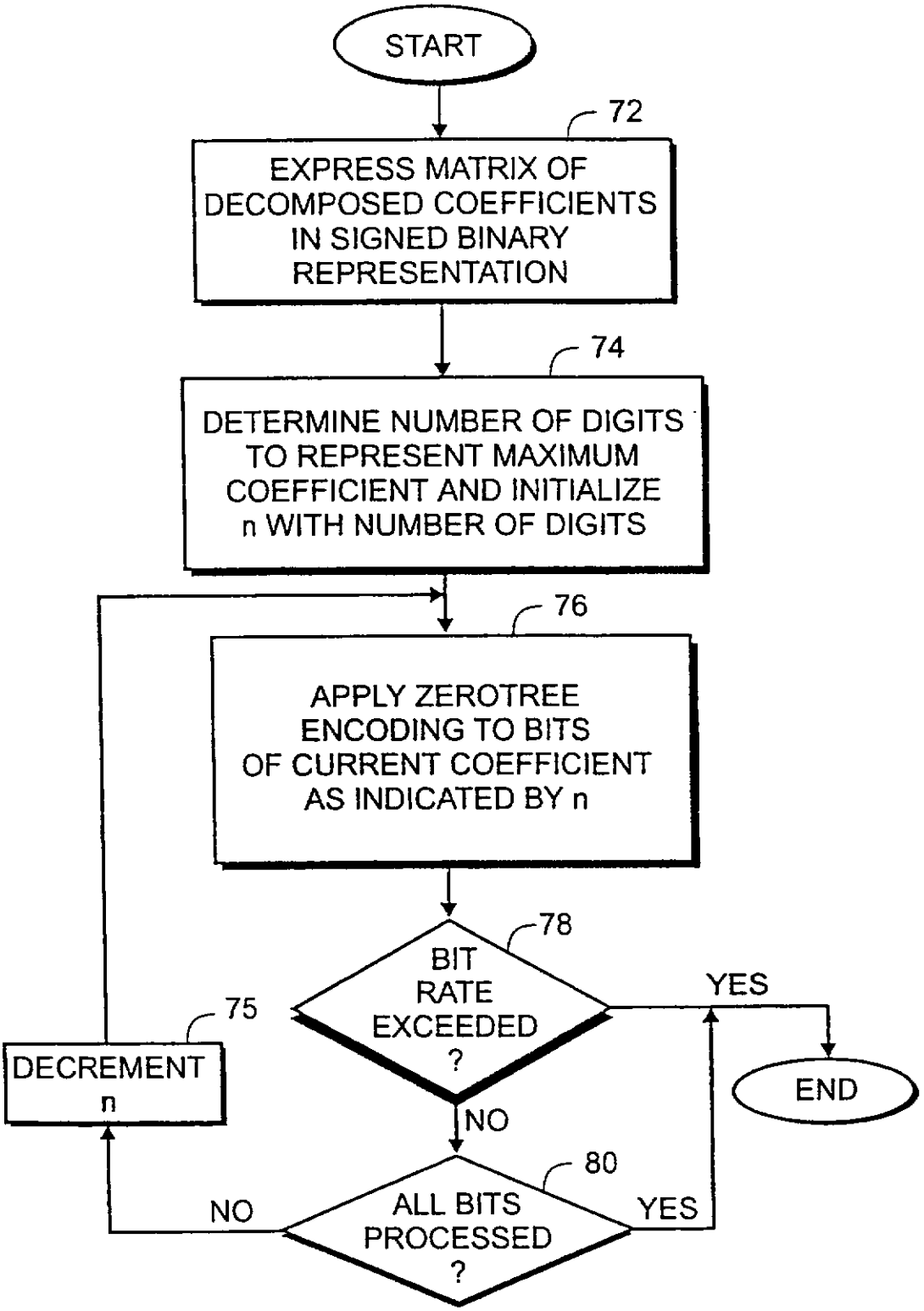

START
72
EXPRESS MATRIX OF DECOMPOSED COEFFICIENTS IN SIGNED BINARY REPRESENTATION
74
DETERMINE NUMBER OF DIGITS TO REPRESENT MAXIMUM COEFFICIENT AND INITIALIZE n WITH NUMBER OF DIGITS
76
APPLY ZEROTREE ENCODING TO BITS OF CURRENT COEFFICIENT AS INDICATED BY n
78
BIT RATE EXCEEDED ?
YES
END
NO
75
DECREMENT n
80
ALL BITS PROCESSED ?
NO
YES

FIG. 7