

Abstract

Method and arrangement for converting memory addresses into memory selection signals

Memory modules (SM) can be inserted into insertion regions (EB1..4) of a memory (SP) and logical capacity and configuration information items (lki,lkoi) are derived from capacity information items (ki) indicating the storage capacity (K1,4) of the said memory modules and from configuration information items (koi) indicating the presence of the said memory modules. With the aid of these conversion information items (lki,lkoi), a current address information item (AD0..3) is converted into a physical memory selection signal (sas) which enables the relevant memory module (SM). Flexible memories (SP) which are co-ordinated with an application can be configured by this means.

FIG 2

This Sheet Has Been Replaced

