

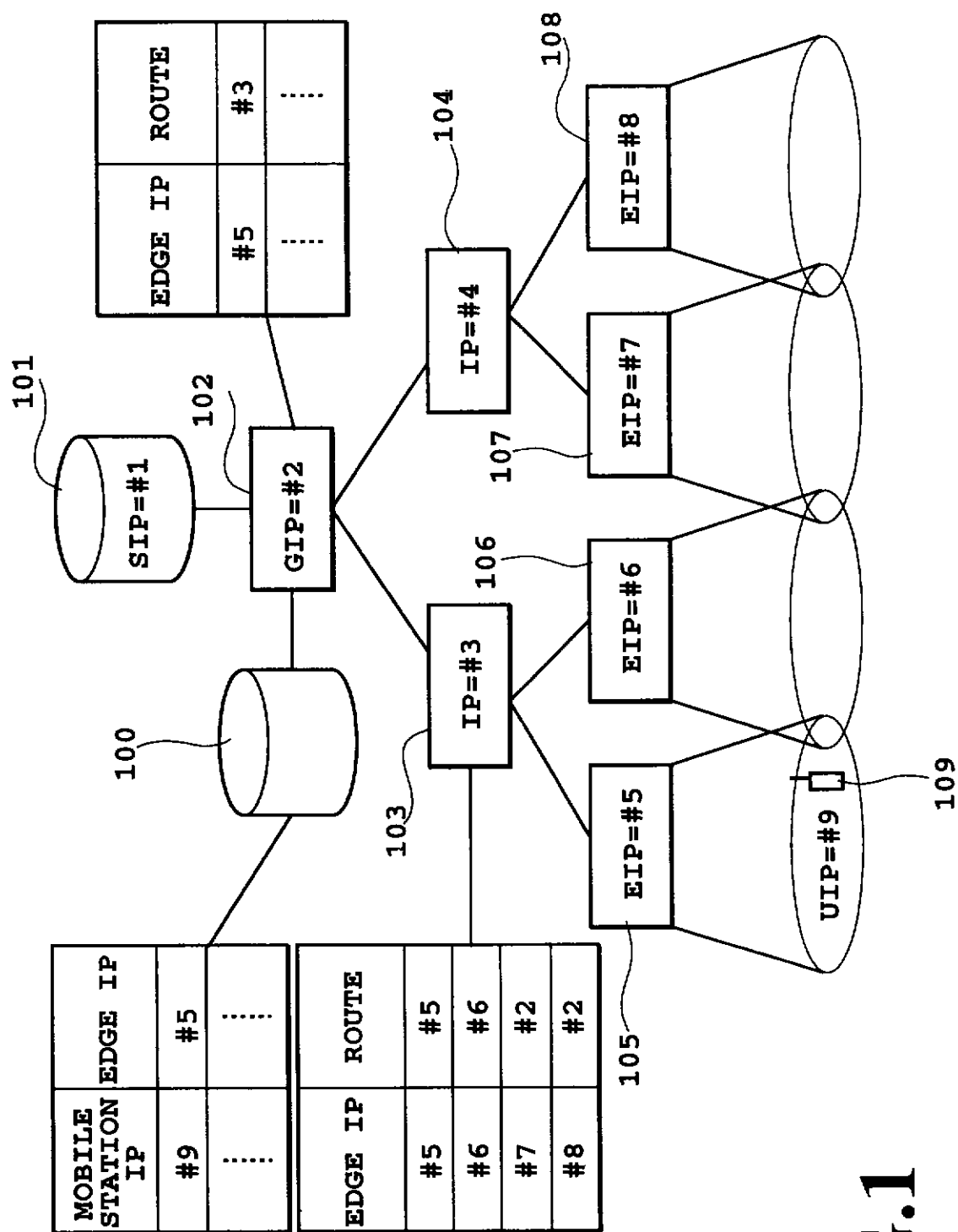
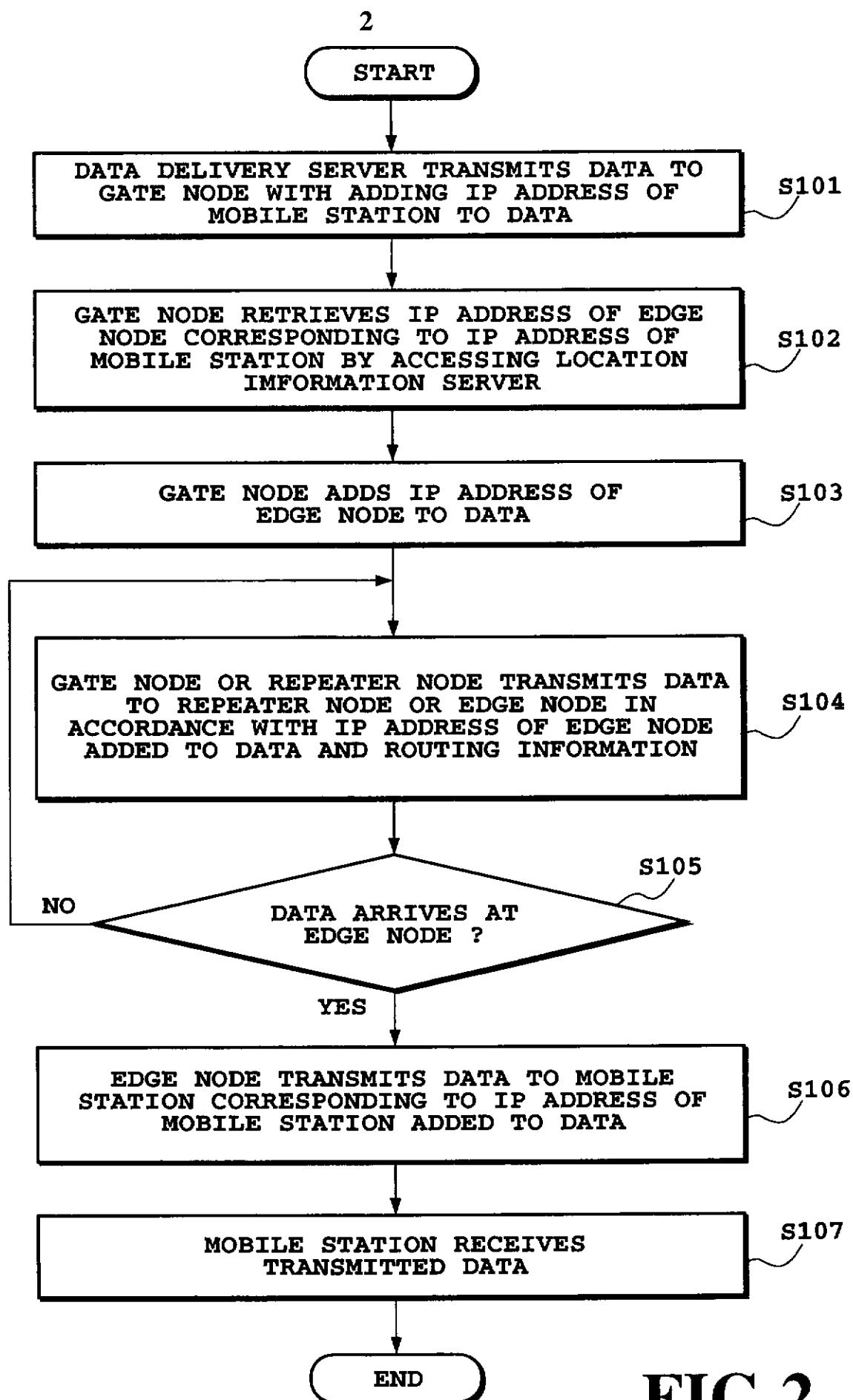


FIG.1

**FIG.2**

3

UD	UIP	DGA	UI
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FIG.3

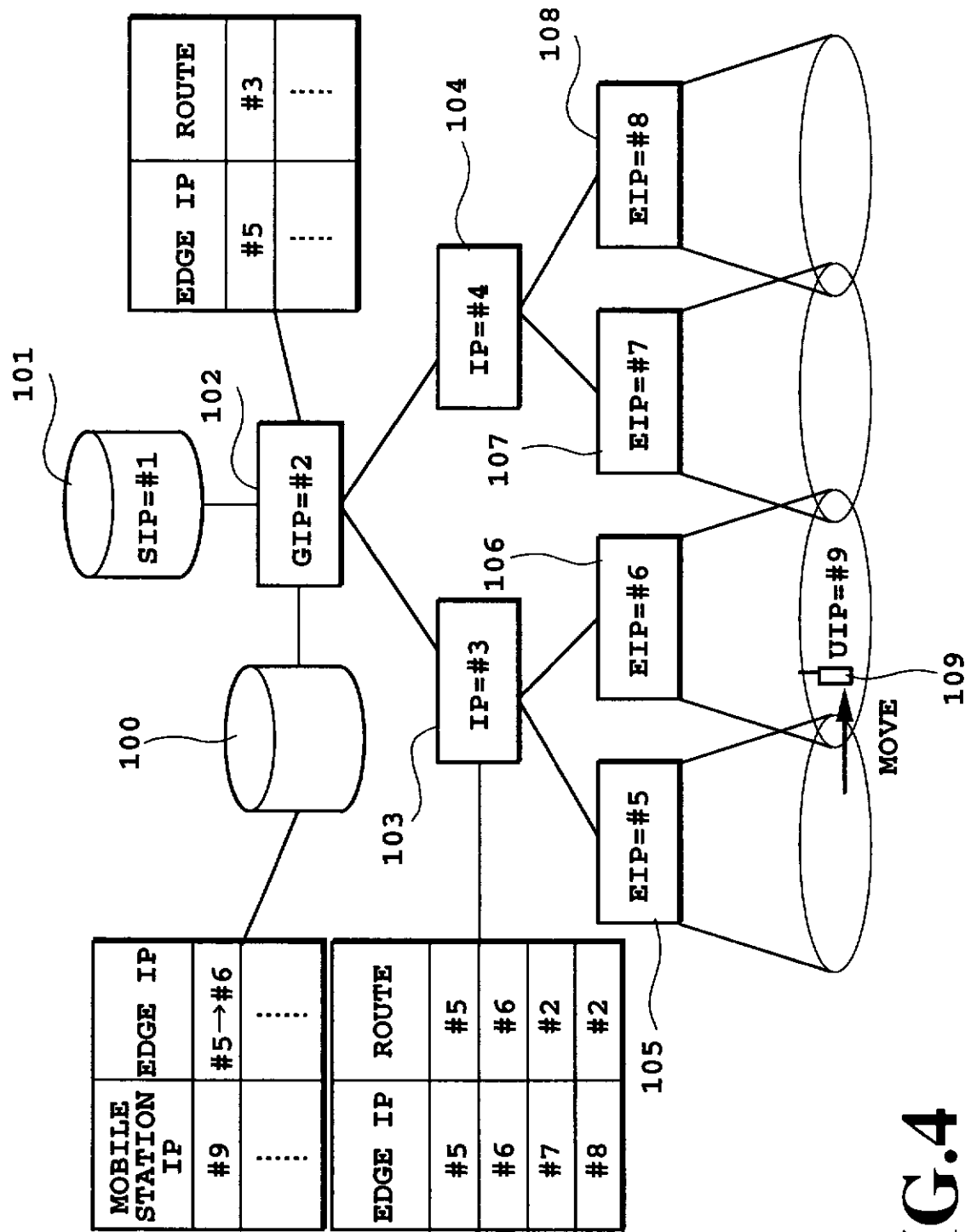
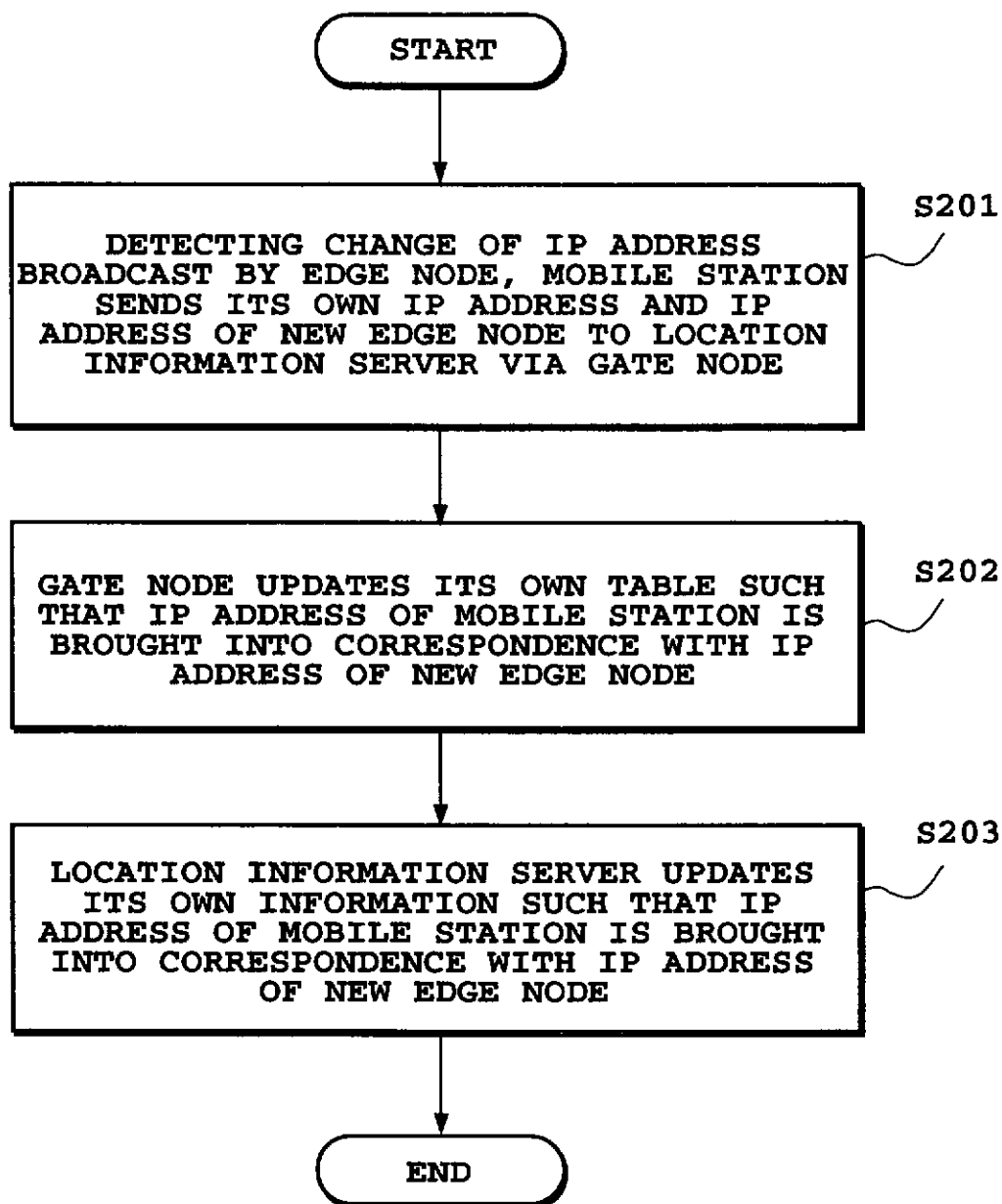


FIG.4

5

**FIG.5**

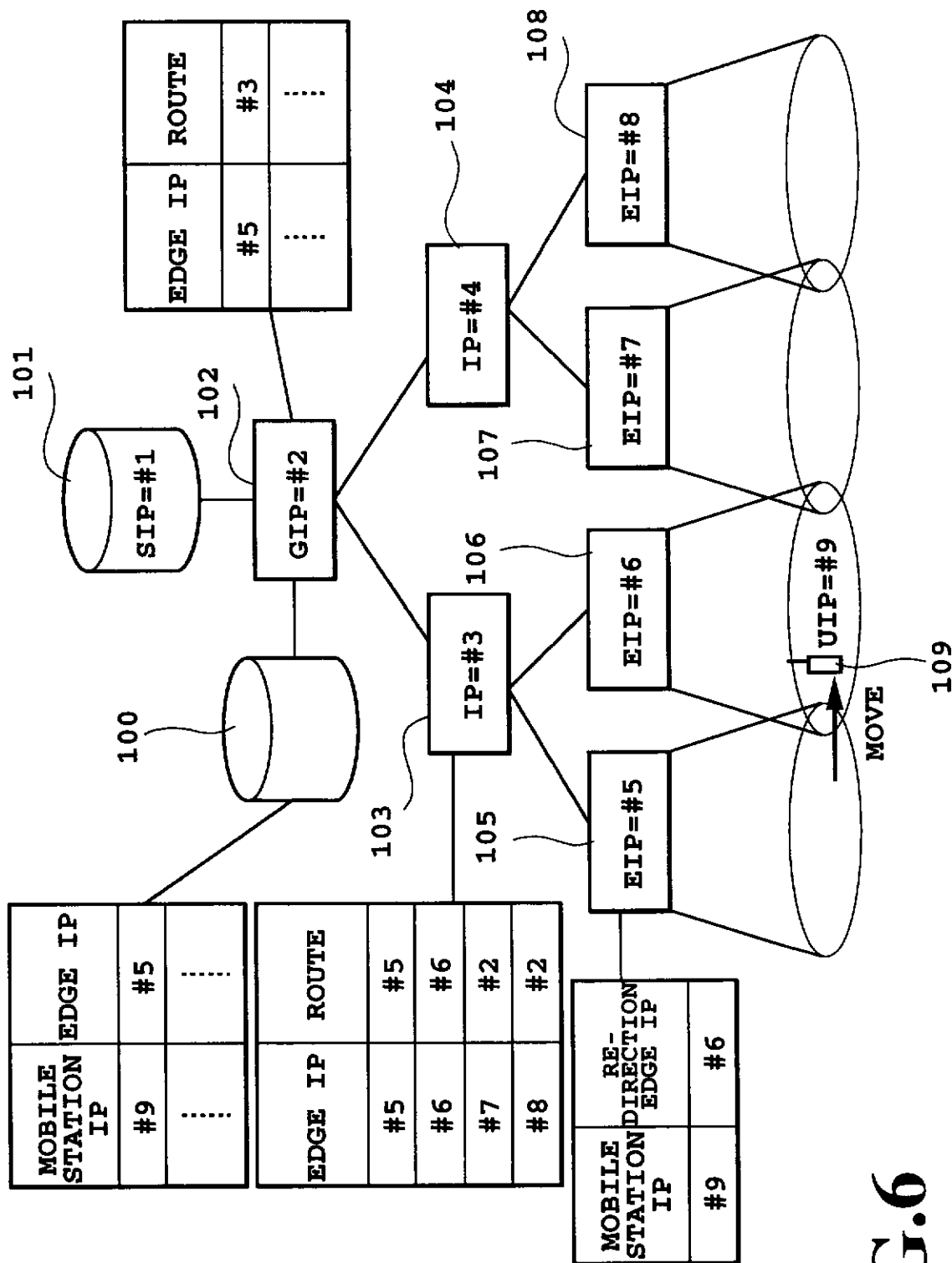
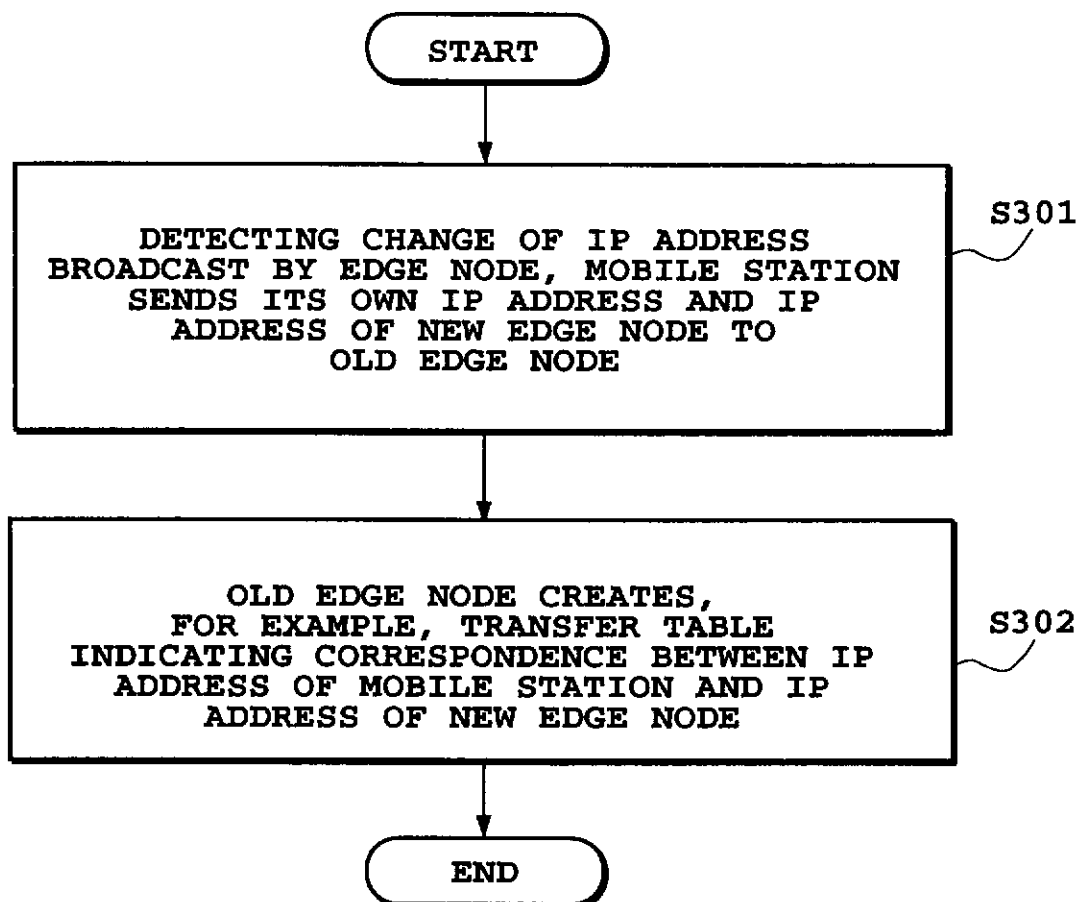


FIG.6

**FIG.7**

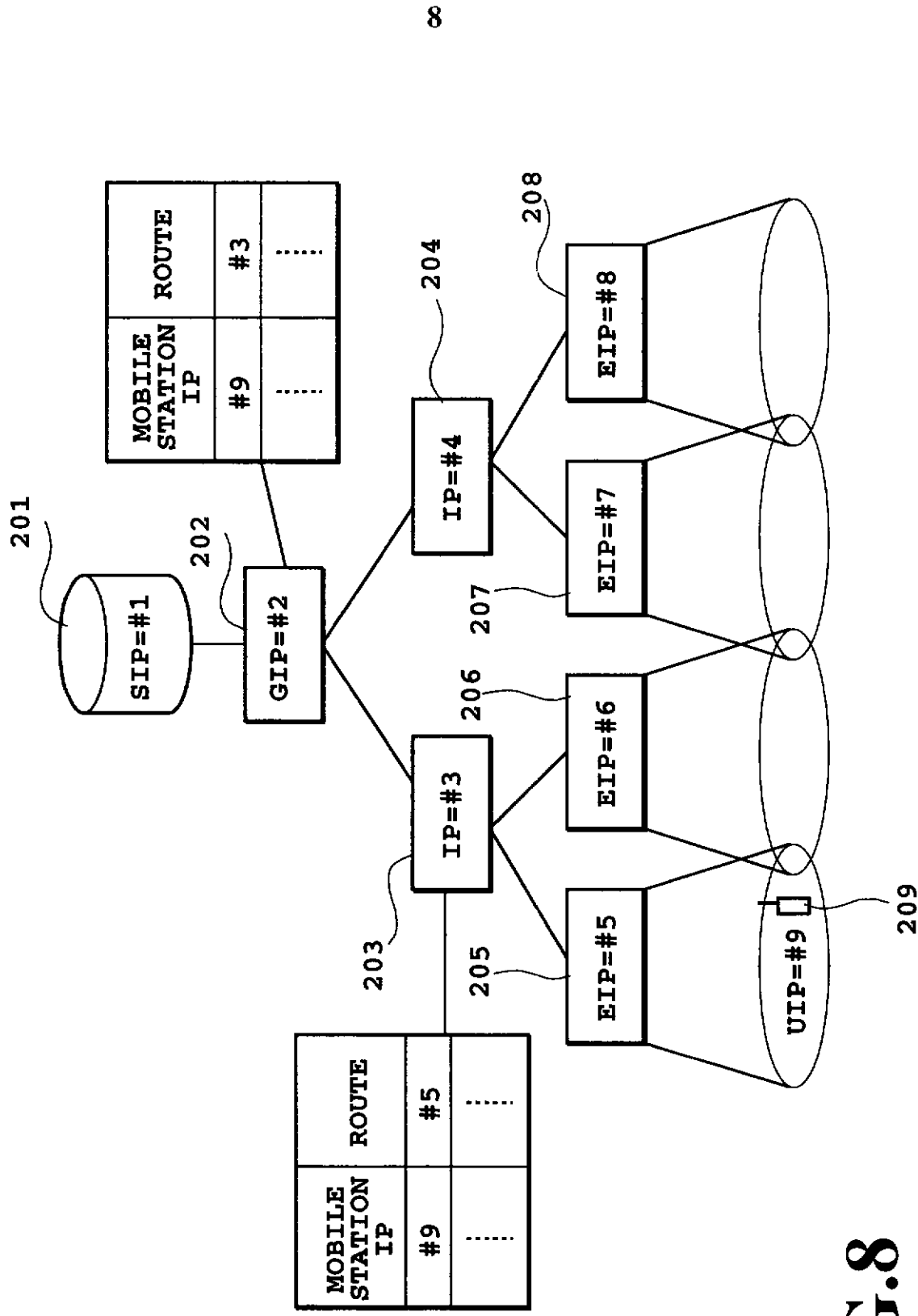
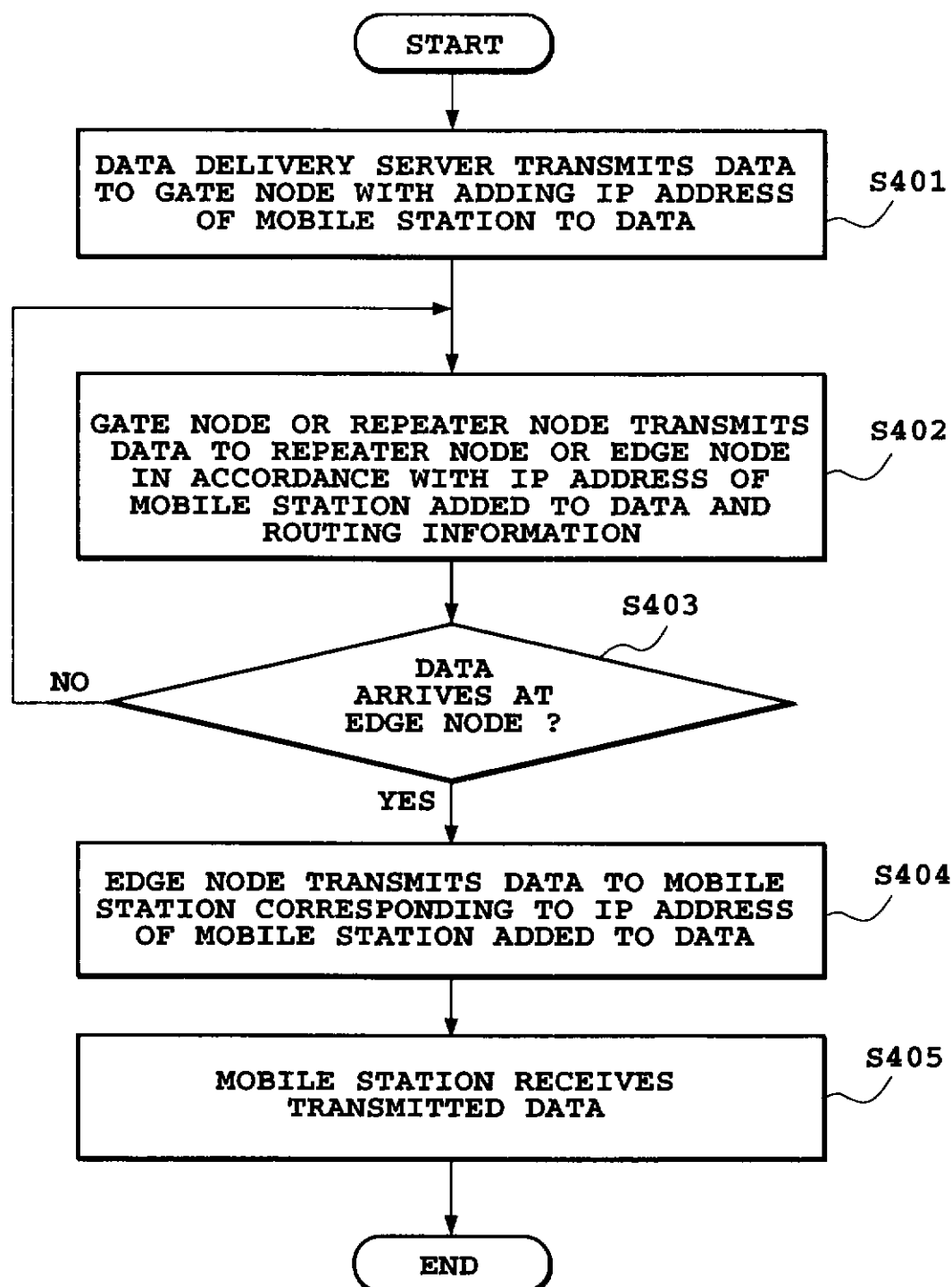


FIG.8

**FIG.9**

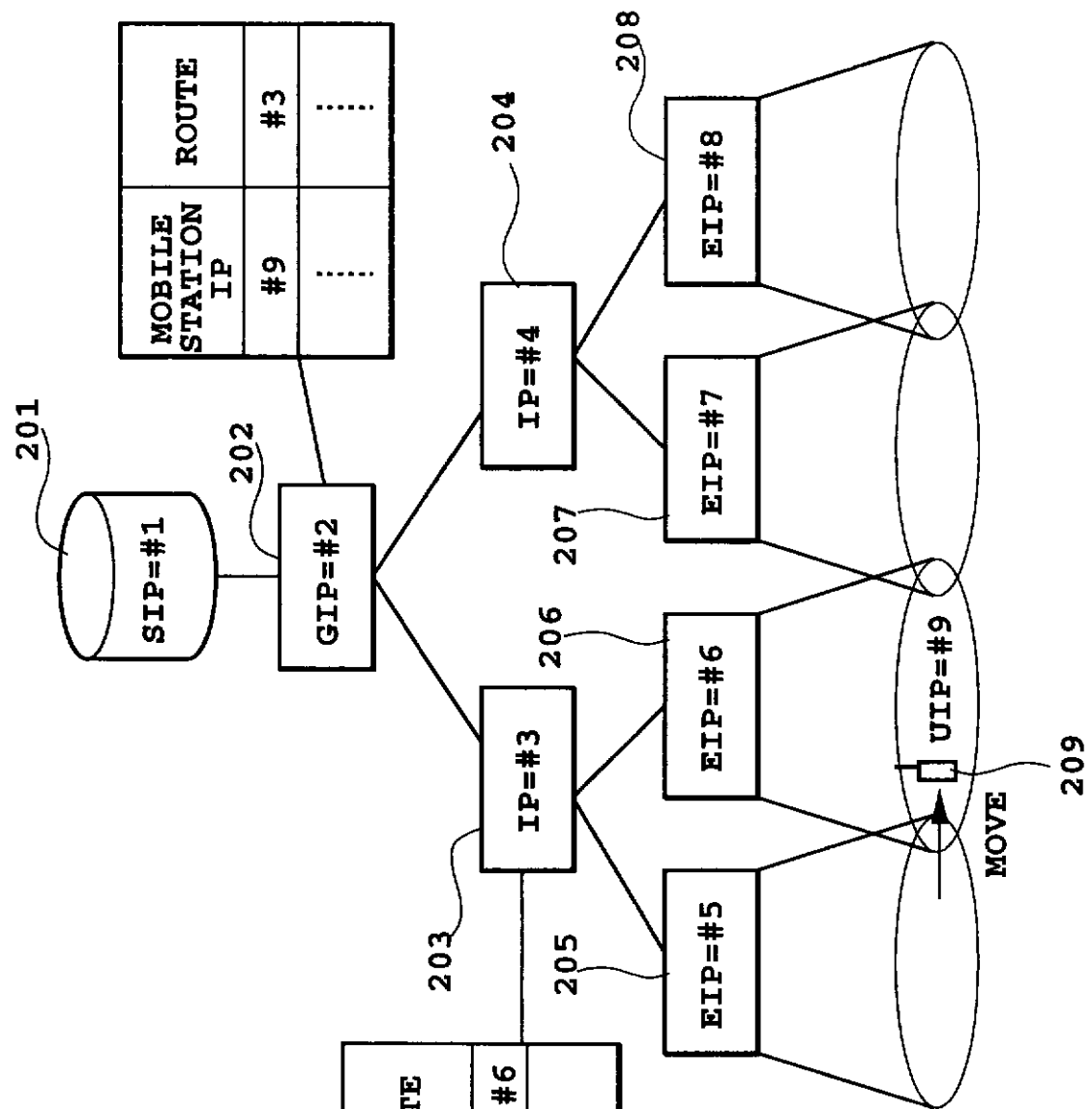
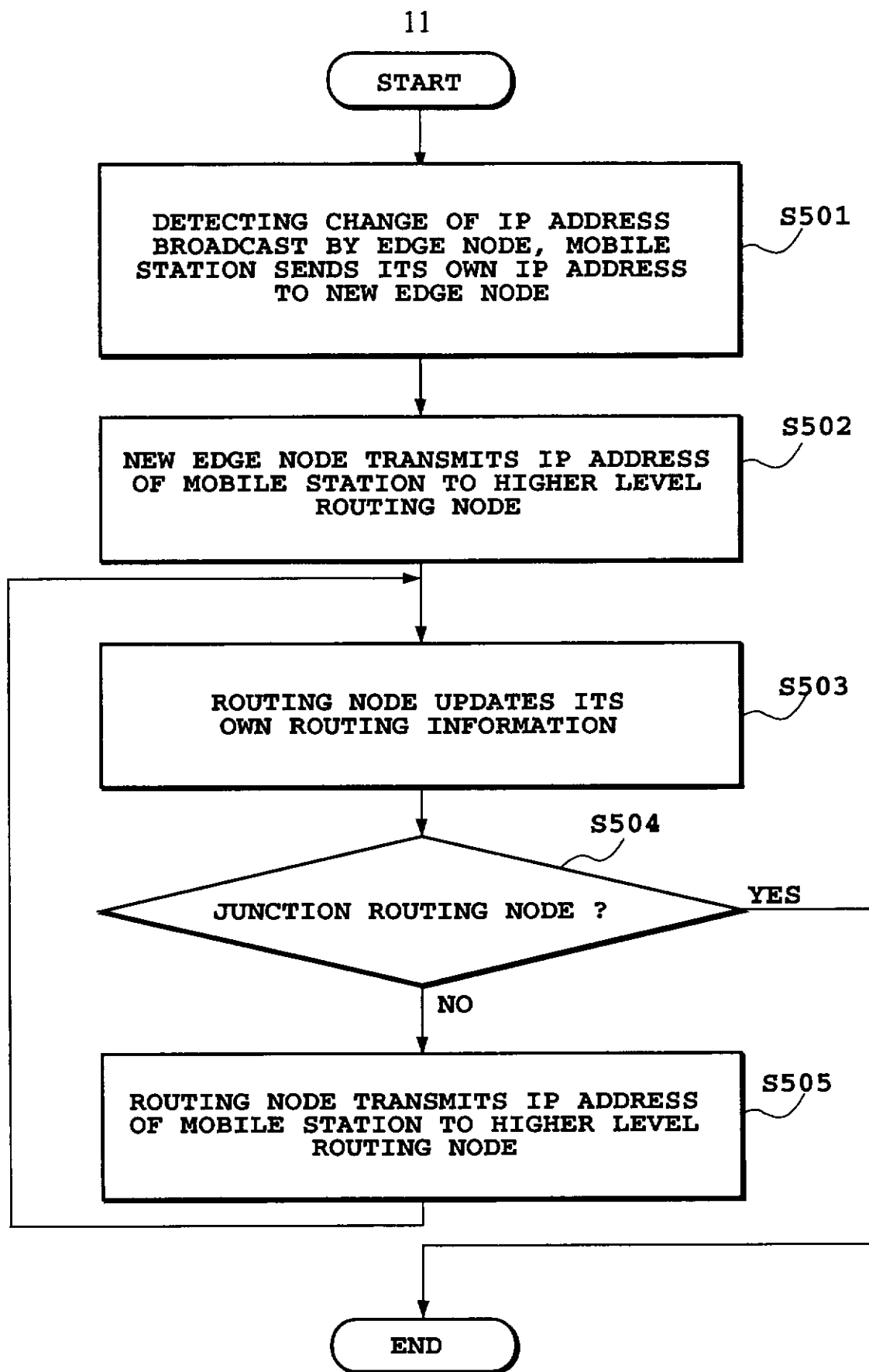


FIG.10

**FIG.11**

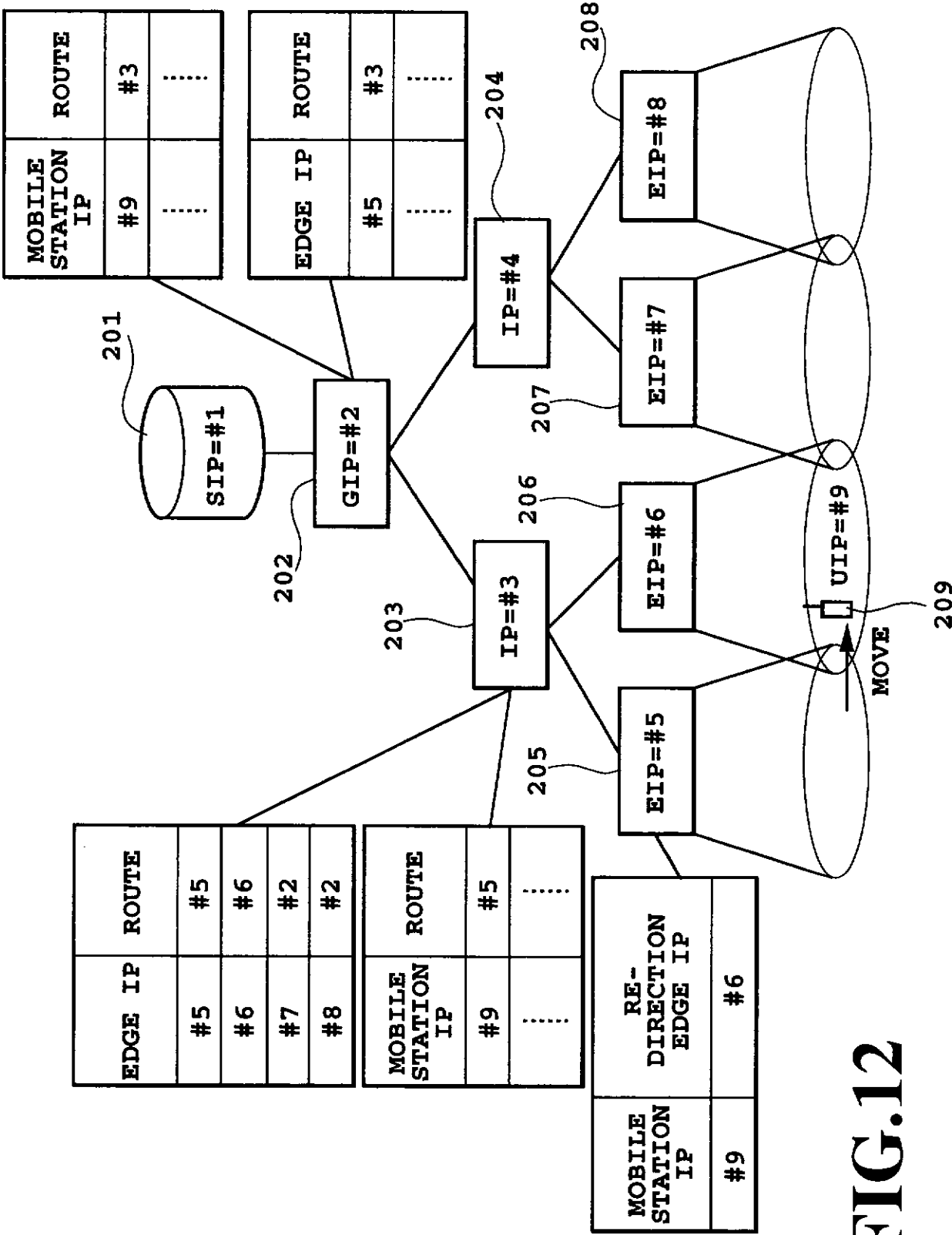
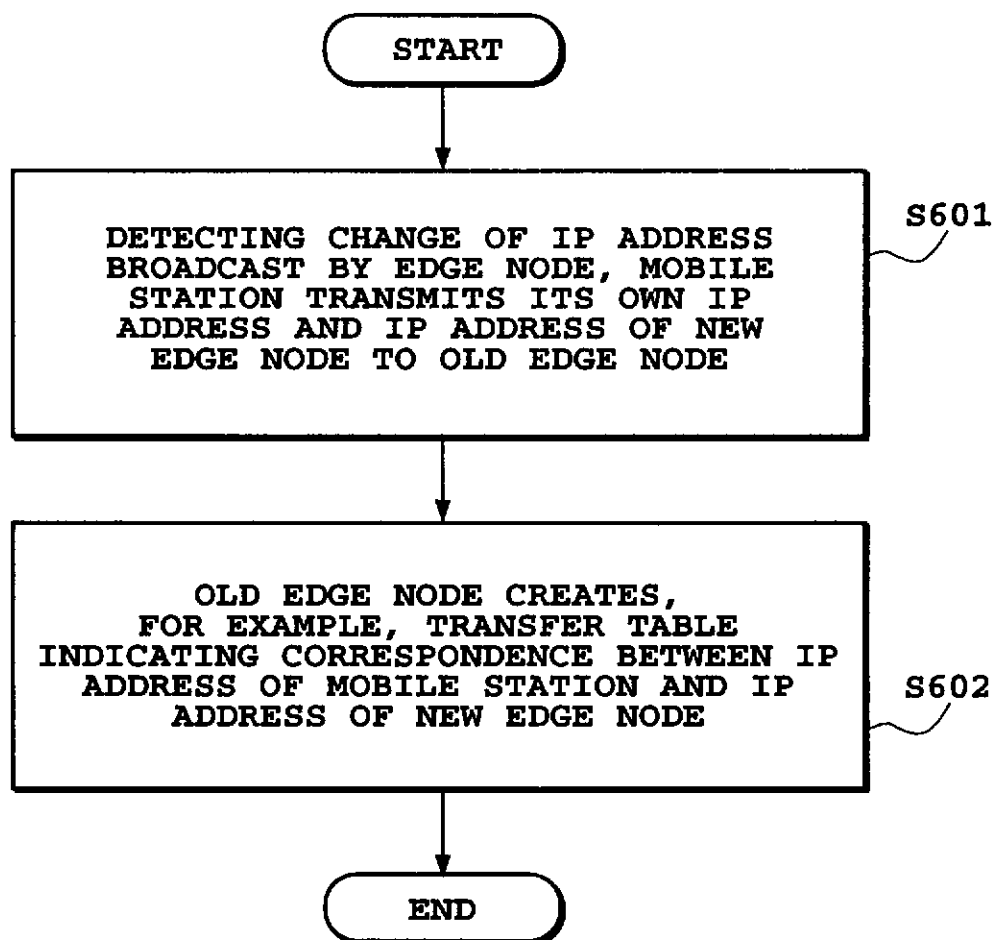


FIG.12

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**FIG.13**

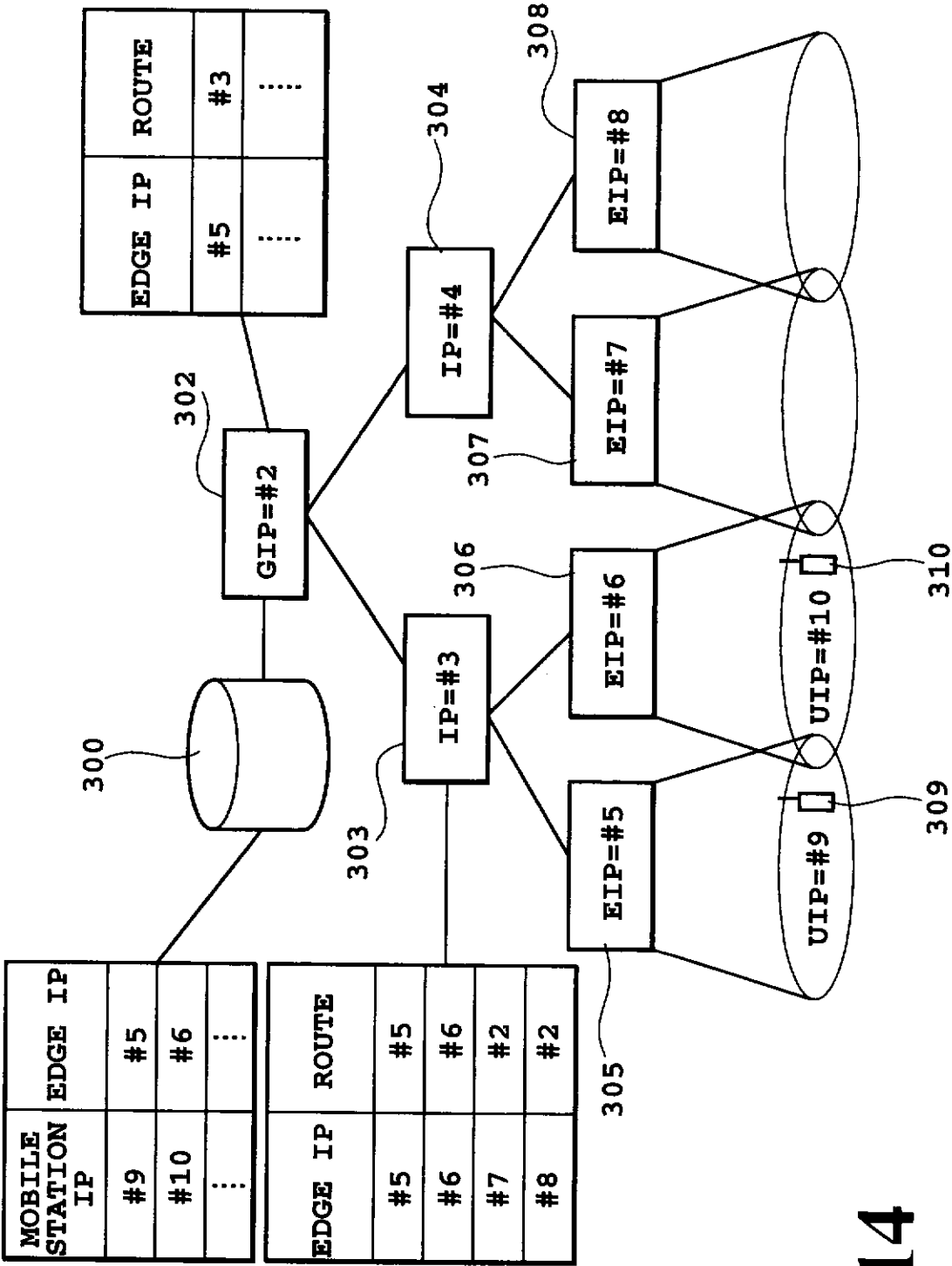
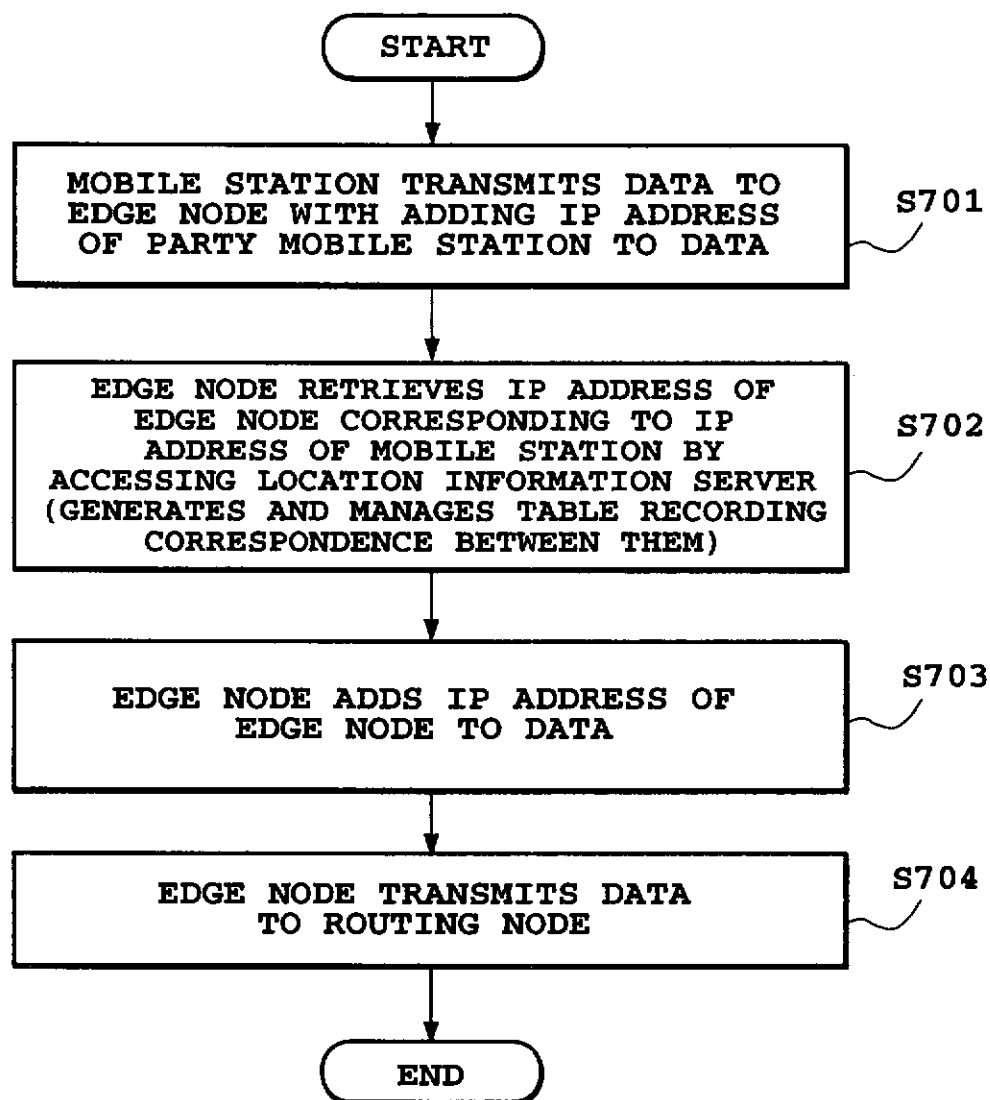
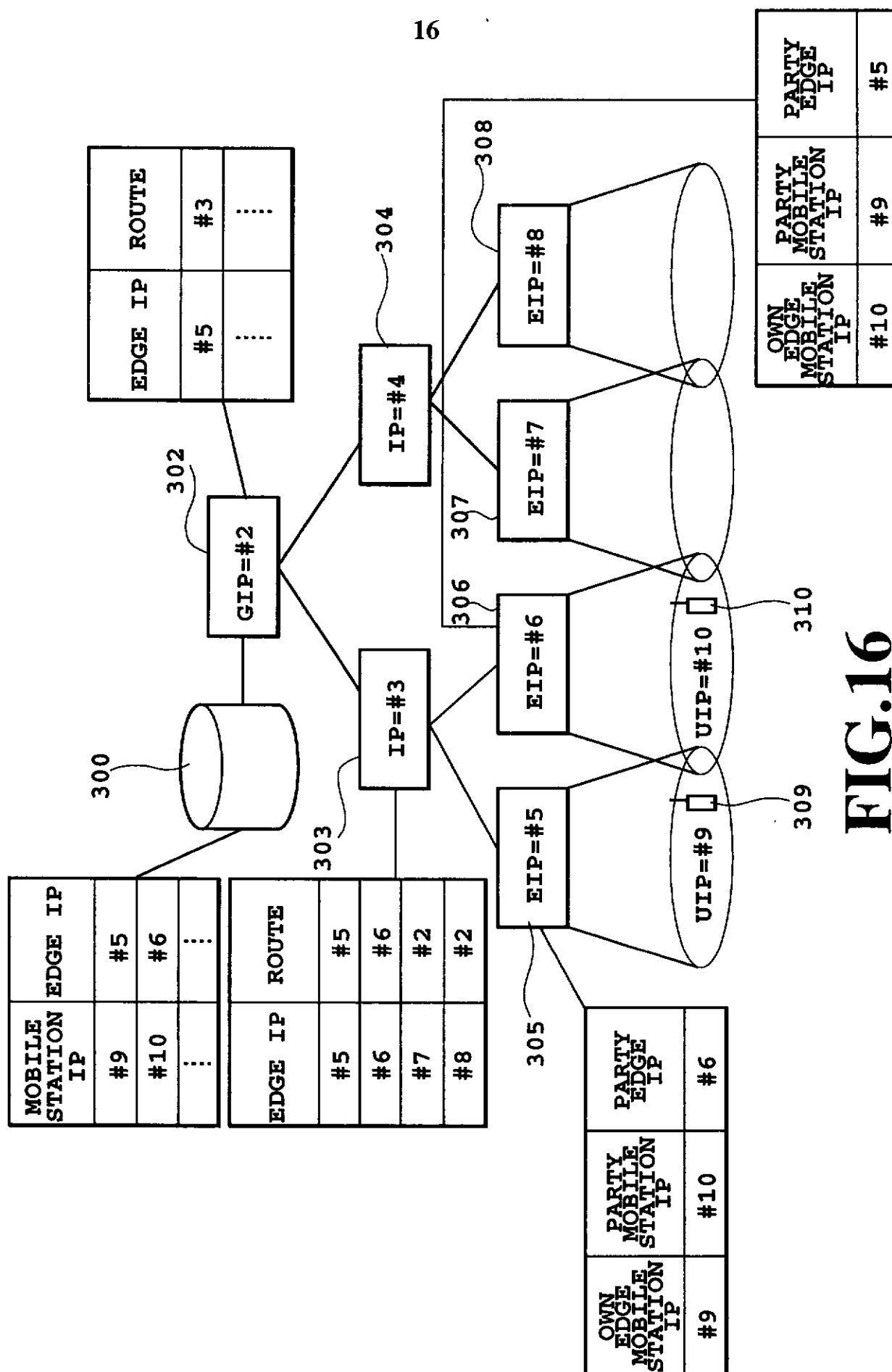


FIG.14

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**FIG.15**



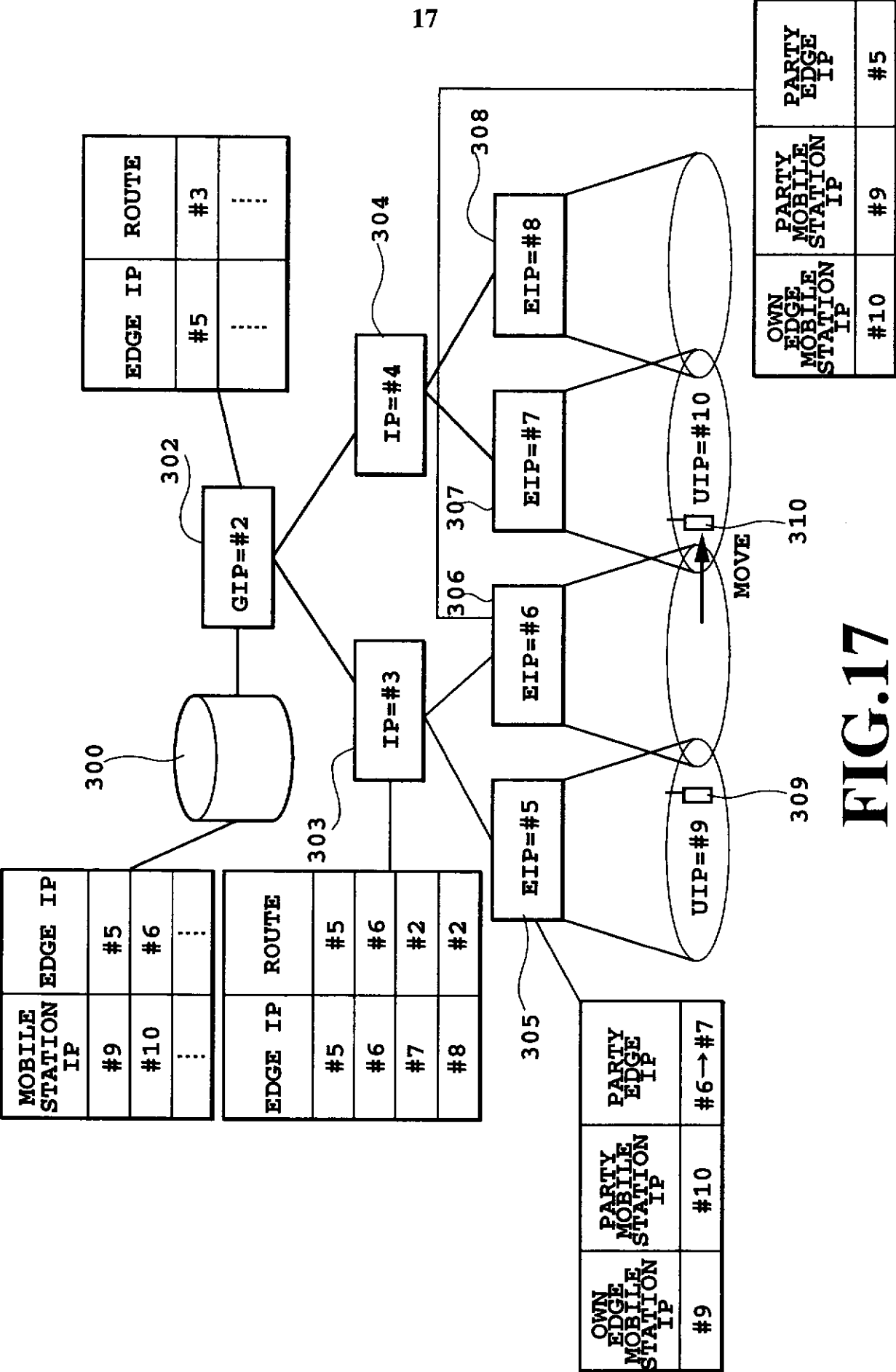


FIG.17

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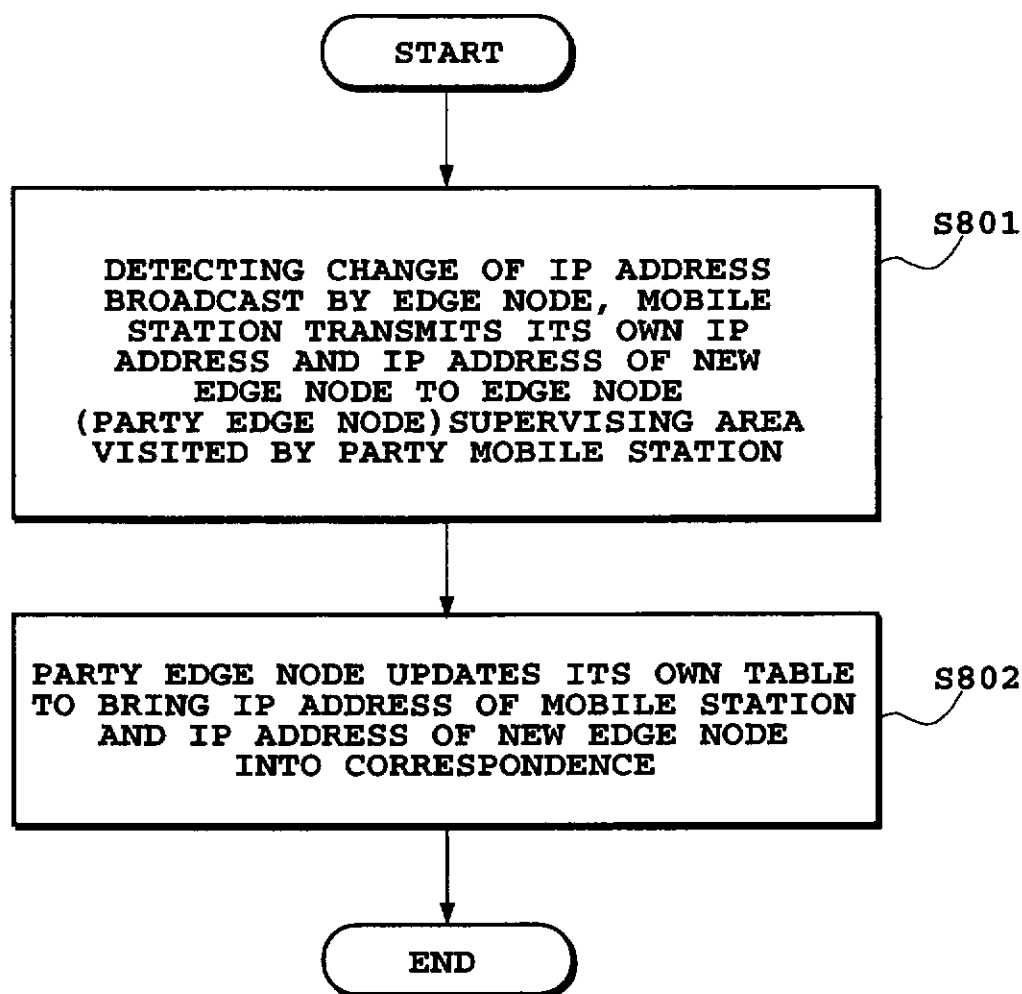
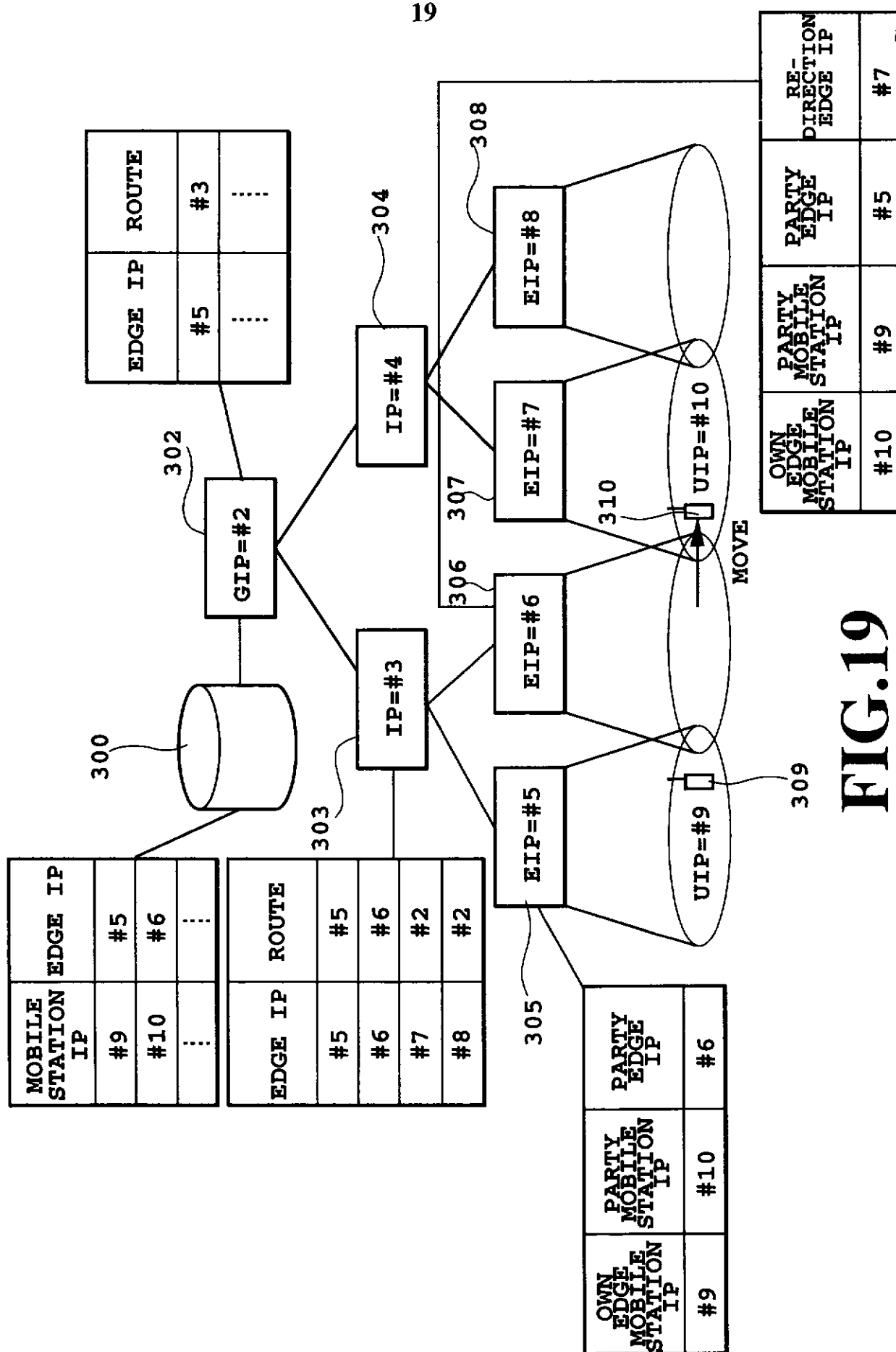
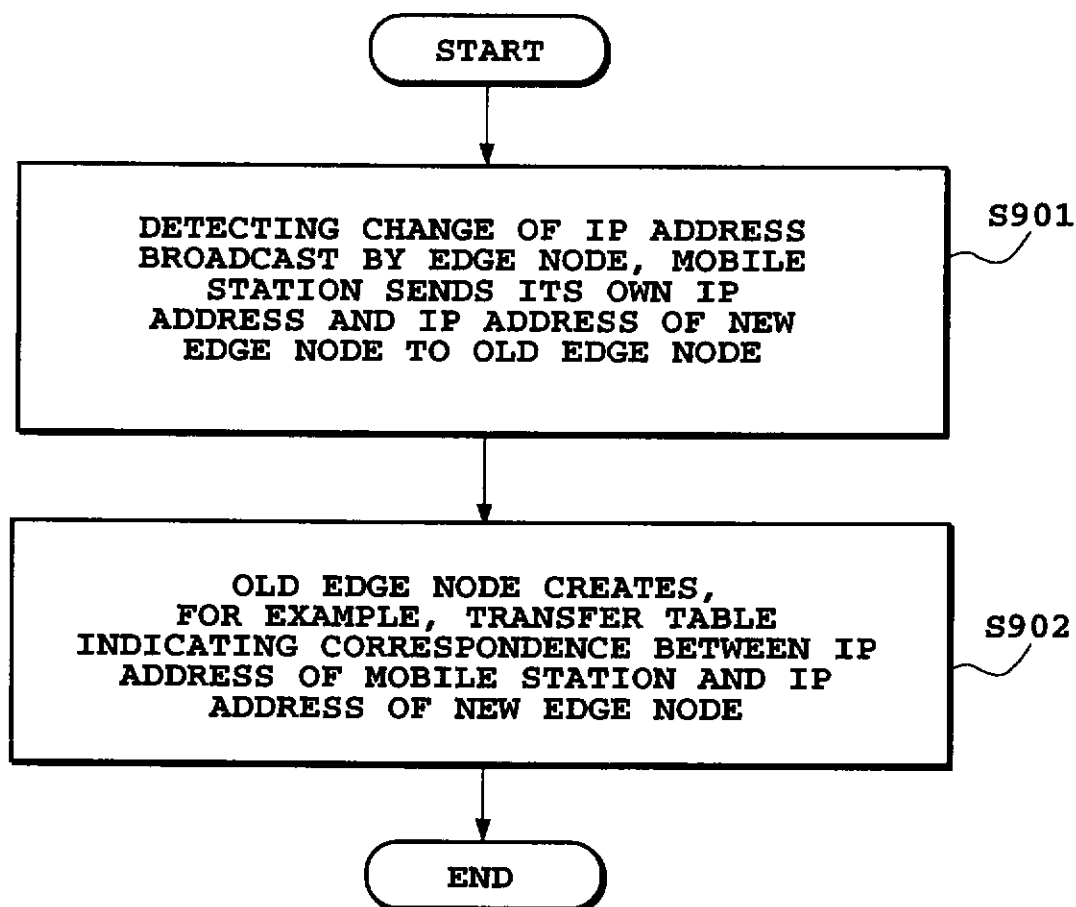


FIG.18



**FIG.20**

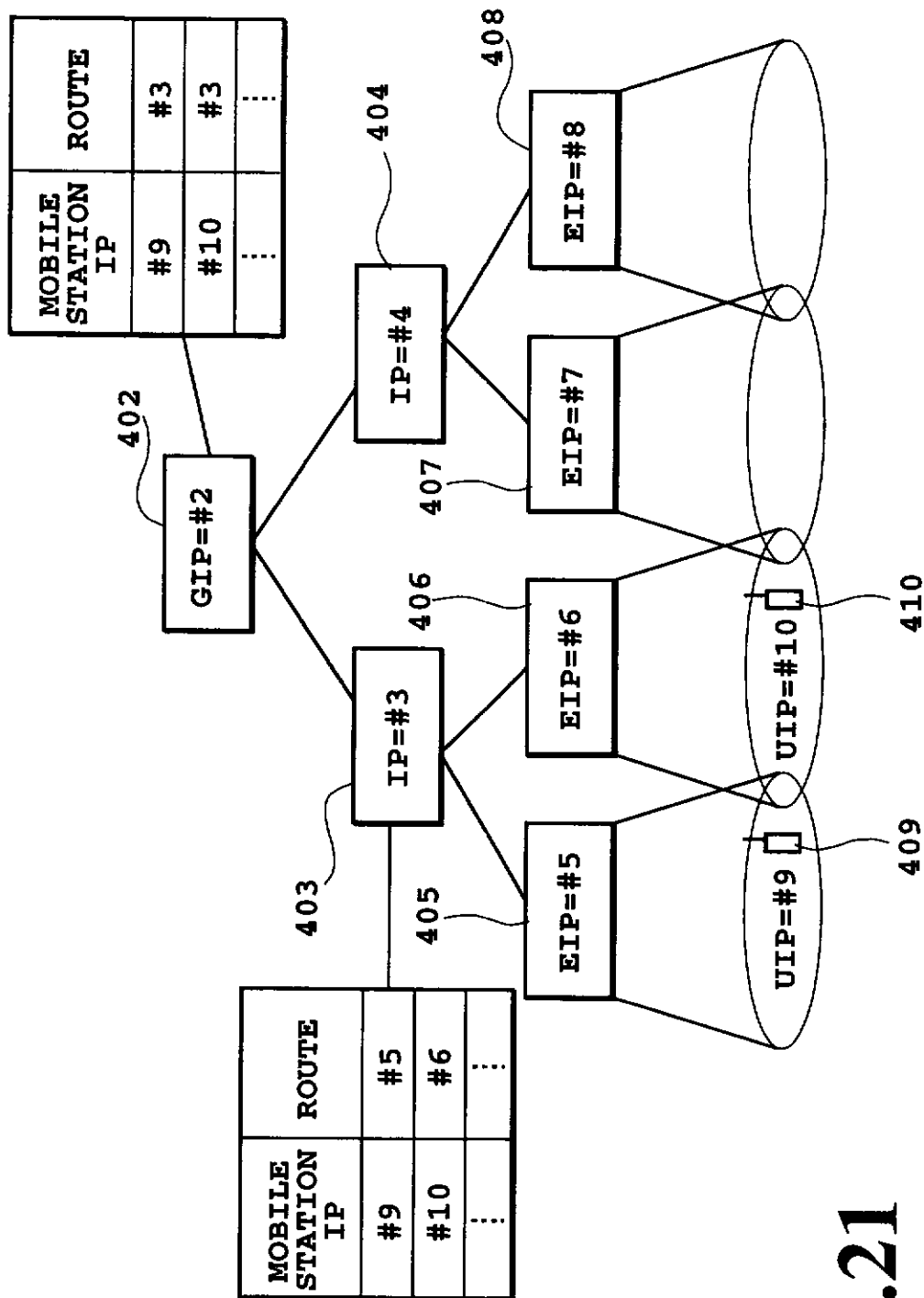
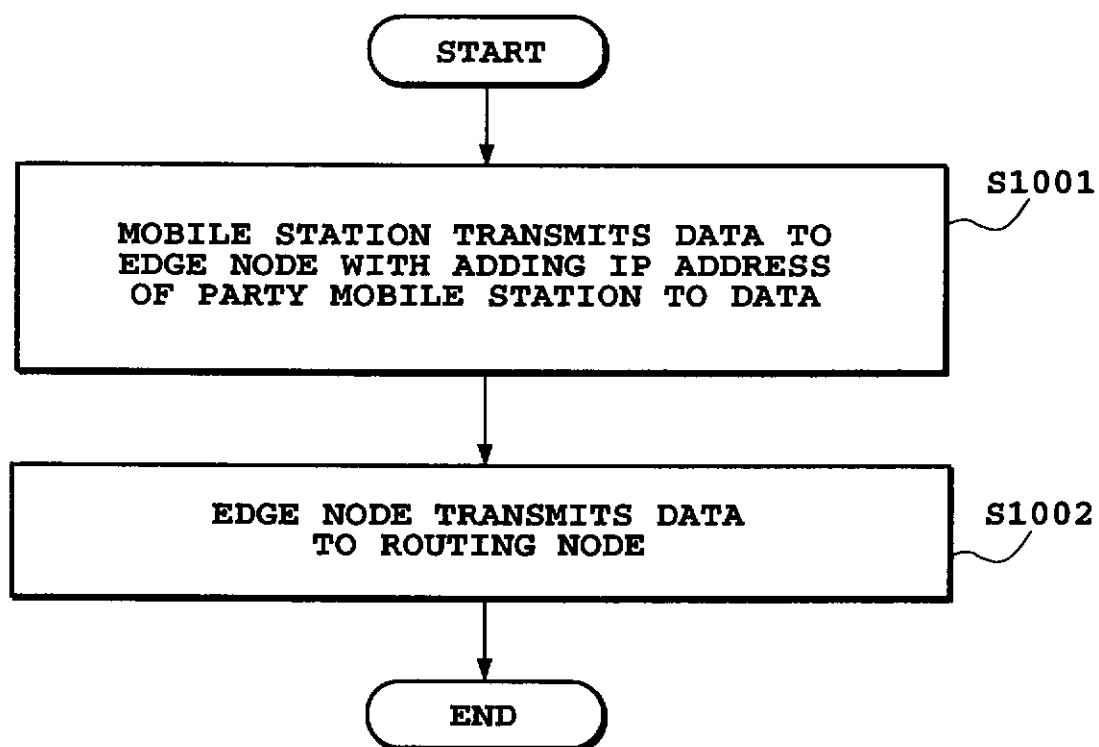


FIG.21

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**FIG.22**

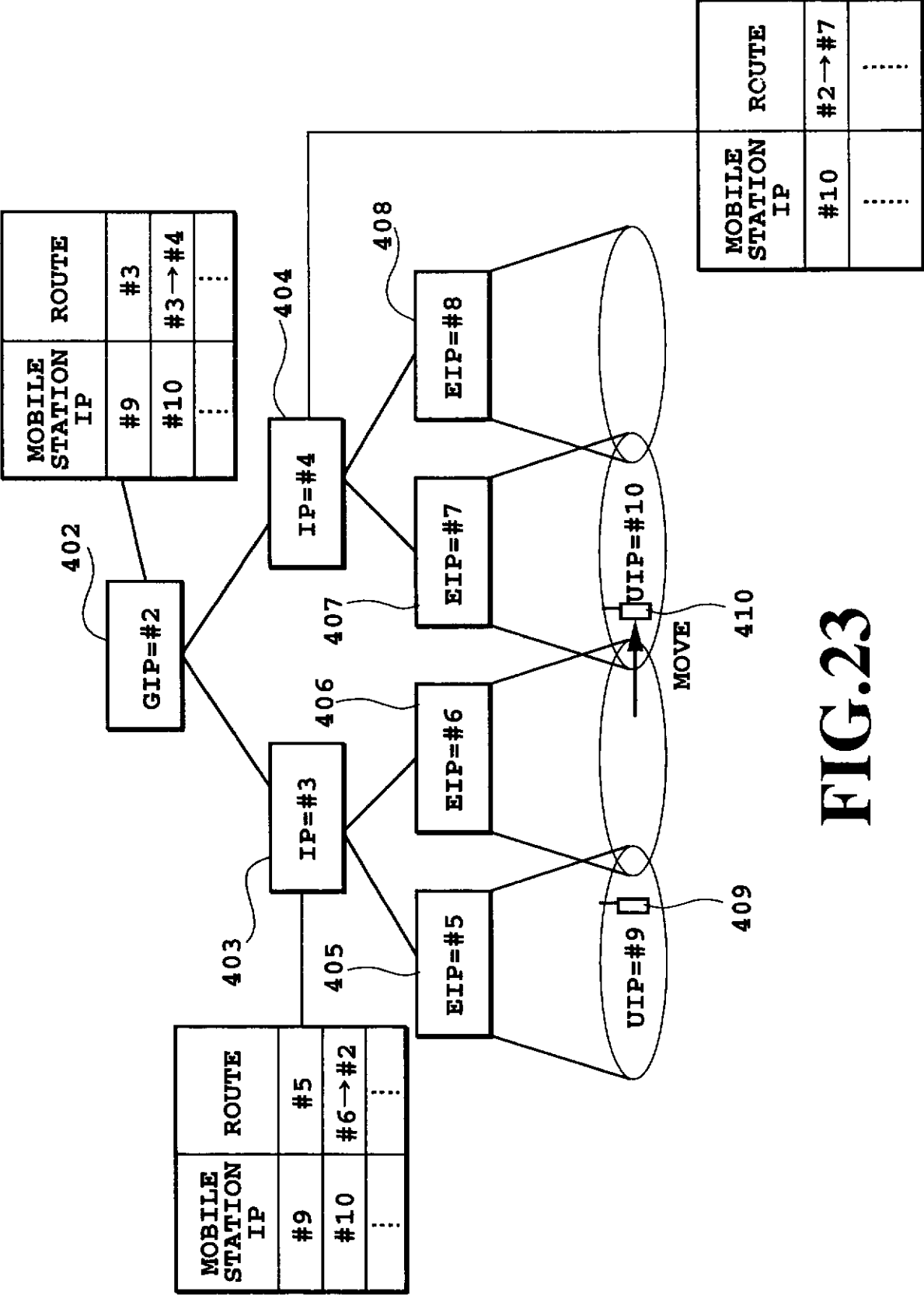


FIG.23

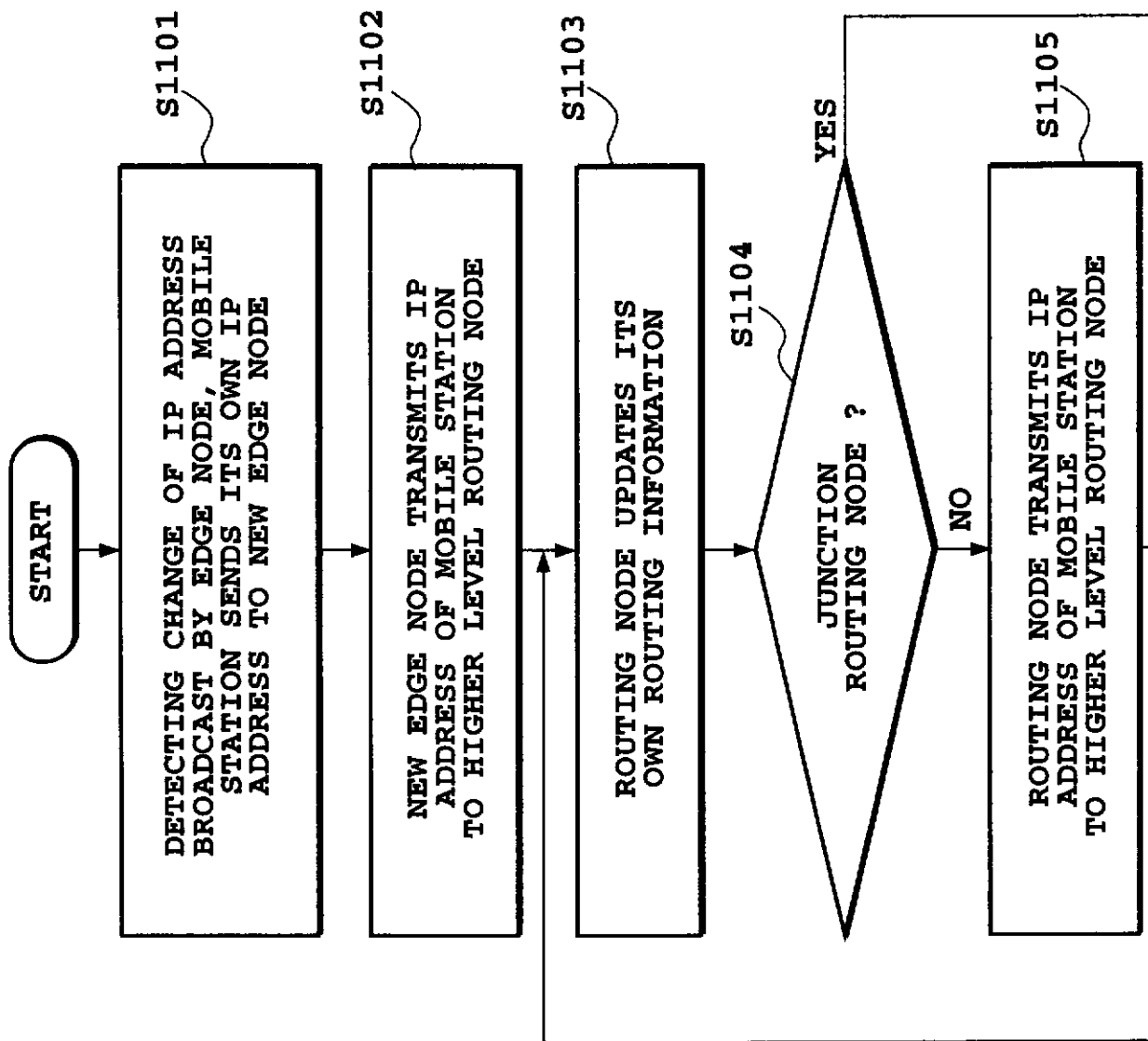


FIG. 24A

FIG. 24

FIG. 24A

FIG. 24B

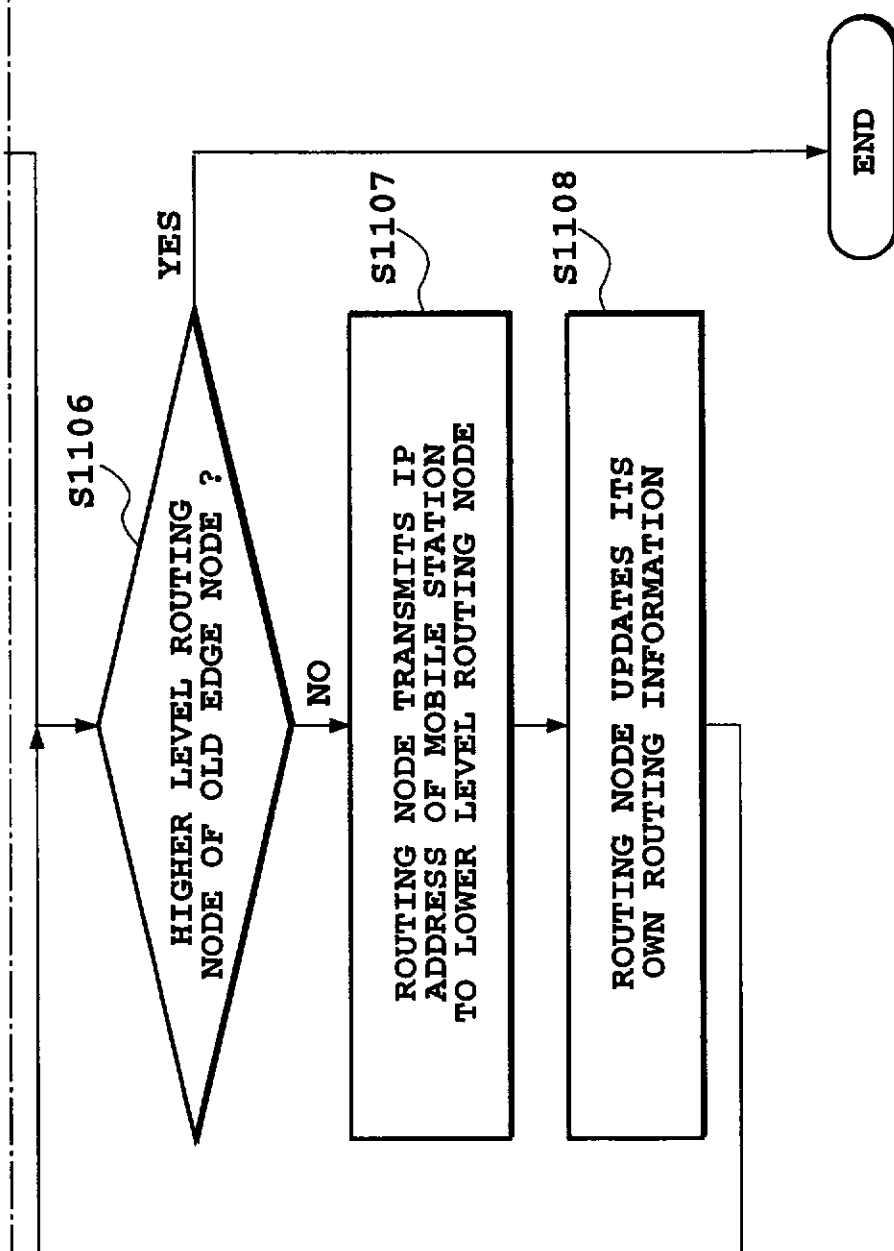


FIG. 24B

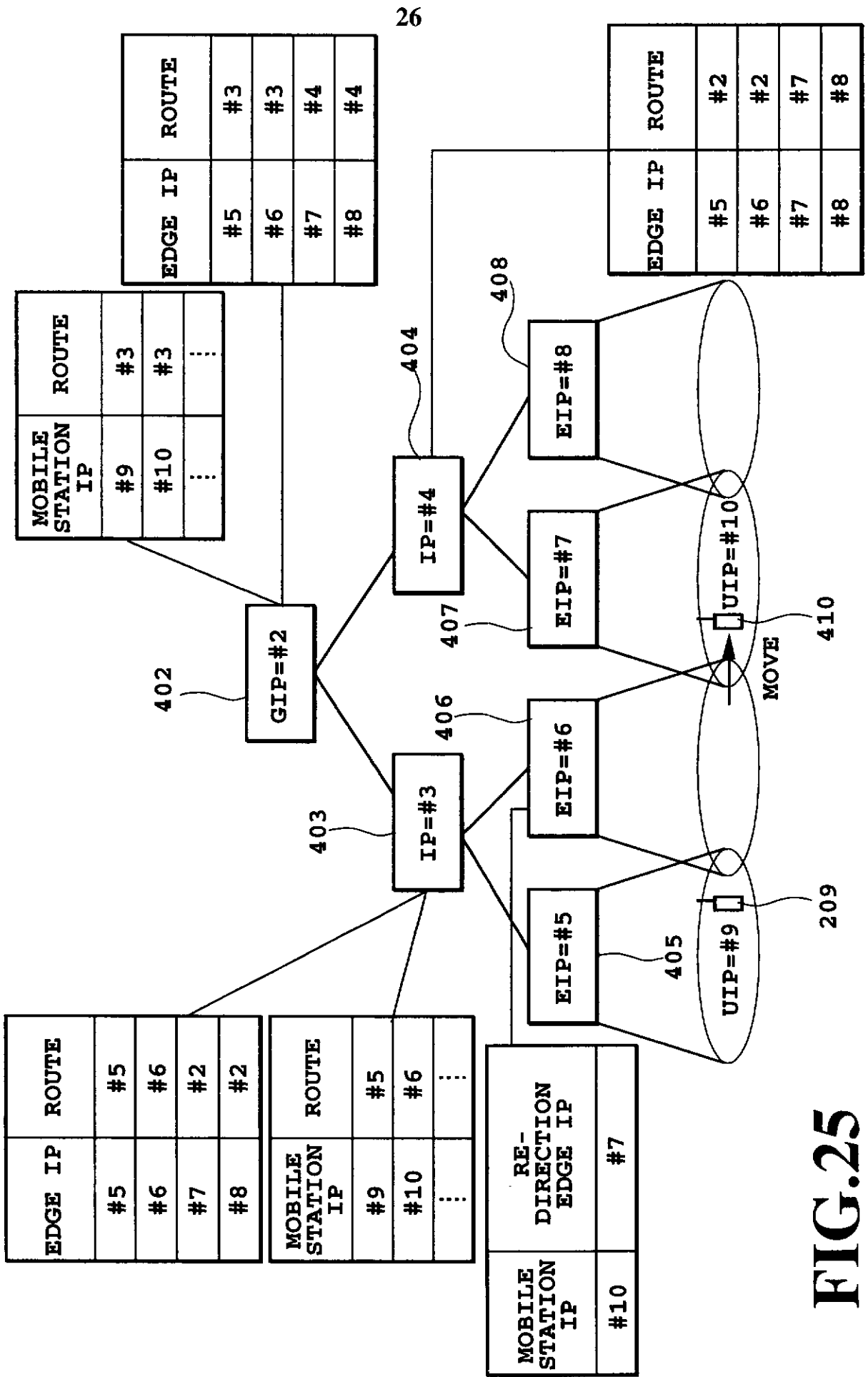
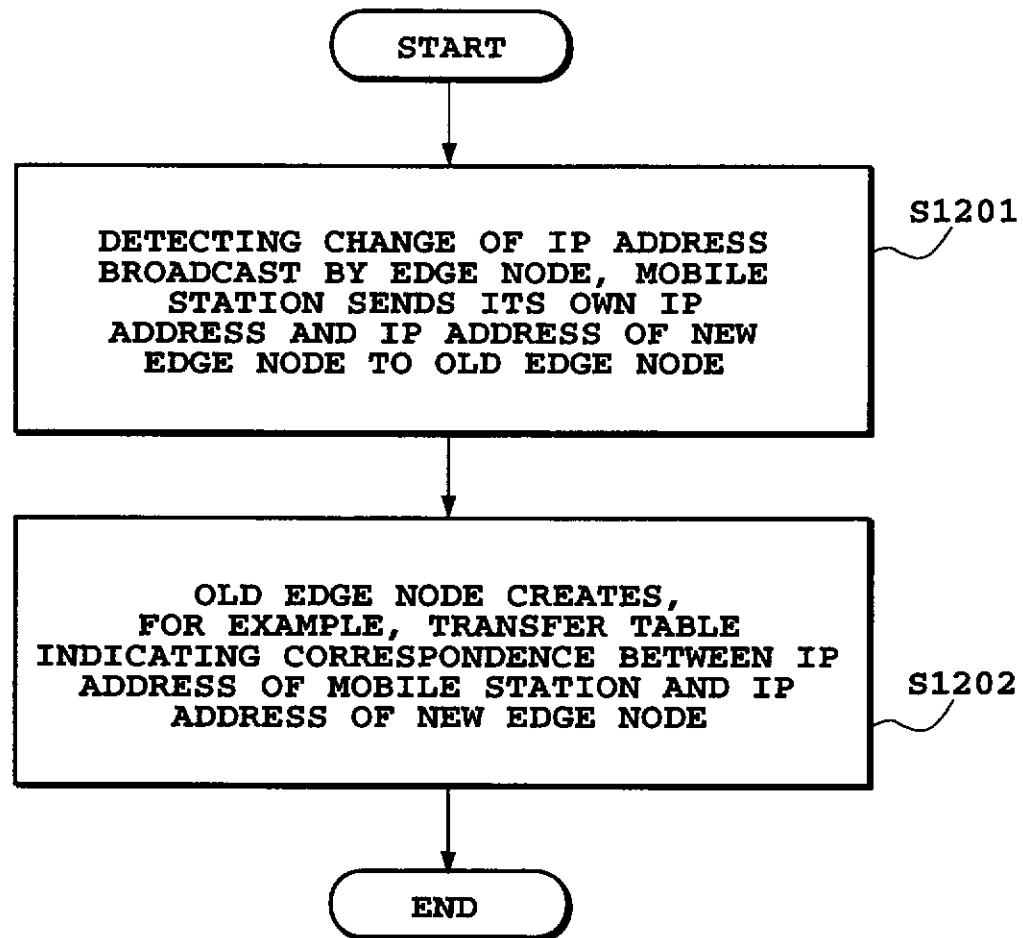


FIG.25

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**FIG.26**