## CLAIMS

1. A method of wireless communication performed by a user equipment (UE), comprising:

receiving a first frame that includes one or more synchronization signal blocks (SSB), each SSB being associated with a respective remaining minimum system information (RMSI) control resource set (CORESET);

determining, for at least one of the SSBs, an SSB index associated with the respective SSB;

determining a time offset of the RMSI CORESET associated with the at least one of the SSBs;

determining a repetition frequency of the RMSI CORESET associated with the at least one of SSBs;

determining, for the at least one of the SSBs, a frame index associated with a frame that includes a monitoring occasion of the RMSI CORESET associated with the at least one of the SSBs based on the respective determined SSB index, time offset and repetition frequency, the determination of the frame index comprising:

determining the frame index such that it is associated with the first frame if

$$((O * 2^{\mu} + [i * M])/\, N_{slot}^{frame,\mu}\,) \bmod 2 = 0, \text{ and}$$

determining the frame index such that it is associated with a second frame after the first frame if

$$((O * 2^{\mu} + [i * M])/\, N_{slot}^{frame,\mu}\,) \bmod 2 = 1;$$

determining a location of the monitoring occasion in the frame associated with the frame index, the determination of the location of the monitoring occasion comprising determining a location of a first slot in time of the monitoring occasion as

$$n_0 = (O * 2^{\mu} + [i * M]) mod\ N_{slot}^{frame,\mu},$$

wherein $n_0$ is a slot index associated with the first slot, *O* is the time offset, *M* is the repetition

frequency in slots, $\mu$ is a numerology index, and $N_{slot}^{frame,\mu}$ is a number of slots per frame;

monitoring, during the monitoring occasion in the frame associated with the frame index, for the RMSI CORESET associated with the at least one of the SSBs; and

receiving the RMSI CORESET associated with the at least one of the SSBs based on the monitoring.

2. The method of claim 1, wherein the first frame includes an RMSI CORESET monitoring configuration that indicates the time offset and the repetition frequency.

3. The method of claim 1, wherein the determination of the frame index comprises determining the frame index such that

$$\text{SFN mod } 2 = ((O * 2^{\mu} + [i * M]) / N_{slot}^{frame,\mu}) \bmod 2$$

is satisfied, wherein SFN is the frame index.

4. The method of claim 1, wherein the time offset is 5 milliseconds, the repetition frequency is 2 slots, and the SSB index is greater than 60.

5. The method of claim 1, wherein the time offset is not equal to 5 milliseconds, the repetition frequency is not equal to 2 slots, and the SSB index is not greater than 60, and wherein the determination of the frame index comprises determining the frame index such that it is associated with the first frame based on the time offset being not equal to 5 milliseconds, the repetition frequency being not equal to 2 slots, and the SSB index being not greater than 60.

6. A user equipment (UE) comprising:

at least one processor; and

at least one memory communicatively coupled with the at least one processor and storing processor-readable code that, when executed by the at least one processor is configured to cause the UE to:

receive a first frame that includes one or more synchronization signal blocks (SSB), each

SSB being associated with a respective remaining minimum system information (RMSI) control resource set (CORESET);

determine, for at least one of the SSBs, an SSB index associated with the respective SSB;

determine a time offset of the RMSI CORESET associated with the at least one of the SSBs;

determine a repetition frequency of the RMSI CORESET associated with the at least one of SSBs;

determine, for the at least one of the SSBs, a frame index associated with a frame that includes a monitoring occasion of the RMSI CORESET associated with the at least one of the SSBs based on the respective determined SSB index, time offset and repetition frequency, the determination of the frame index comprising:

determining the frame index such that it is associated with the first frame if

$$((O * 2^{\mu} + [i * M])/ N_{slot}^{frame,\mu}) \bmod 2 = 0, \text{ and}$$

determining the frame index such that it is associated with a second frame after the first frame if

$$((O * 2^{\mu} + [i * M])/ N_{slot}^{frame,\mu}) \bmod 2 = 1;$$

determine a location of the monitoring occasion in the frame associated with the frame index, the determination of the location of the monitoring occasion comprising determining a location of a first slot in time of the monitoring occasion as

$$n_0 = (O * 2^{\mu} + [i * M]) mod\ N_{slot}^{frame,\mu},$$

wherein $n_0$ is a slot index associated with the first slot, *O* is the time offset, *M* is the repetition frequency in slots, $\mu$ is a numerology index, and $N_{slot}^{frame,\mu}$ is a number of slots per frame;

monitor, during the monitoring occasion in the frame associated with the frame index, for the RMSI CORESET associated with the at least one of the SSBs; and

receive the RMSI CORESET associated with the at least one of the SSBs based on the

monitoring.

7. The UE of claim 6, wherein the first frame includes an RMSI CORESET monitoring configuration that indicates the time offset and the repetition frequency.

8. The UE of claim 6, wherein the determination of the frame index comprises determining the frame index such that

$$\text{SFN mod } 2 = ((O * 2^{\mu} + [i * M]) / N_{slot}^{frame,\mu}) \text{ mod } 2$$

is satisfied, wherein SFN is the frame index.

9. The UE of claim 6, wherein the time offset is 5 milliseconds, the repetition frequency is 2 slots, and the SSB index is greater than 60.

10. The UE of claim 6, wherein the time offset is not equal to 5 milliseconds, the repetition frequency is not equal to 2 slots, and the SSB index is not greater than 60, and wherein the determination of the frame index comprises determining the frame index such that it is associated with the first frame based on the time offset being not equal to 5 milliseconds, the repetition frequency being not equal to 2 slots, and the SSB index being not greater than 60.