

**CARRIER COMPRISING AN ELECTRICAL-CHARGE-TRAPPING LAYER FOR A
COMPOSITE SUBSTRATE AND METHOD OF SELECTING SUCH A CARRIER
ABSTRACT**

The invention relates to a carrier (1) for a composite substrate (S). The carrier comprises a base substrate and a trapping layer (3a) of polycrystalline silicon disposed on the base substrate (2). The trapping layer has electrical traps of a first type having an activation energy of 0.383 eV to within 0.008 eV and an effective capture cross section for holes, and for electrons, that is less than 10^{-16} cm². The trapping layer has electrical traps of a second type having an activation energy of 0.428 eV to within 0.016 eV and an effective capture cross section for holes, and for electrons, that is less than 10^{-16} cm².

Fig. 1