WHAT IS CLAIMED IS:

1. A method for indicating a position of a synchronous signal block, applied to a network apparatus, comprising:

determining an actual transmission position of a synchronous signal block of at least one first type of cell; and

sending, by a signaling, the actual transmission position of the synchronous signal block of the at least one first type of cell to a terminal apparatus; wherein the terminal apparatus is located within a range of a second type of cell managed by the network apparatus, and the first type of cell is a neighboring cell of the second type of cell, and wherein the signaling is broadcast signaling or radio resource control (RRC) dedicated signaling,

wherein, the sending, by the signaling, the actual transmission position of the synchronous signal block of the at least one first type of cell to the terminal apparatus, comprises:

generating a bitmap based on the actual transmission position of the synchronous signal block of the at least one first type of cell, and sending the bitmap to the terminal apparatus by the signaling,

wherein the bitmap is used to notify the actual transmission position of the synchronous signal block of the neighboring cell;

wherein the bitmap comprises a plurality of bits, each bit corresponds to a candidate position of the synchronous signal block, and a value of each bit is used to indicate whether the synchronous signal block is actually transmitted at the corresponding candidate position; and

wherein each bit has a value of 1 or 0, wherein a value of 1 indicates that the synchronous signal block is transmitted at its corresponding candidate position, and a value of 0 indicates no synchronous signal block is transmitted at the corresponding candidate position.

2. The method according to claim 1, further comprising:

sending, to the terminal apparatus, a synchronous relationship between the at least one

first type of cell and a cell managed by the network apparatus;

wherein, the synchronous relationship comprises: a synchronous state between the first type of cell and the cell managed by the network apparatus; or, a synchronous deviation between the first type of cell and the cell managed by the network apparatus.

3. A method for indicating a position of a synchronous signal block, applied to a terminal apparatus, comprising:

receiving a signaling which indicates an actual transmission position of a synchronous signal block of at least one first type of cell from a network apparatus, wherein the terminal apparatus is located in a cell managed by the network device, and the first type of cell is a neighboring cell of the cell where the terminal apparatus is located, and wherein the signaling is broadcast signaling or radio resource control (RRC) dedicated signaling; and

measuring the synchronous signal block of the at least one first type of cell based on the actual transmission position of the synchronous signal block of the at least one first type of cell,

wherein, the receiving the signaling which indicates the actual transmission position of the synchronous signal block of the at least one first type of cell from the network apparatus, comprises:

receiving a signaling from the network apparatus; and acquiring a bitmap from the signaling, and acquiring, by the bitmap, the actual transmission position of the synchronous signal block of the at least one first type of cell,

wherein the bitmap is used to notify the actual transmission position of the synchronous signal block of the neighboring cell,

wherein the bitmap comprises a plurality of bits, each bit corresponds to a candidate position of the synchronous signal block, and a value of each bit is used to indicate whether the synchronous signal block is actually transmitted at the corresponding candidate position; and

wherein each bit has a value of 1 or 0, wherein a value of 1 indicates that the synchronous

signal block is transmitted at its corresponding candidate position, and a value of 0 indicates no synchronous signal block is transmitted at the corresponding candidate position.

4. The method according to claim 3, further comprising:

receiving a synchronous relationship between the at least one first type of cell sent by the network apparatus and a cell managed by the network apparatus;

wherein, the synchronous relationship comprises: a synchronous state between the first type of cell and the cell managed by the network apparatus; or, a synchronous deviation between the first type of cell and the cell managed by the network apparatus.

5. A network apparatus, comprising:

a processor, a memory for storing a computer program executable on the processor, and a network interface,

wherein, when executing the computer program, the processor performs:

determining an actual transmission position of a synchronous signal block of at least one first type of cell; and

sending, via the network interface, the actual transmission position of the synchronous signal block of the at least one first type of cell to a terminal apparatus by a signaling; wherein the terminal apparatus is located within a range of a second type of cell managed by the network apparatus, and the first type of cell is a neighboring cell of the second type of cell, and wherein the signaling is broadcast signaling or radio resource control (RRC) dedicated signaling,

wherein, the processor is further configured to generate a bitmap based on the actual transmission position of the synchronous signal block of the at least one first type of cell, and send the bitmap to the terminal apparatus by the signaling via the network interface,

wherein the bitmap is used to notify the actual transmission position of the synchronous

signal block of the neighboring cell;

wherein the bitmap comprises a plurality of bits, each bit corresponds to a candidate position of the synchronous signal block, and a value of each bit is used to indicate whether the synchronous signal block is actually transmitted at the corresponding candidate position; and

wherein each bit has a value of 1 or 0, wherein a value of 1 indicates that the synchronous signal block is transmitted at its corresponding candidate position, and a value of 0 indicates no synchronous signal block is transmitted at the corresponding candidate position.

6. The network apparatus according to claim 5, wherein, the processor is further configured to send, to the terminal apparatus, a synchronous relationship between the at least one first type of cell and the cell managed by the network apparatus via the network interface;

wherein, the synchronous relationship comprises: a synchronous state between the first type of cell and the cell managed by the network apparatus; or, a synchronous deviation between the first type of cell and the cell managed by the network apparatus.

7. The network apparatus according to claim 5, wherein, the processor is further configured to, when the at least one first type of cell is in a synchronous state with the cell managed by the network apparatus, generate the bitmap of the actual transmission position of the synchronous signal block comprising all the first type of cell based on the actual transmission position of the synchronous signal block of all the first type of cell; and send, by the signaling, the bitmap comprising the actual transmission position of the synchronous signal block of all the first type of cell to the terminal apparatus via the network interface.

8. The network apparatus according to claim 5, wherein, the processor is further configured to, when there is a synchronous deviation between the first type of cell and the cell managed by the network apparatus, send, to the terminal apparatus, the synchronous deviation between the at least one first type of cell and the cell managed by the network apparatus via the

network interface; generate the bitmap corresponding to each of the first type of cell based on the actual transmission position of the synchronous signal block of the at least one first type of cell; and send, by the signaling, the bitmap corresponding to each of the first type of cell to the terminal apparatus via the network interface.

9. The network apparatus according to claim 5, wherein, the processor is further configured to, if there is a synchronous deviation between a first part of first type of cell in the at least one first type of cell and the cell managed by the network apparatus, and a second part of first type of cell in the first type of cell is in a synchronous state with the cell managed by the network apparatus; wherein, the first part of first type of cell is different from the second part of first type of cell, and the first part of first type of cell and the second part of first type of cell form all of the first type of cell;

then send, to the terminal apparatus, the synchronous deviation between the first part of first type of cell and the cell managed by the network apparatus via the network interface;

generate the bitmap corresponding to each of the first type of cell in the first part of first type of cell based on the actual transmission position of the synchronous signal block of the first part of first type of cell; and send, by the signaling, the bitmap corresponding to each of the first type of cell in the first type of cell to the terminal apparatus via the network interface;

and, generate, the bitmap comprising the actual transmission position of the synchronous signal block of the second part of first type of cell according to the actual transmission position of the synchronous signal block of the second part of first type of cell; and send, to the terminal apparatus, the bitmap comprising the actual transmission position of the synchronous signal block of the second part of the first type of cell by the signaling via the network interface.

10. The network apparatus according to claim 5, wherein, the processor is further configured to send, to the terminal apparatus, a synchronous deviation between each of the first type of cell in the at least one first type of cell and the cell managed by the network apparatus

via the network interface; and generate the bitmap corresponding to each of the first type of cell based on the actual transmission position of the synchronous signal block of the first type of cell;

and send, by the signaling, the bitmap corresponding to each of the first type of cell to the terminal apparatus via the network interface.

11. A terminal apparatus, comprising:

a processor, a memory for storing a computer program executable on the processor, and a network interface,

wherein, when executing the computer program, the processor performs:

receiving, via the network interface, a signaling which indicates an actual transmission position of a synchronous signal block of at least one first type of cell from a network apparatus, wherein the terminal apparatus is located in a cell managed by the network device, and the first type of cell is a neighboring cell of the cell where the terminal apparatus is located, and wherein the signaling is broadcast signaling or radio resource control (RRC) dedicated signaling; and

measuring the synchronous signal block of the at least one first type of cell based on the actual transmission position of the synchronous signal block of the at least one first type of cell,

wherein, the processor is further configured to receive a signaling from the network apparatus via the network interface; and

acquire a bitmap from the signaling, and acquire, by the bitmap, the actual transmission position of the synchronous signal block of at least one first type of cell,

wherein the bitmap is used to notify the actual transmission position of the synchronous signal block of the neighboring cell;

wherein the bitmap comprises a plurality of bits, each bit corresponds to a candidate position of the synchronous signal block, and a value of each bit is used to indicate whether the

synchronous signal block is actually transmitted at the corresponding candidate position; and

wherein each bit has a value of 1 or 0, wherein a value of 1 indicates that the synchronous signal block is transmitted at its corresponding candidate position, and a value of 0 indicates no synchronous signal block is transmitted at the corresponding candidate position.

12. The terminal apparatus according to claim 11, wherein, the processor is further configured to receive a synchronous relationship between the at least one first type of cell sent by the network apparatus and a cell managed by the network apparatus via the network interface;

wherein, the synchronous relationship comprises: a synchronous state between the first type of cell and the cell managed by the network apparatus; or, a synchronous deviation between the first type of cell and the cell managed by the network apparatus.

13. The terminal apparatus according to claim 12, wherein, the processor is further configured to, when there is the synchronous deviation between the first type of cell and the cell managed by the network apparatus, acquire, from the bitmap, the actual transmission position of the synchronous signal block of all the first type of cell.

14. The terminal apparatus according to claim 12, wherein, the processor is further configured to, when there is the synchronous deviation between the first type of cell and the cell managed by the network apparatus, acquire, the synchronous deviation between the at least one first type of cell and the cell managed by the network apparatus; and acquire, the bitmap corresponding to each of the first type of cell, and acquire the actual transmission position of the synchronous signal block of each of the first type of cell based on the bitmap.

15. The terminal apparatus according to claim 12, wherein, the processor is further

configured to, if there is the synchronous deviation between a first part of first type of cell in the at least one first type of cell and the cell managed by the network apparatus, and a second part of first type of cell in the first type of cell is in the synchronous state with the cell managed by the network apparatus; wherein, the first part of first type of cell is different from the second part of first type of cell, and the first part of first type of cell and the second part of first type of cell form all of the first type of cell;

then acquire the actual transmission position of the synchronous signal block of the first part of first type of cell based on the bitmap of each first type of cell in the first part of first type of cell;

and, acquire, according to the bitmap of the second part the first type of cell, the actual transmission position of the synchronous signal block of the second part of first type of cell.

16. The terminal apparatus according to claim 12, wherein, the processor is further configured to acquire the synchronous deviation between each first type of cell in the at least one first type of cell and the cell managed by the network apparatus; and

acquire the bitmap corresponding to each of the first type of cell, and acquire the actual transmission position of the synchronous signal block of each of the first type of cell from the bitmap.