CLAIMS:

1. A method of video processing in a decoder, comprising:

decoding information of a coding block from a bitstream, the information indicating an intra prediction mode for the coding block;

determining whether a selectable secondary non-separable transform is not applied to the bitstream and a selectable separable primary transform is applied to the bitstream;

in response to determining that the selectable secondary non-separable transform is not applied to the bitstream and the selectable separable primary transform is applied to the bitstream, determining residuals of the coding block from the bitstream based on a selection of a scan pattern from a plurality of scan pattern candidates indicated by the information and a selection of the selectable separable primary transform from a plurality of separable primary transform candidates indicated by the information;

in response to determining that the selectable secondary non-separable transform is applied to the bitstream and the selectable separable primary transform is not applied to the bitstream, determining the residuals of the coding block from the bitstream based on a fixed scan pattern; and

reconstructing the coding block based on a combination of the residuals with an intra prediction of the coding block, the intra prediction being based on the intra prediction mode.

2. The method of claim 1, wherein the determining the residuals of the coding block based on the selection of the scan pattern further comprises:

decoding the bitstream to determine transform coefficients in a transform domain based on the selection of the scan pattern; and

applying an inverse transform associated with the selectable separable primary transform on the transform coefficients in the transform domain to determine the residuals.

3. The method of claim 1, wherein the information indicates an exclusion of a selectable secondary non separable transform and the determining the residuals of the coding block excludes an application of an inverse transform associated with the selectable secondary non separable transform.

4. The method of claim 1, wherein the scan pattern is applicable respectively with a subset of the plurality of separable primary transform candidates.

5. The method of claim 1, wherein the scan pattern is applicable respectively with each of the plurality of separable primary transform candidates.

6. The method of claim 1, further comprising:

decoding a value of a syntax from the bitstream, the value of the syntax indicating a combination of the selection of the selectable separable primary transform from the plurality of separable primary transform candidates and the selection of the scan pattern from the plurality of scan pattern candidates.

7. The method of claim 1, further comprising:

decoding a value of a first syntax from the bitstream, the value of the first syntax indicating the selection of the selectable separable primary transform; and

decoding a value of a second syntax from the bitstream, the value of the second syntax indicating the selection of the scan pattern.

8. The method of claim 1, further comprising:

inferring the selection of the scan pattern.

9. The method of claim 8, further comprising:

inferring the selection of the scan pattern based on the intra prediction mode.

10. The method of claim 8, further comprising at least one of:

inferring the selection of the selectable separable primary transform; and

decoding a value of a syntax from the bitstream, the value of the syntax indicating the selection of the selectable separable primary transform.

11. An apparatus for video decoding, comprising processing circuitry configured to:

decode information of a coding block from a bitstream, the information indicating an intra prediction mode for the coding block;

determine whether a selectable secondary non-separable transform is not applied to the bitstream and a selectable separable primary transform is applied to the bitstream;

in response to determining that the selectable secondary non-separable transform is not applied to the bitstream and the selectable separable primary transform is applied to the bitstream, determine residuals of the coding block from the bitstream based on a selection of a scan pattern from a plurality of scan pattern candidates indicated by the information and a selection of the selectable separable primary transform from a plurality of separable primary transform candidates indicated by the information;

in response to determining that the selectable secondary non-separable transform is applied to the bitstream and the selectable separable primary transform is not applied to the bitstream, determining the residuals of the coding block from the bitstream based on a fixed scan pattern; and

reconstruct the coding block based on a combination of the residuals with an intra prediction of the coding block, the intra prediction being based on the intra prediction mode.

12. The apparatus of claim 11, wherein the processing circuitry is further configured to:

decode the bitstream to determine transform coefficients in a transform domain based on the selection of the scan pattern; and

apply an inverse transform associated with the selectable separable primary transform on the transform coefficients in the transform domain to determine the residuals.

13. The apparatus of claim 11, wherein the information indicates an exclusion of a selectable secondary non separable transform and the processing circuitry is configured to determine the residuals of the coding block without an application of an inverse transform associated with the selectable secondary non separable transform.

14. The apparatus of claim 11, wherein the scan pattern is applicable respectively with a subset of the plurality of separable primary transform candidates.

15. The apparatus of claim 11, wherein the scan pattern is applicable respectively with each of the plurality of separable primary transform candidates.

16. The apparatus of claim 11, wherein the processing circuitry is configured to:

decode a value of a syntax from the bitstream, the value of the syntax indicating a combination of the selection of the selectable separable primary transform from the plurality of separable primary transform candidates and the selection of the scan pattern from the plurality of scan pattern candidates.

17. The apparatus of claim 11, wherein the processing circuitry is configured to:

decode a value of a first syntax from the bitstream, the value of the first syntax indicating the selection of the selectable separable primary transform; and

decode a value of a second syntax from the bitstream, the value of the second syntax indicating the selection of the scan pattern.

18. The apparatus of claim 11, wherein the processing circuitry is configured to:

infer the selection of the scan pattern.

19. The apparatus of claim 18, wherein the processing circuitry is configured to:

infer the selection of the scan pattern based on the intra prediction mode.

20. The apparatus of claim 18, wherein the processing circuitry is configured to perform at least one of:

inferring the selection of the selectable separable primary transform; and

decoding a value of a syntax from the bitstream, the value of the syntax indicating the selection of the selectable separable primary transform.