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(54) Title: EQUALIZATION TRAINING METHOD, APPARATUS, AND SYSTEM

(54) 发明名称: 一种均衡训练方法、装置及系统

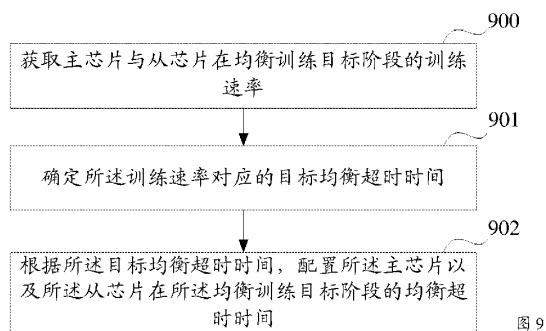


图 9

900 Acquire the training speed of a target phase of a main chip and a slave chip during equalization training

901 Determine a target equalization time-out time corresponding to the training speed

902 According to the target equalization time-out time, configure the equalization time-out time of the target phase of the main chip and the slave chip during equalization training

(57) Abstract: An equalization training method, apparatus, and system. The method comprises: acquiring the training speed of a target phase of a main chip and a slave chip during equalization training (900); determining a target speed threshold interval in which the training speed of the target phase is located, and according to the correspondence between N+1 speed threshold intervals and N+1 equalization time-out times, determining a target equalization time-out time corresponding to the target speed threshold interval, and configuring the target equalization time-out time as the equalization time-out time of the target phase; then the main chip and the slave chip performing equalization training on the target phase within the equalization time-out time of the target phase. The described method can flexibly configure an equalization time-out time used for equalization training for each equalization training process, thus enabling

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the configured equalization time-out time to better comply with the current training speed used for link negotiation, better ensure that equalization parameters are found within the configured equalization time-out time, and increase the success rate of equalization training.

(57) 摘要: 一种均衡训练方法、装置及系统, 包括: 获取主芯片与从芯片在均衡训练的目标阶段的训练速率(900); 确定所述目标阶段的训练速率所在的目标速率阈值区间, 根据N+1个速率阈值区间与N+1个均衡超时时间的对应关系, 确定所述目标速率阈值区间对应的目标均衡超时时间, 并将所述目标均衡超时时间配置为所述目标阶段的均衡超时时间; 则所述主芯片与所述从芯片在所述目标阶段的均衡超时时间内进行所述目标阶段的均衡训练。该方法能够针对每次均衡训练过程, 灵活的配置用于均衡训练的均衡超时时间, 从而使配置的均衡超时时间更符合当前用于链路协商的训练速率, 更好的保证了在配置的均衡超时时间内, 找到均衡参数, 提升了均衡训练成功率。