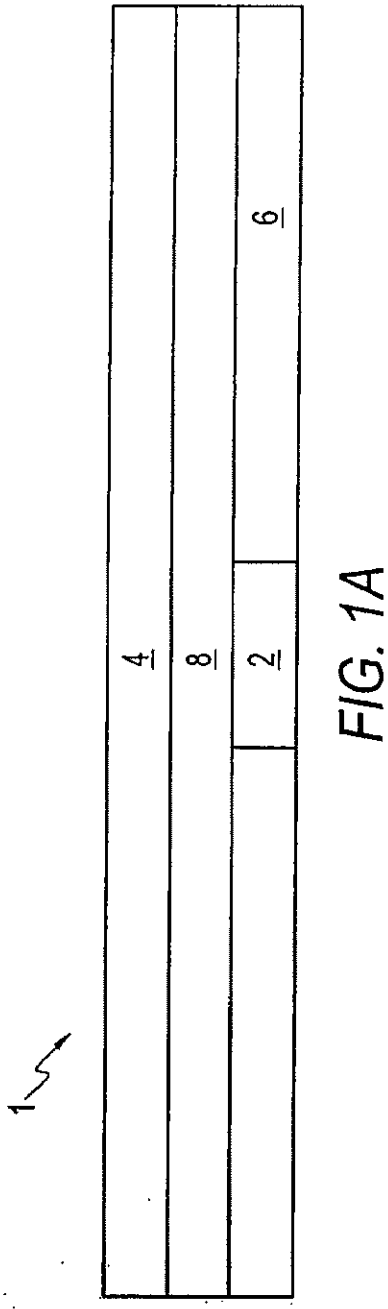


FIG. 1A

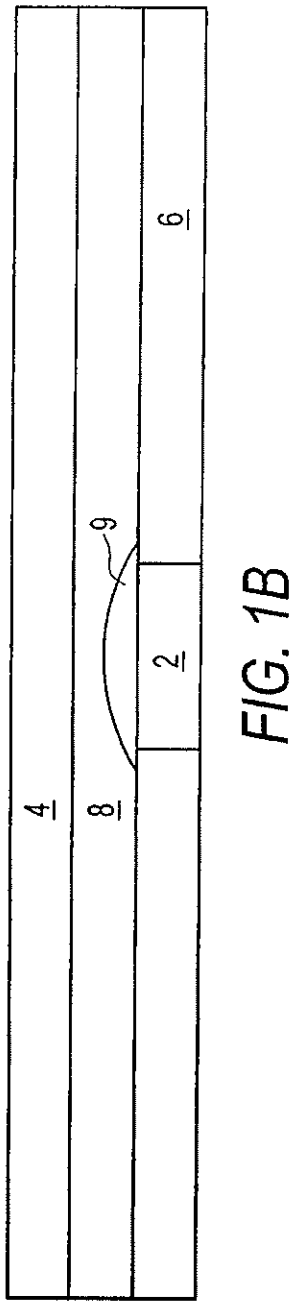


FIG. 1B

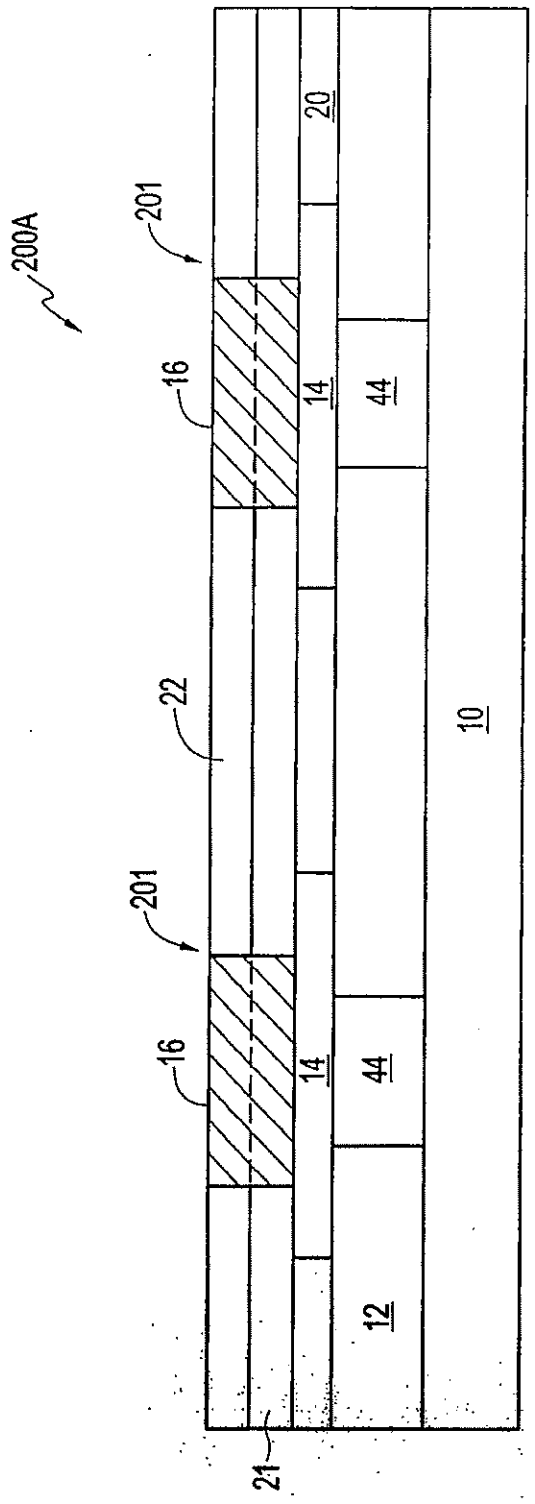


FIG. 2A

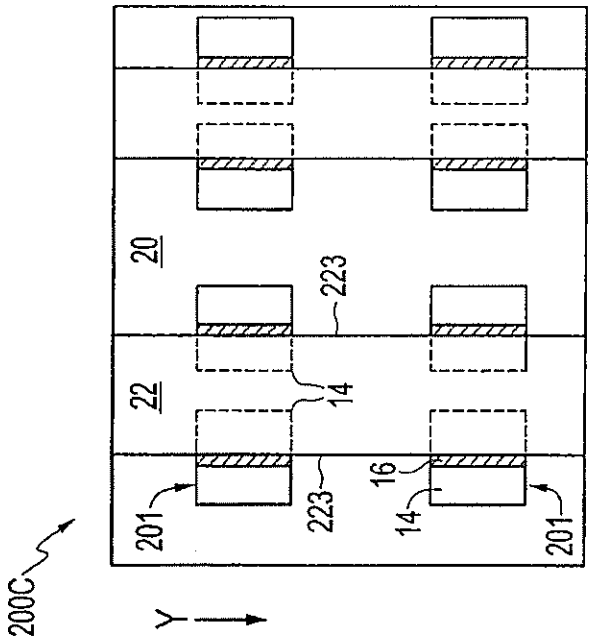

200C
Y
20
22
223
14
16
201
223
14
201

FIG. 2C

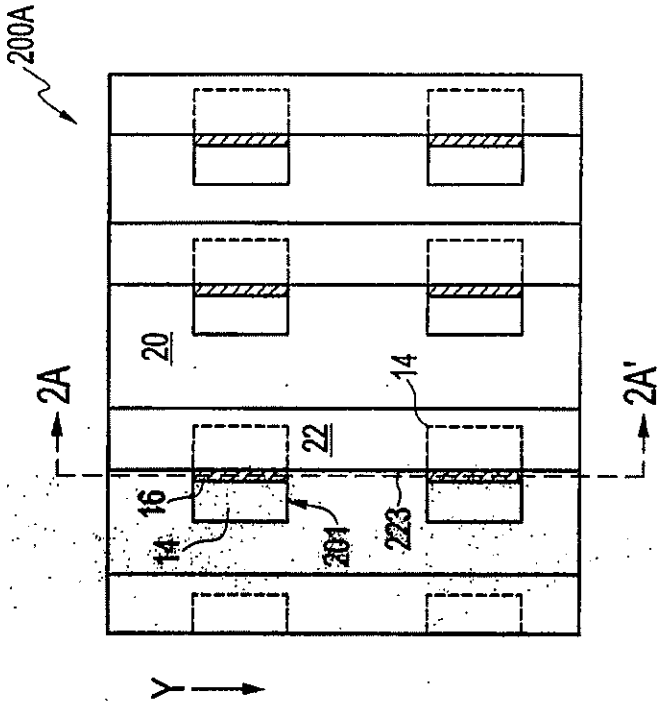

200A
2A
2A'
Y
20
22
14
16
14
201
223

FIG. 2B

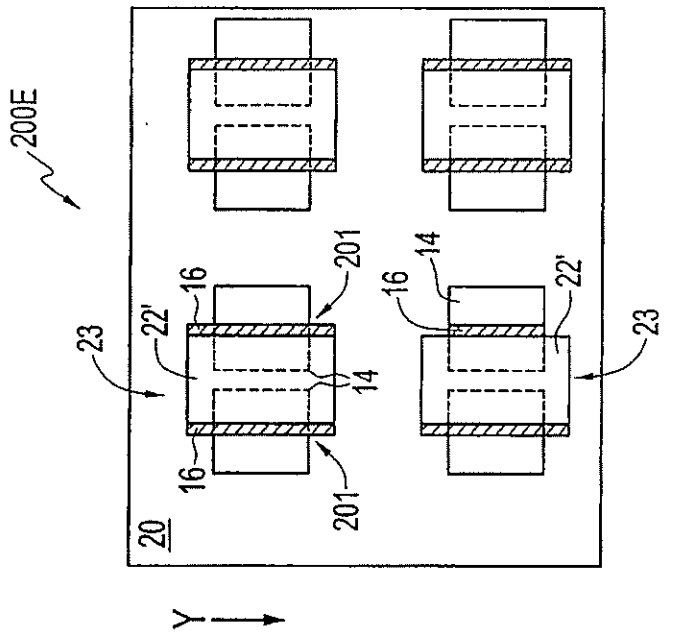


FIG. 2E

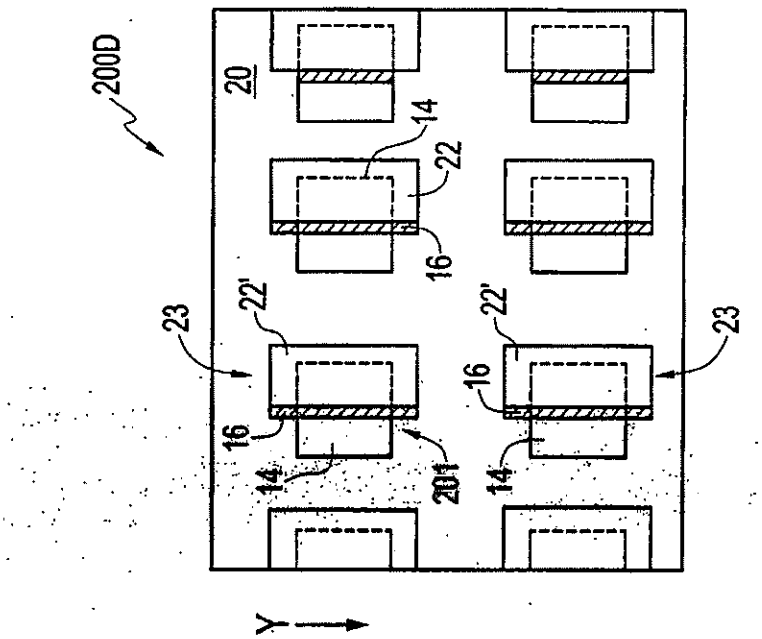


FIG. 2D

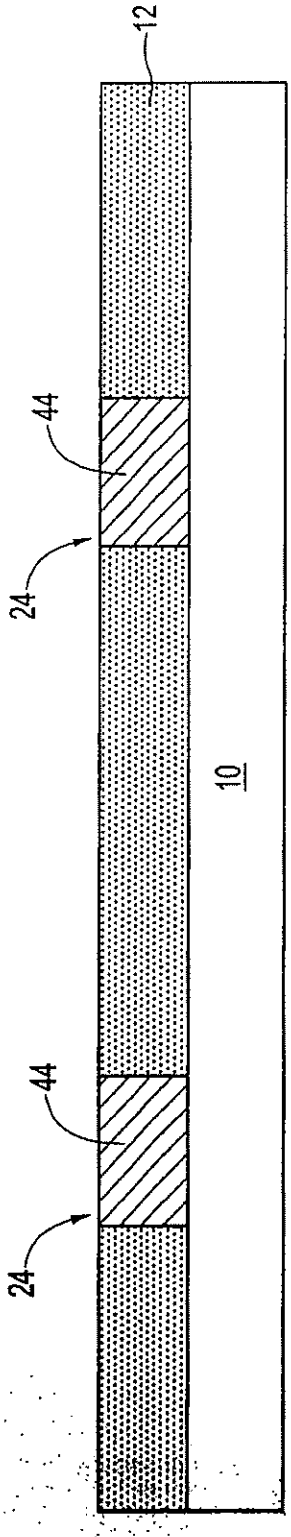


FIG. 3A

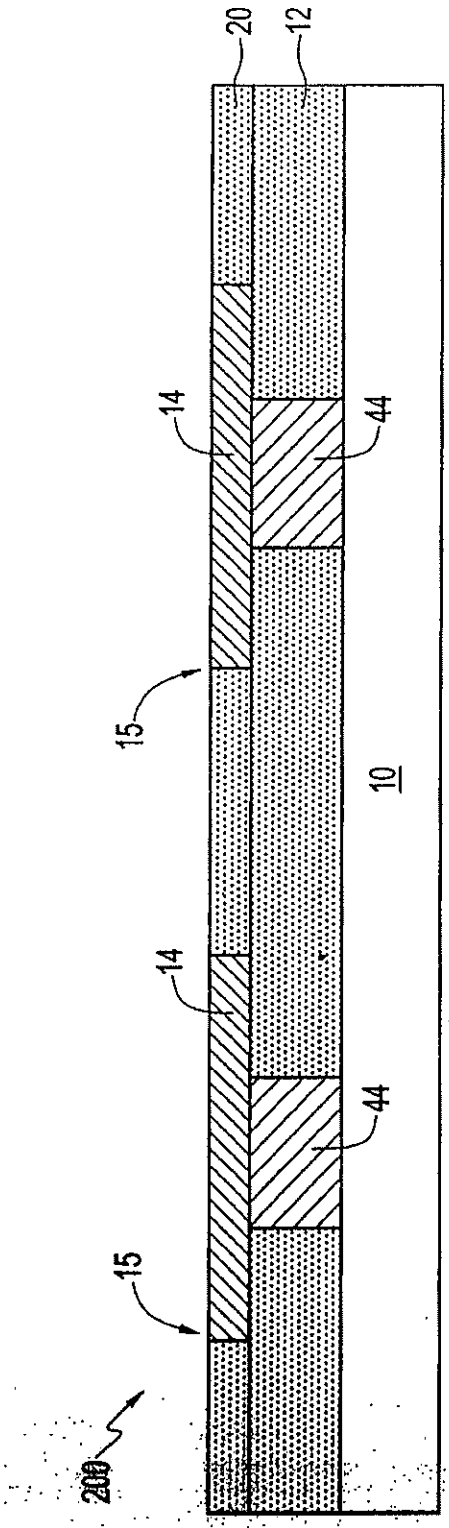


FIG. 3B

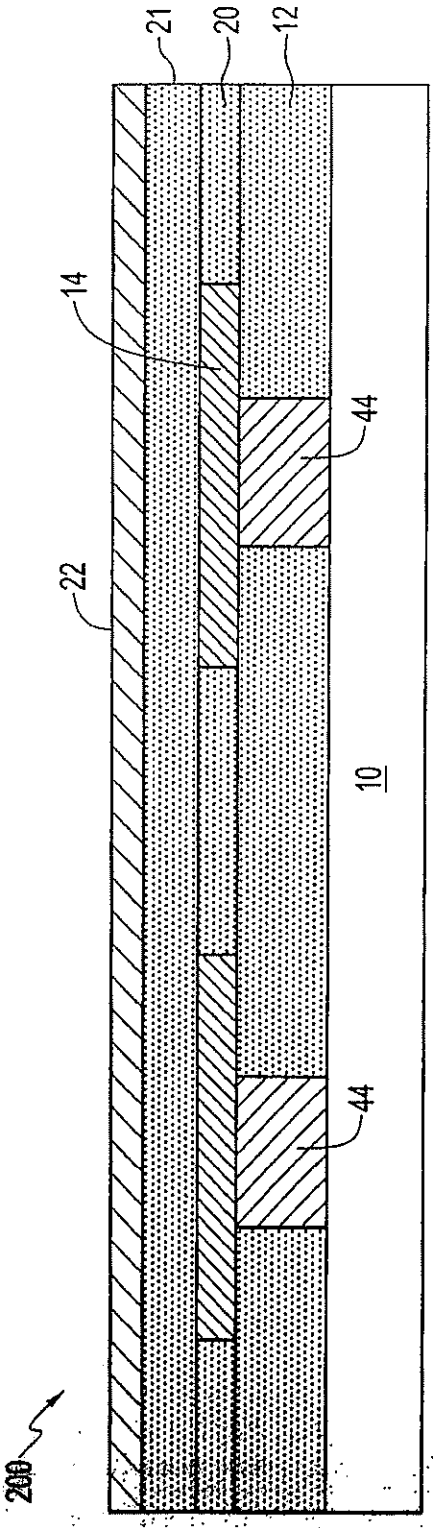


FIG. 3C

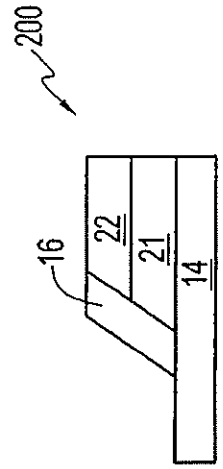


FIG. 3F

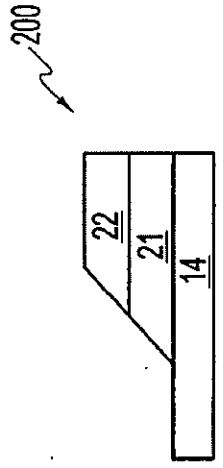


FIG. 3D

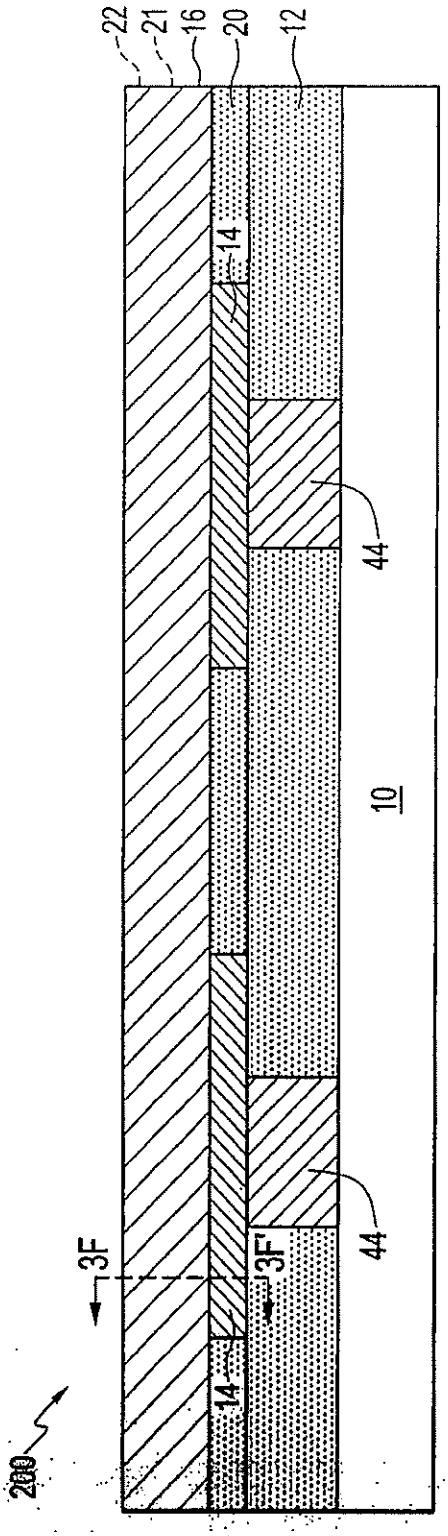


FIG. 3E

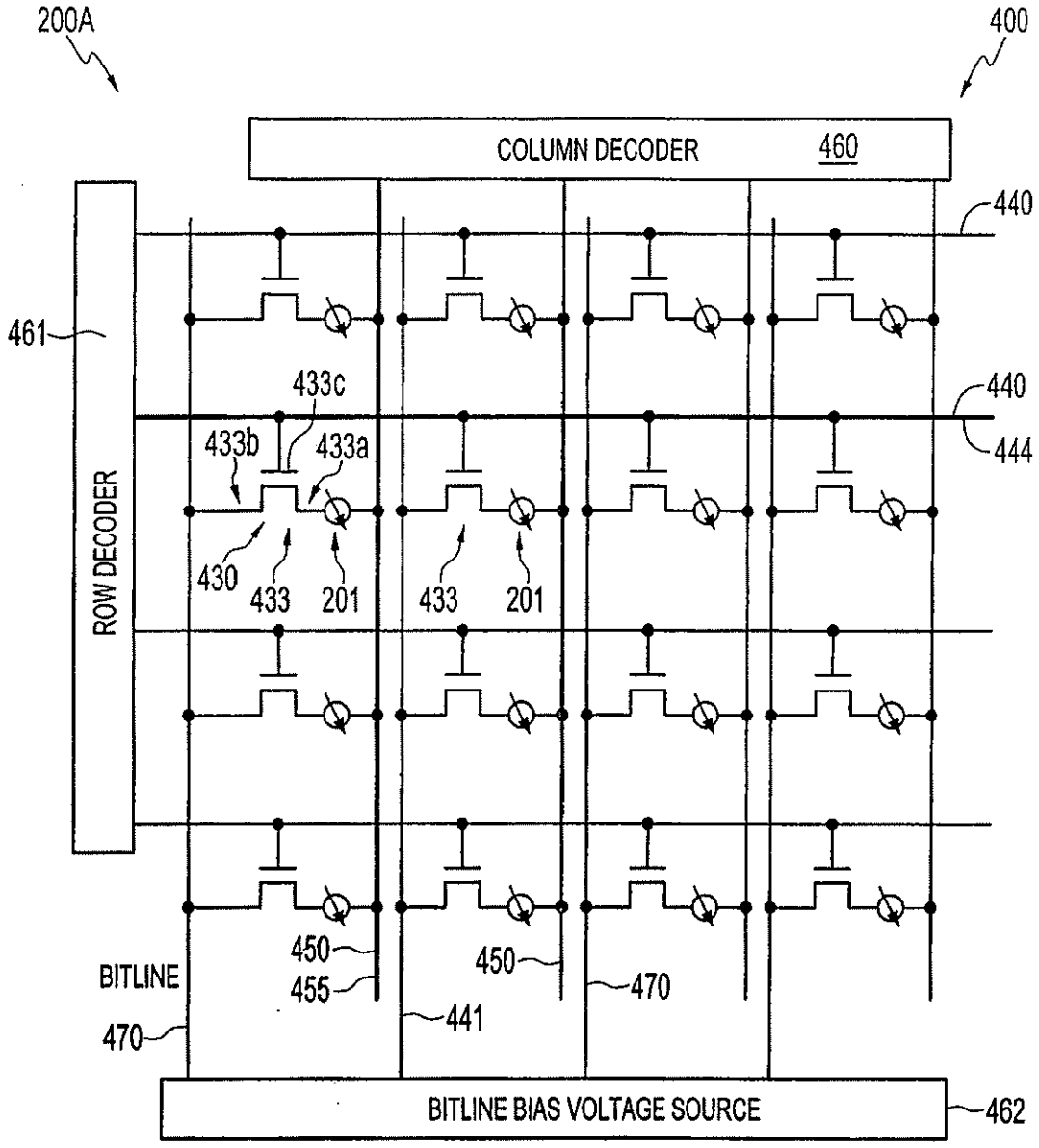

200A
400
COLUMN DECODER
460
440
461
433c
440
433b
433a
444
ROW DECODER
430
433
201
433
201
450
450
BITLINE
455
470
470
441
BITLINE BIAS VOLTAGE SOURCE
462

FIG. 4

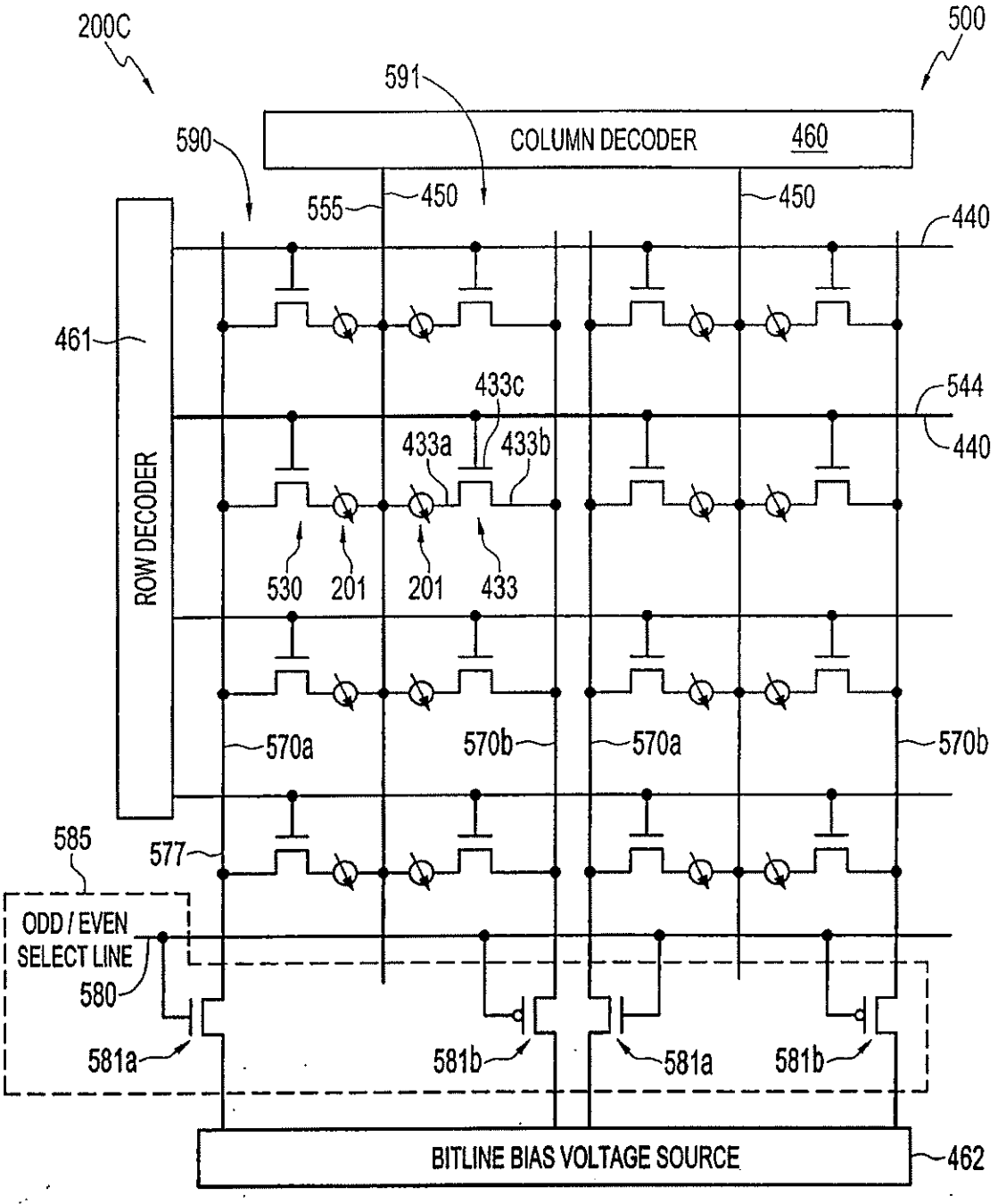

200C
500
591
590
COLUMN DECODER
460
555
450
450
440
461
433c
544
433a
433b
440
ROW DECODER
530
201
201
433
570a
570b
570a
570b
585
577
ODD / EVEN
SELECT LINE
580
581a
581b
581a
581b
BITLINE BIAS VOLTAGE SOURCE
462

FIG. 5

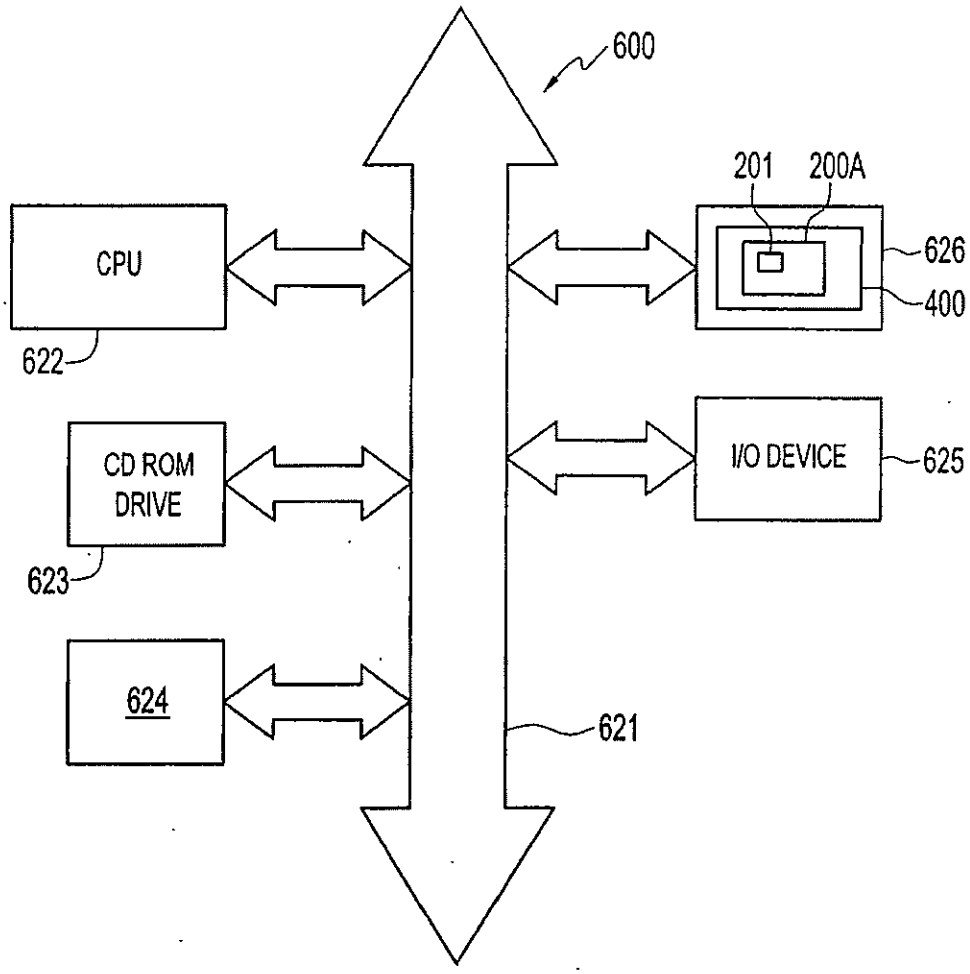

600
201
200A
CPU
626
400
622
CD ROM DRIVE
I/O DEVICE
625
623
624
621

FIG. 6