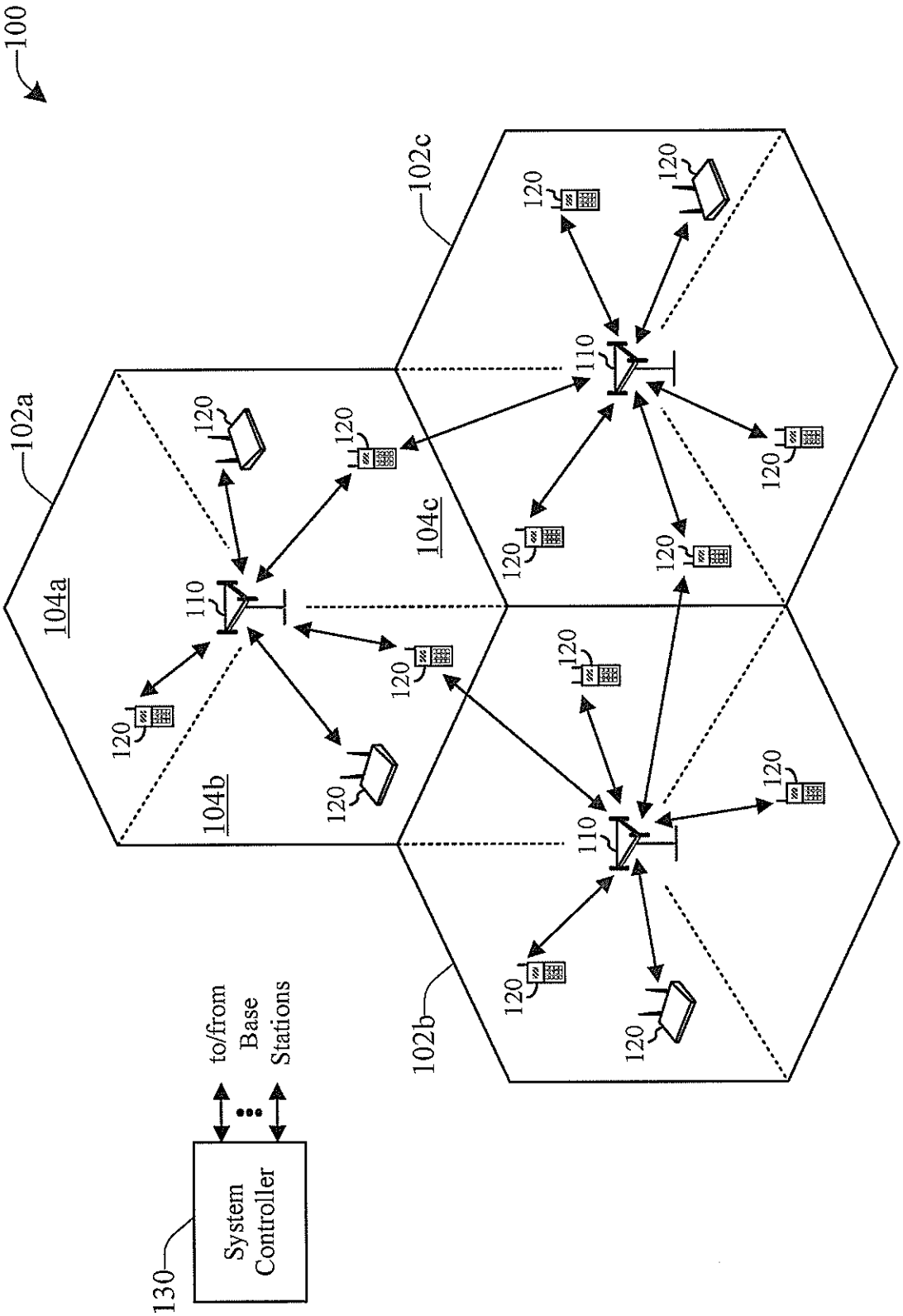

100
102a
102b
102c
104a
104b
104c
110
120
130
System Controller
to/from Base Stations

FIG. 1

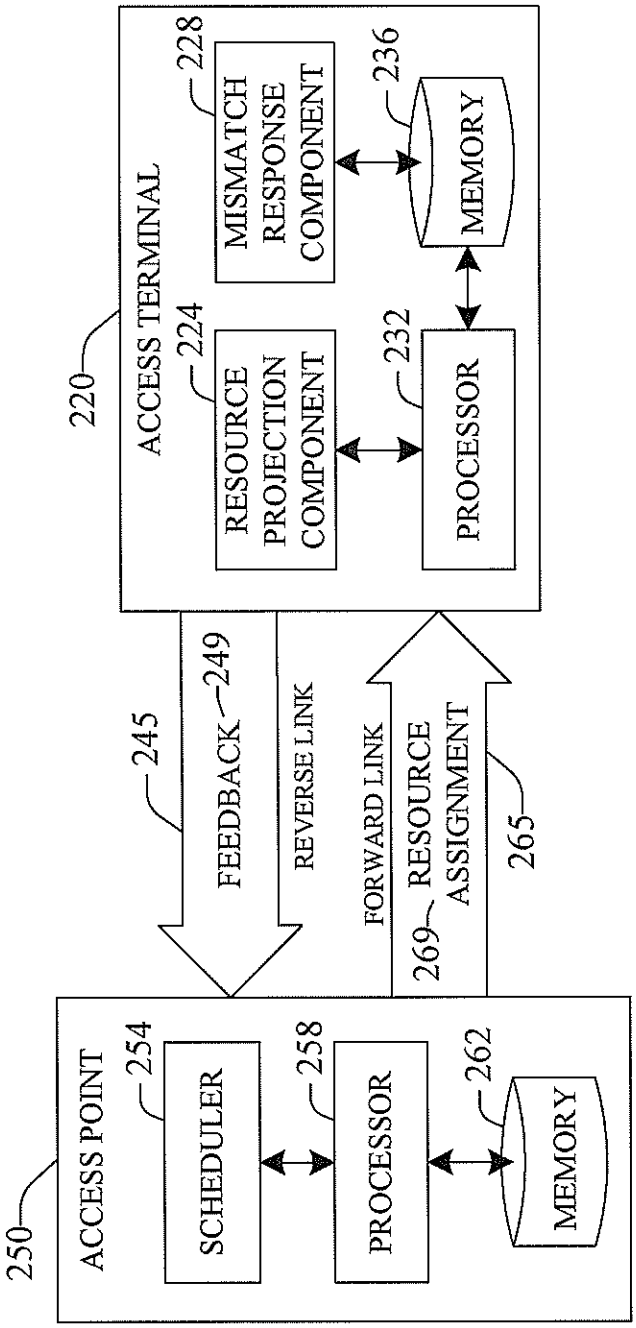

ACCESS TERMINAL
220
MISMATCH RESPONSE COMPONENT
228
MEMORY
236
RESOURCE PROJECTION COMPONENT
224
PROCESSOR
232
FEEDBACK
249
245
REVERSE LINK
FORWARD LINK
RESOURCE ASSIGNMENT
269
265
ACCESS POINT
250
SCHEDULER
254
PROCESSOR
258
MEMORY
262

FIG. 2

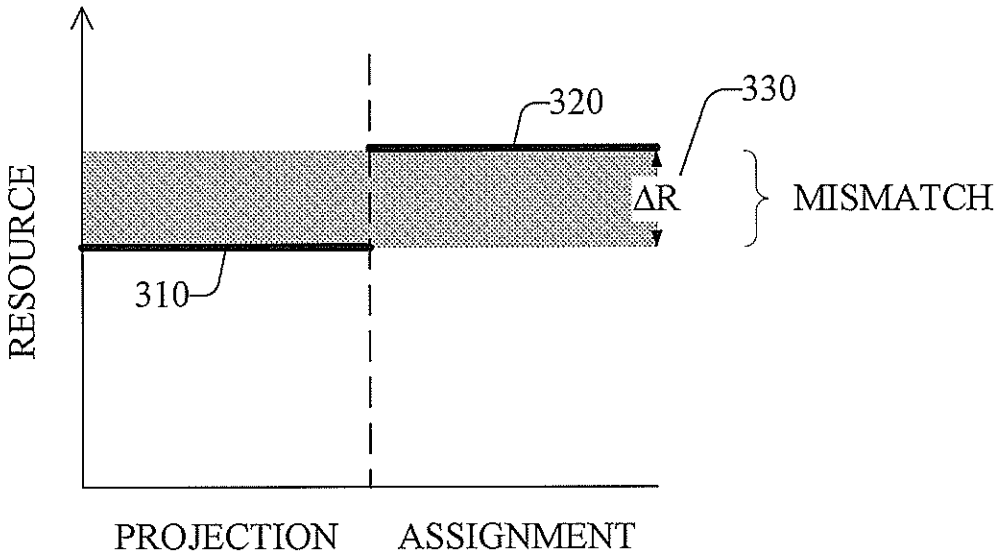

RESOURCE
320
330
ΔR
MISMATCH
310
PROJECTION
ASSIGNMENT

FIG. 3A

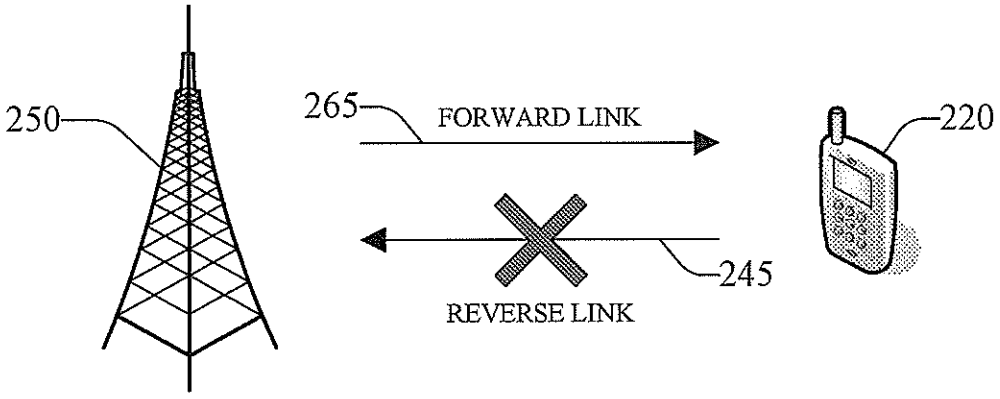

250
265
FORWARD LINK
220
245
REVERSE LINK

FIG. 3B

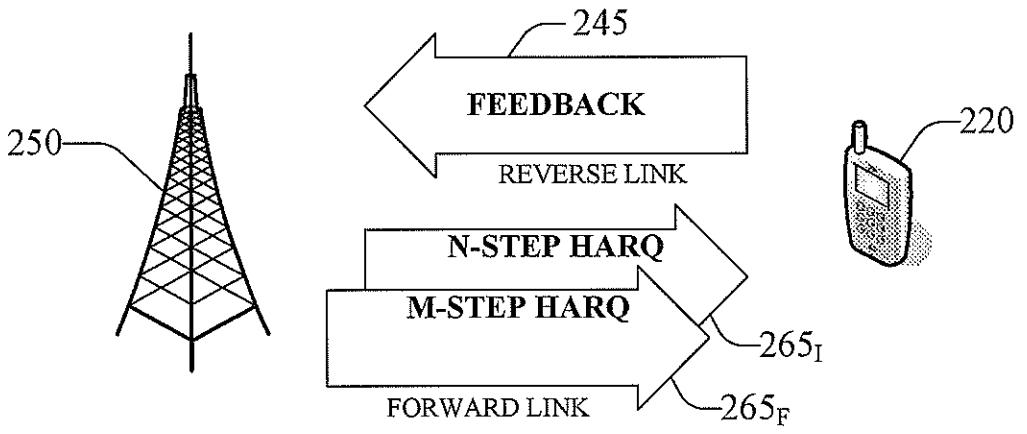


FIG. 4A

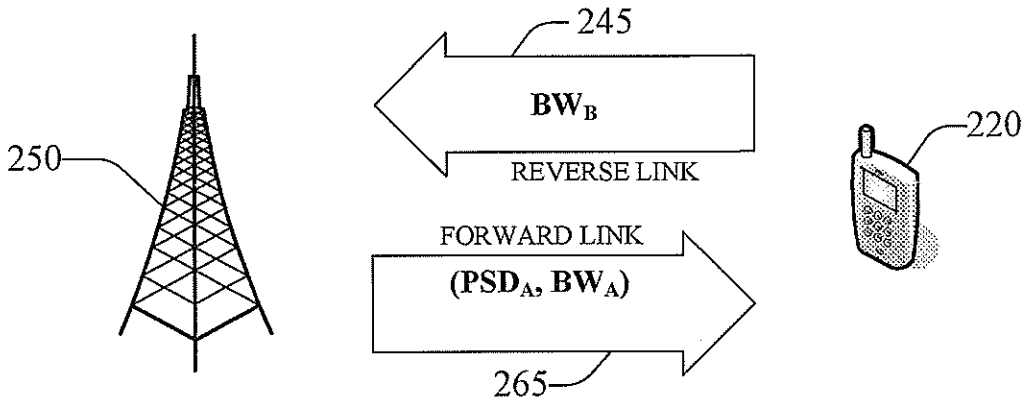


FIG. 4B

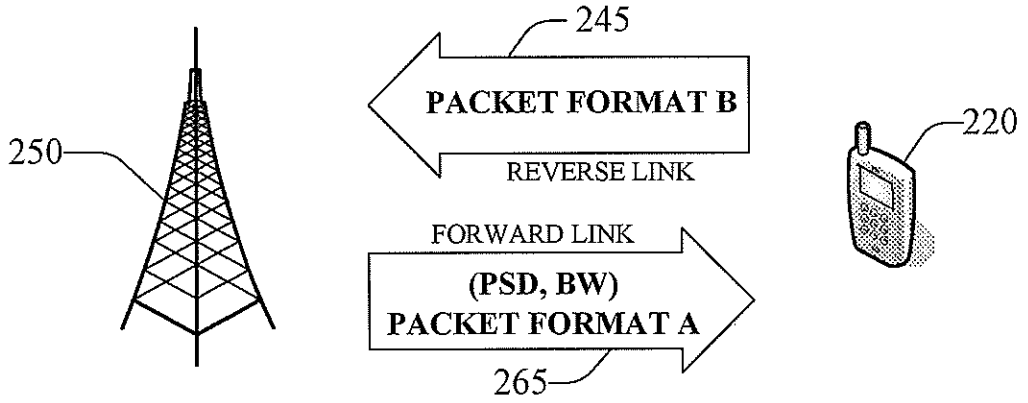


FIG. 4C

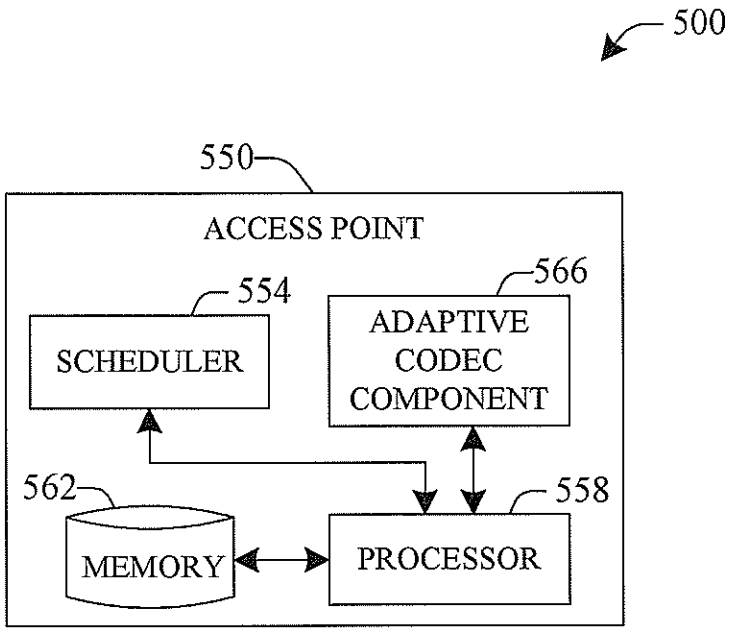

500
550
ACCESS POINT
554
SCHEDULER
566
ADAPTIVE CODEC COMPONENT
562
MEMORY
558
PROCESSOR

FIG. 5

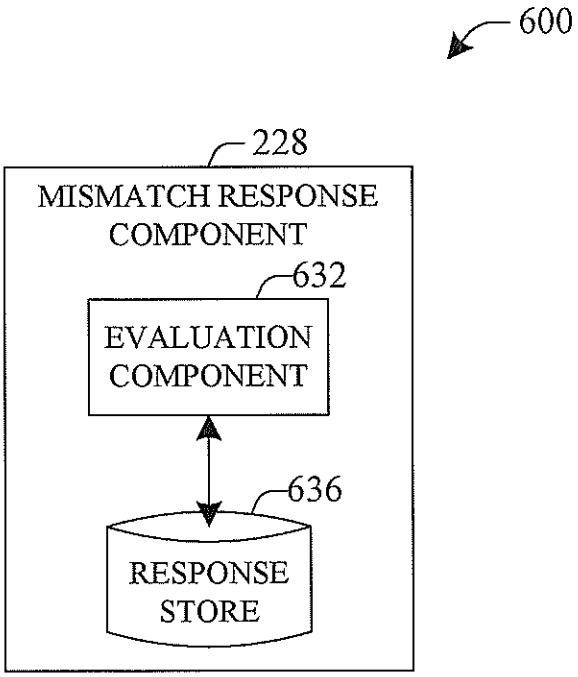

600
228
MISMATCH RESPONSE COMPONENT
632
EVALUATION COMPONENT
636
RESPONSE STORE

FIG. 6

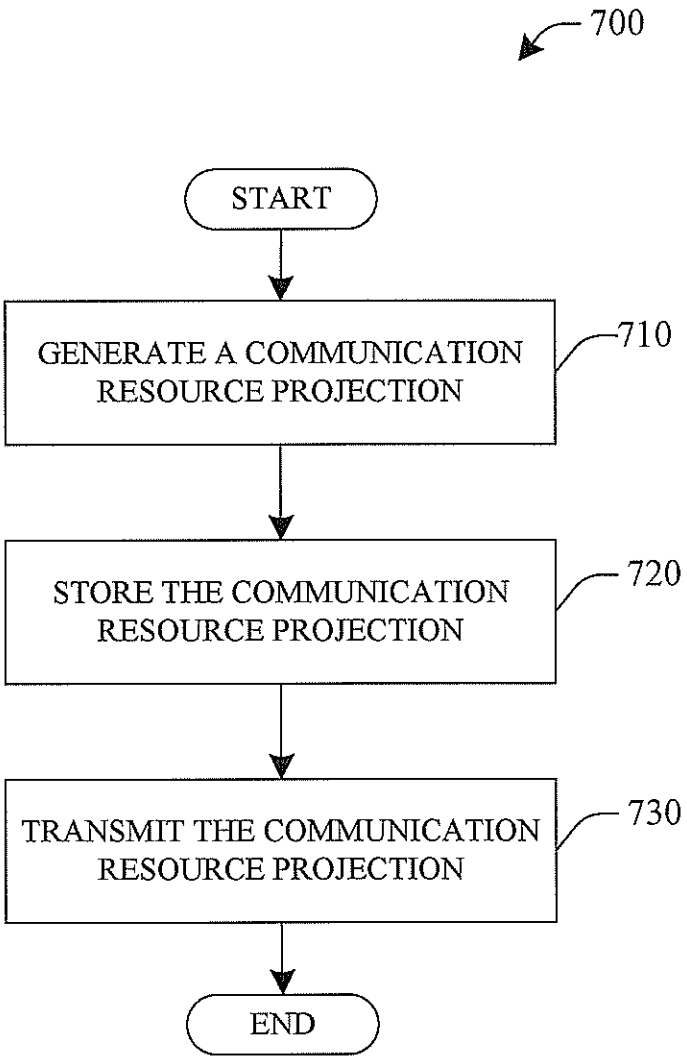

700
START
GENERATE A COMMUNICATION RESOURCE PROJECTION
710
STORE THE COMMUNICATION RESOURCE PROJECTION
720
TRANSMIT THE COMMUNICATION RESOURCE PROJECTION
730
END

FIG. 7

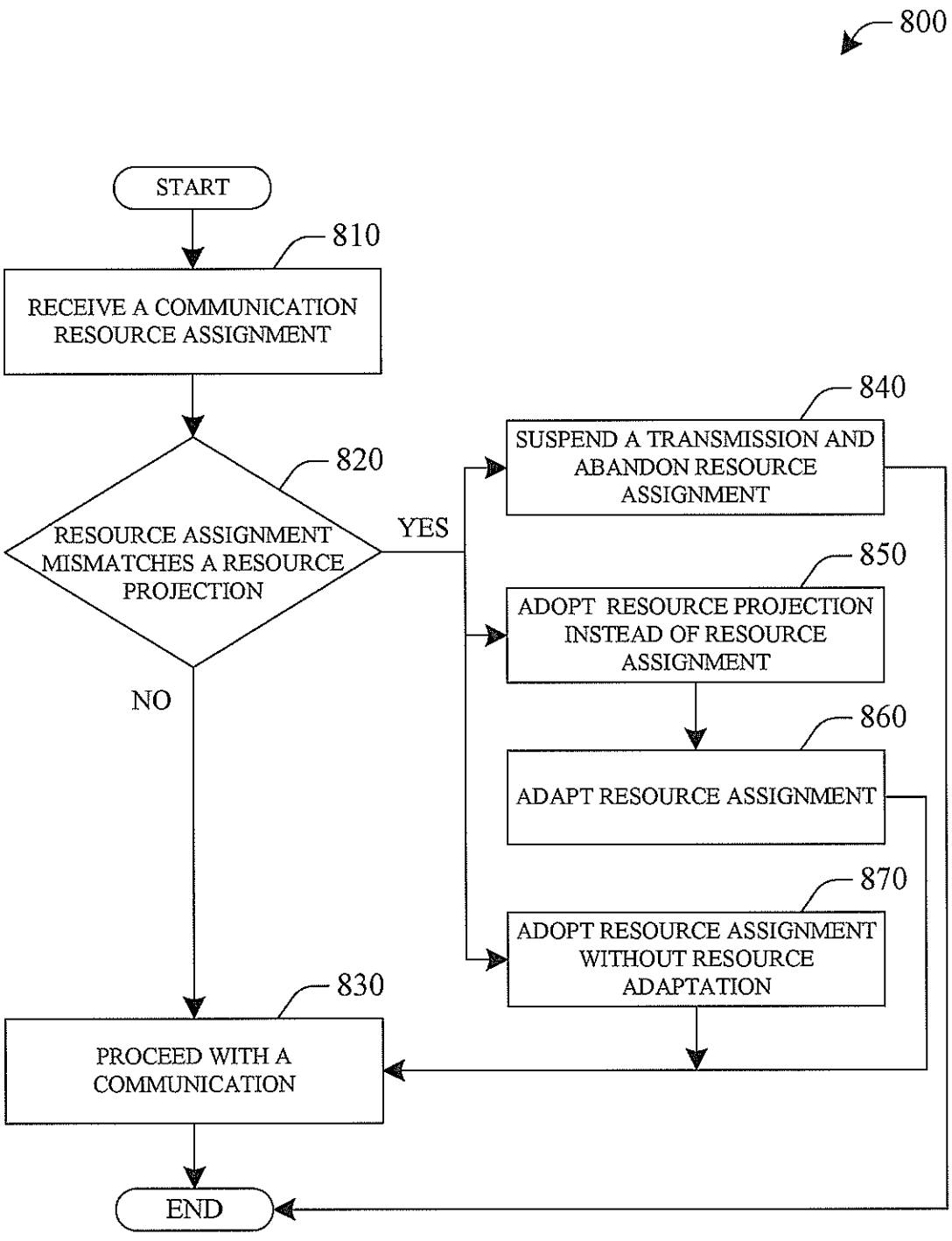

800
START
810
RECEIVE A COMMUNICATION RESOURCE ASSIGNMENT
820
RESOURCE ASSIGNMENT MISMATCHES A RESOURCE PROJECTION
YES
NO
840
SUSPEND A TRANSMISSION AND ABANDON RESOURCE ASSIGNMENT
850
ADOPT RESOURCE PROJECTION INSTEAD OF RESOURCE ASSIGNMENT
860
ADAPT RESOURCE ASSIGNMENT
870
ADOPT RESOURCE ASSIGNMENT WITHOUT RESOURCE ADAPTATION
830
PROCEED WITH A COMMUNICATION
END

FIG. 8

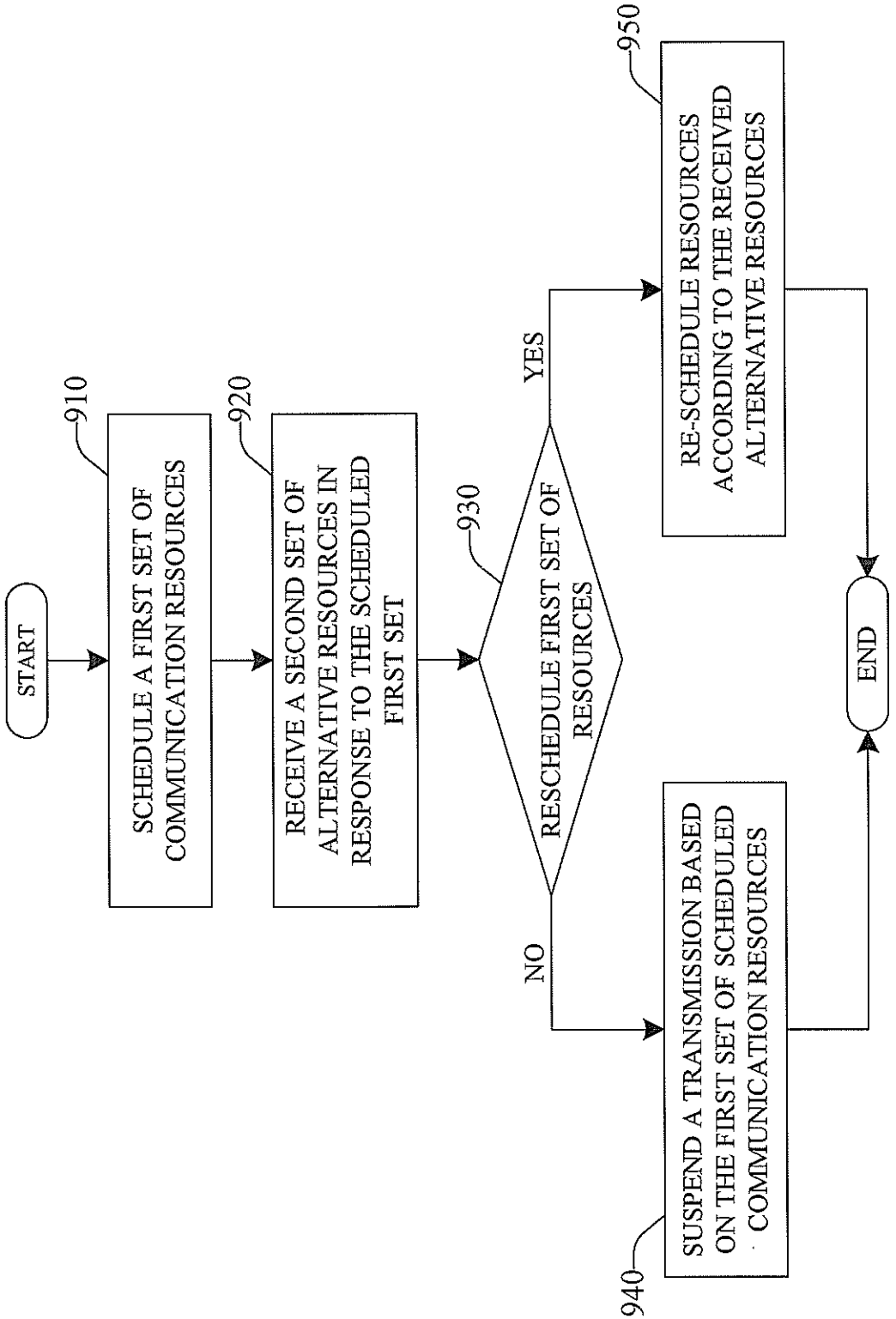

900
START
910
SCHEDULE A FIRST SET OF COMMUNICATION RESOURCES
920
RECEIVE A SECOND SET OF ALTERNATIVE RESOURCES IN RESPONSE TO THE SCHEDULED FIRST SET
930
RESCHEDULE FIRST SET OF RESOURCES
YES
NO
950
RE-SCHEDULE RESOURCES ACCORDING TO THE RECEIVED ALTERNATIVE RESOURCES
940
SUSPEND A TRANSMISSION BASED ON THE FIRST SET OF SCHEDULED COMMUNICATION RESOURCES
END

FIG. 9

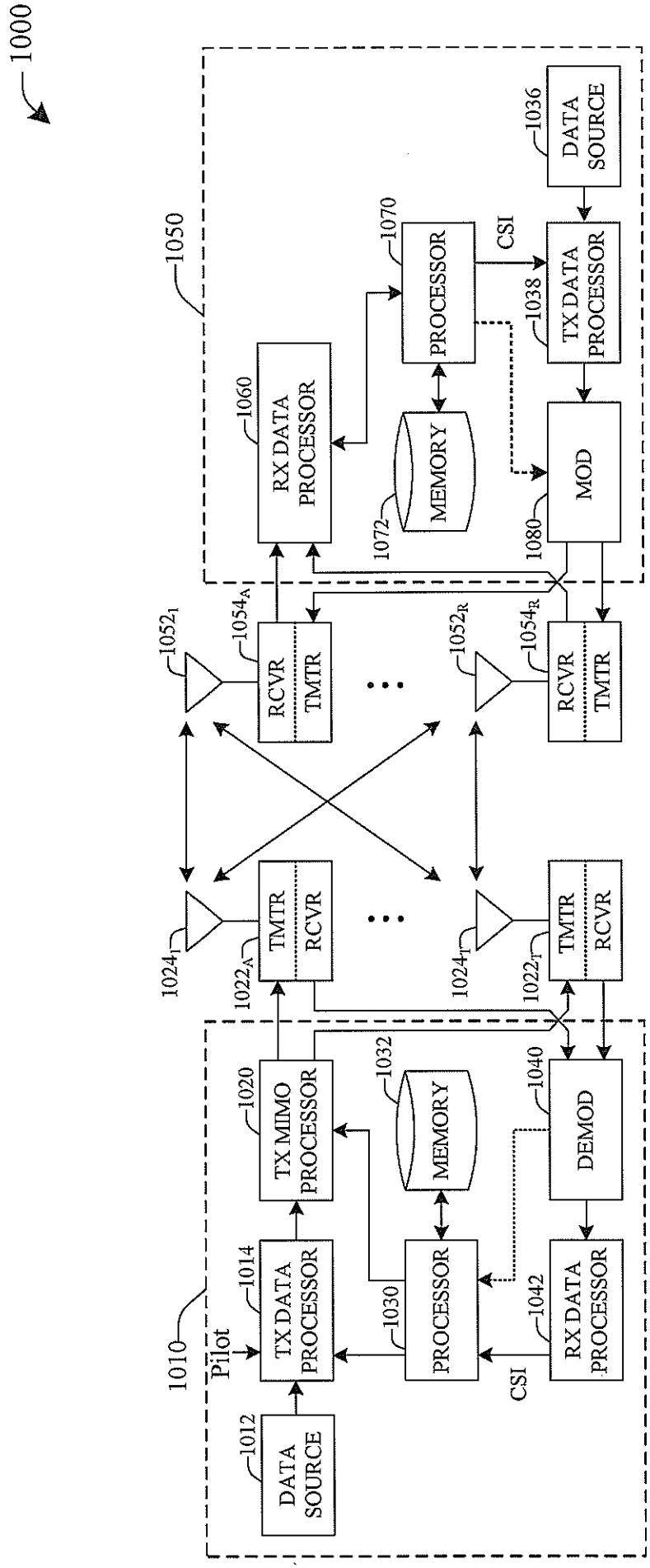

1000
1050
1036
DATA SOURCE
1070
PROCESSOR
CSI
1038
TX DATA PROCESSOR
1060
RX DATA PROCESSOR
MEMORY
1072
MOD
1080
1052_1
1054_A
RCVR
TMTR
1052_R
1054_R
RCVR
TMTR
1024_1
1022_A
TMTR
RCVR
1024_T
1022_T
TMTR
RCVR
1010
Pilot
1020
TX MIMO PROCESSOR
1032
MEMORY
1040
DEMOD
1014
TX DATA PROCESSOR
1030
PROCESSOR
1042
RX DATA PROCESSOR
CSI
1012
DATA SOURCE

FIG. 10

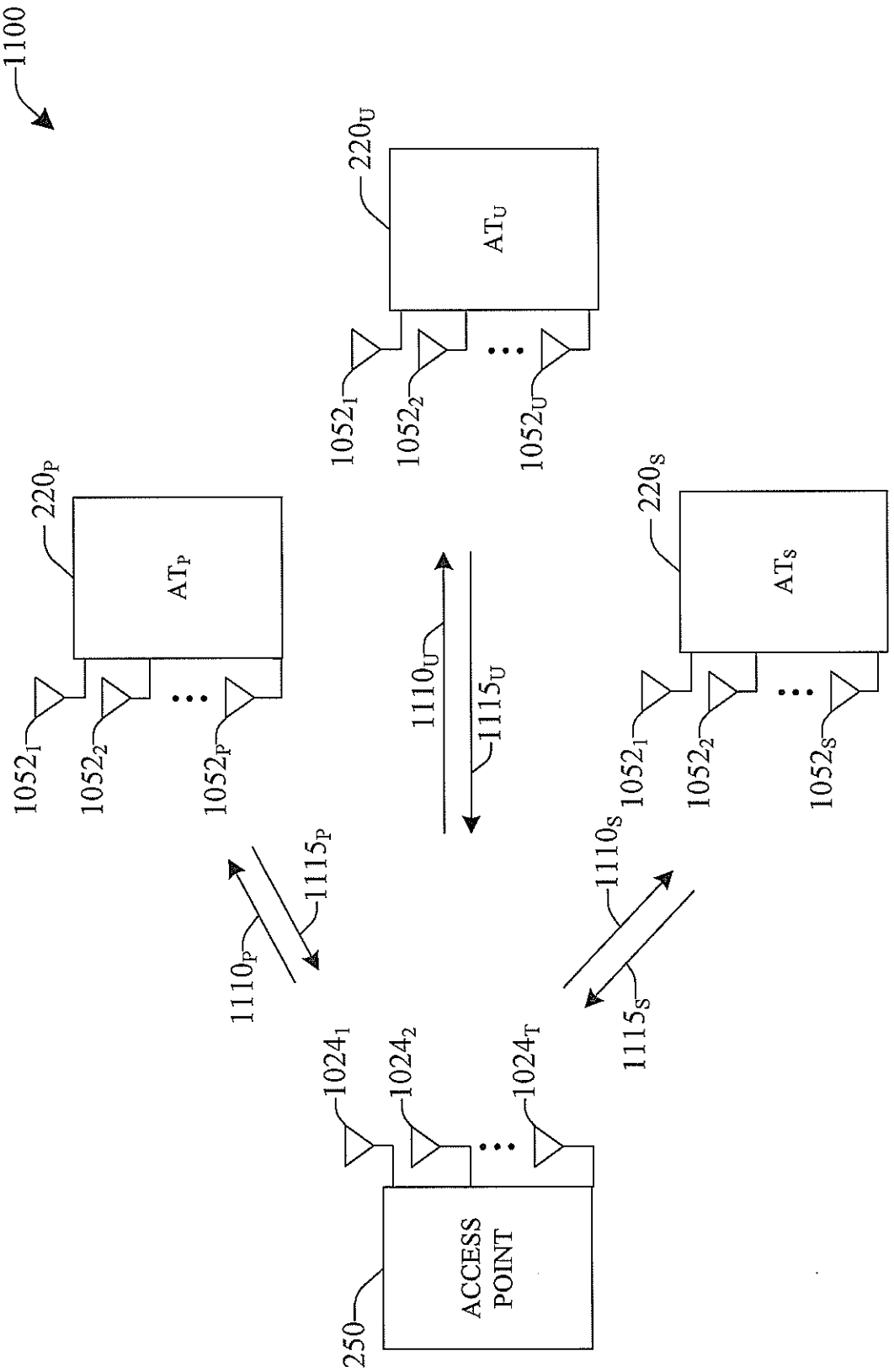

1100
220U
ATU
1052I
1052₂
1052U
220P
ATP
1052I
1052₂
1052P
1110U
1115U
1115P
1110P
220S
ATS
1052I
1052₂
1052S
1110S
1115S
1024I
1024₂
1024T
ACCESS POINT
250

FIG. 11

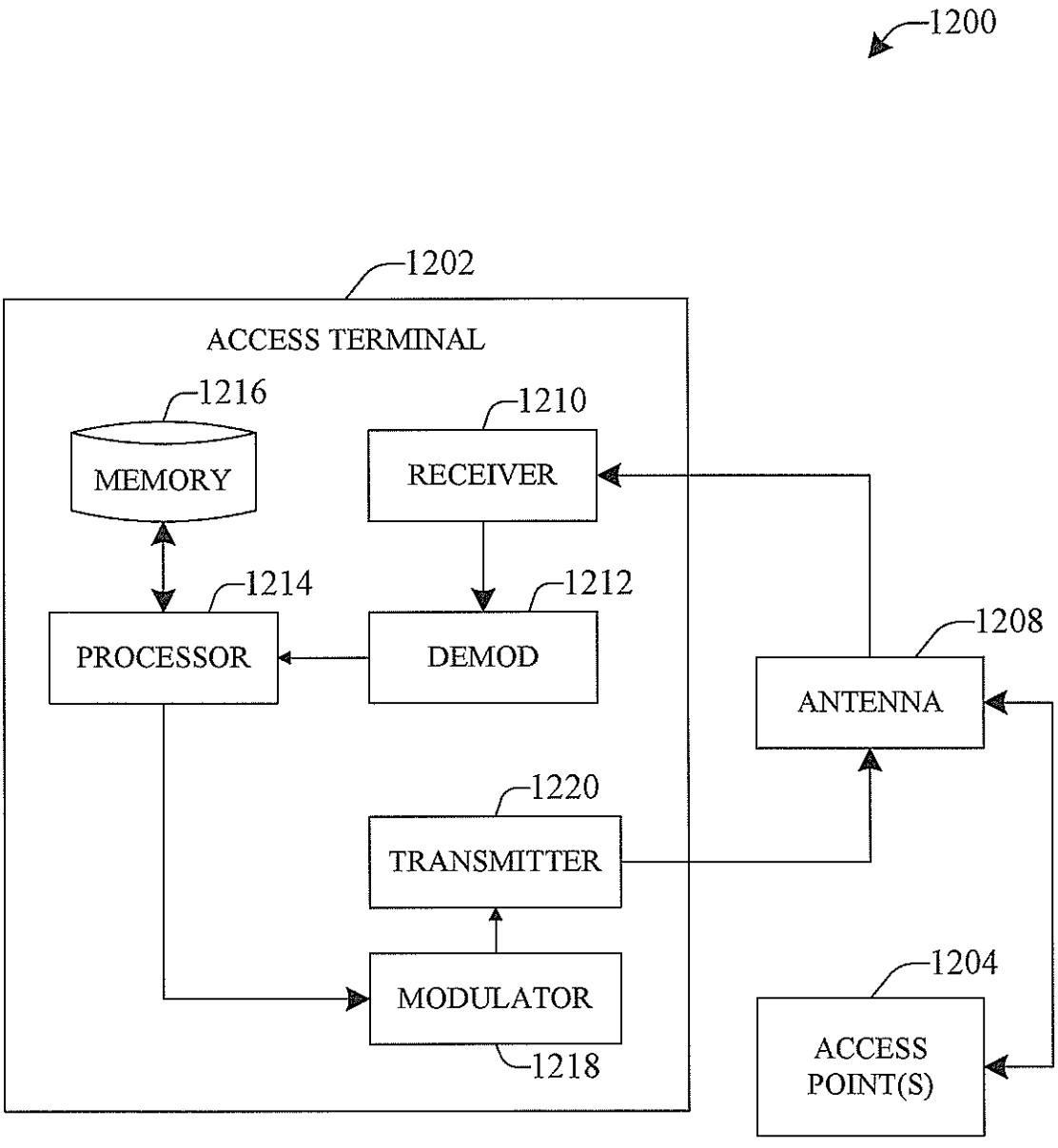

1200
1202
ACCESS TERMINAL
1216
MEMORY
1210
RECEIVER
1214
PROCESSOR
1212
DEMOD
1208
ANTENNA
1220
TRANSMITTER
MODULATOR
1218
1204
ACCESS POINT(S)

FIG. 12

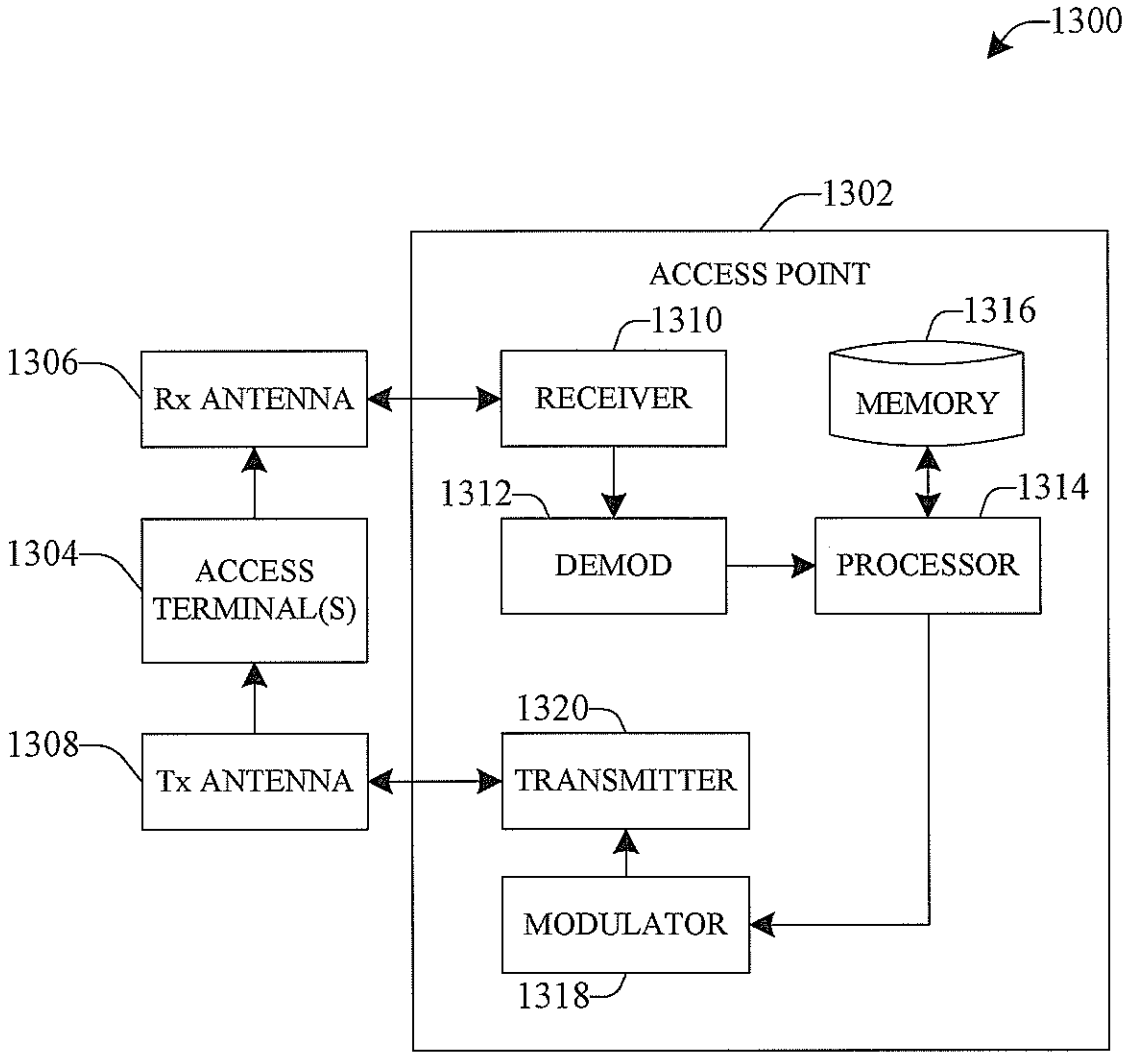

1300
1302
ACCESS POINT
1310
1316
1306
Rx ANTENNA
RECEIVER
MEMORY
1312
1314
1304
ACCESS TERMINAL(S)
DEMOD
PROCESSOR
1320
1308
Tx ANTENNA
TRANSMITTER
MODULATOR
1318

FIG. 13

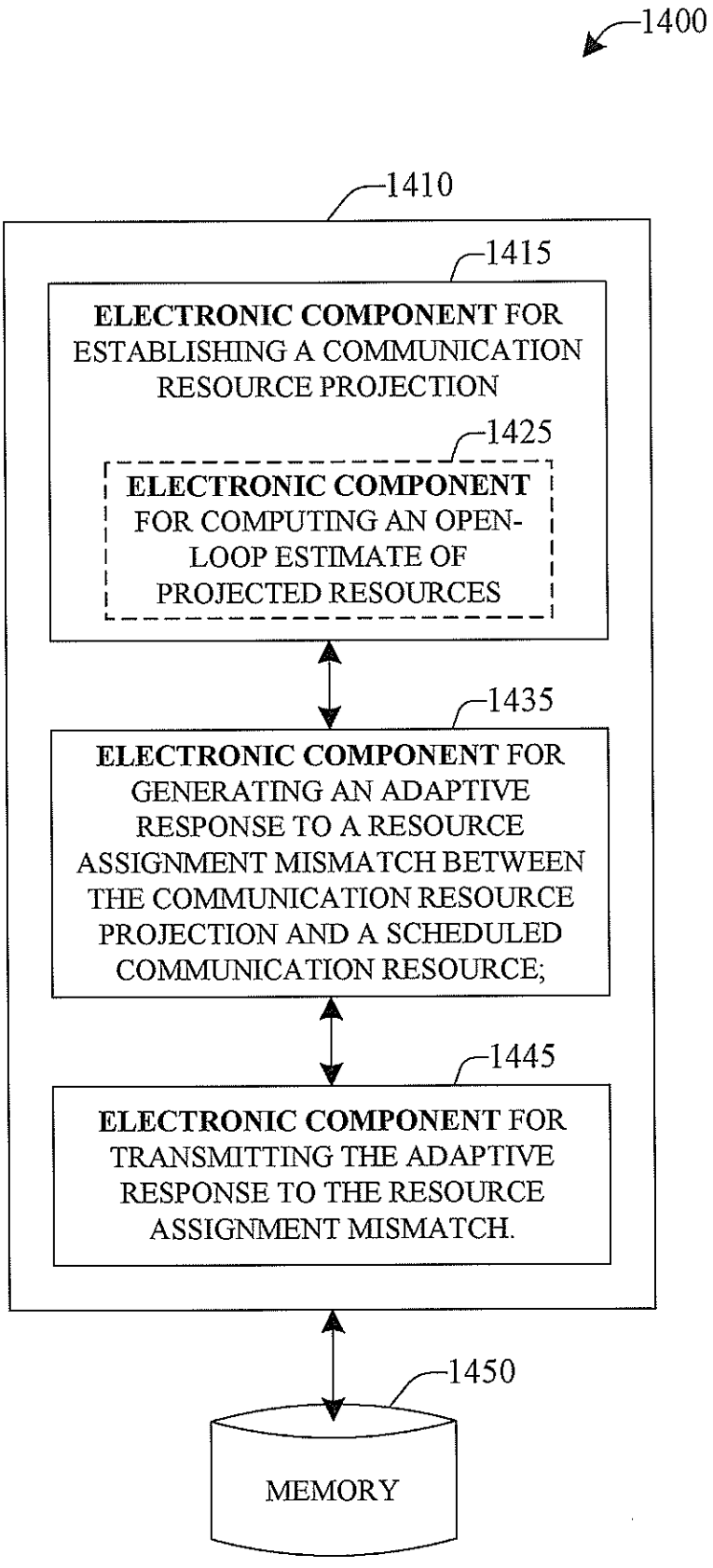

1400
1410
1415
ELECTRONIC COMPONENT FOR ESTABLISHING A COMMUNICATION RESOURCE PROJECTION
1425
ELECTRONIC COMPONENT FOR COMPUTING AN OPEN-LOOP ESTIMATE OF PROJECTED RESOURCES
1435
ELECTRONIC COMPONENT FOR GENERATING AN ADAPTIVE RESPONSE TO A RESOURCE ASSIGNMENT MISMATCH BETWEEN THE COMMUNICATION RESOURCE PROJECTION AND A SCHEDULED COMMUNICATION RESOURCE;
1445
ELECTRONIC COMPONENT FOR TRANSMITTING THE ADAPTIVE RESPONSE TO THE RESOURCE ASSIGNMENT MISMATCH.
1450
MEMORY

FIG. 14

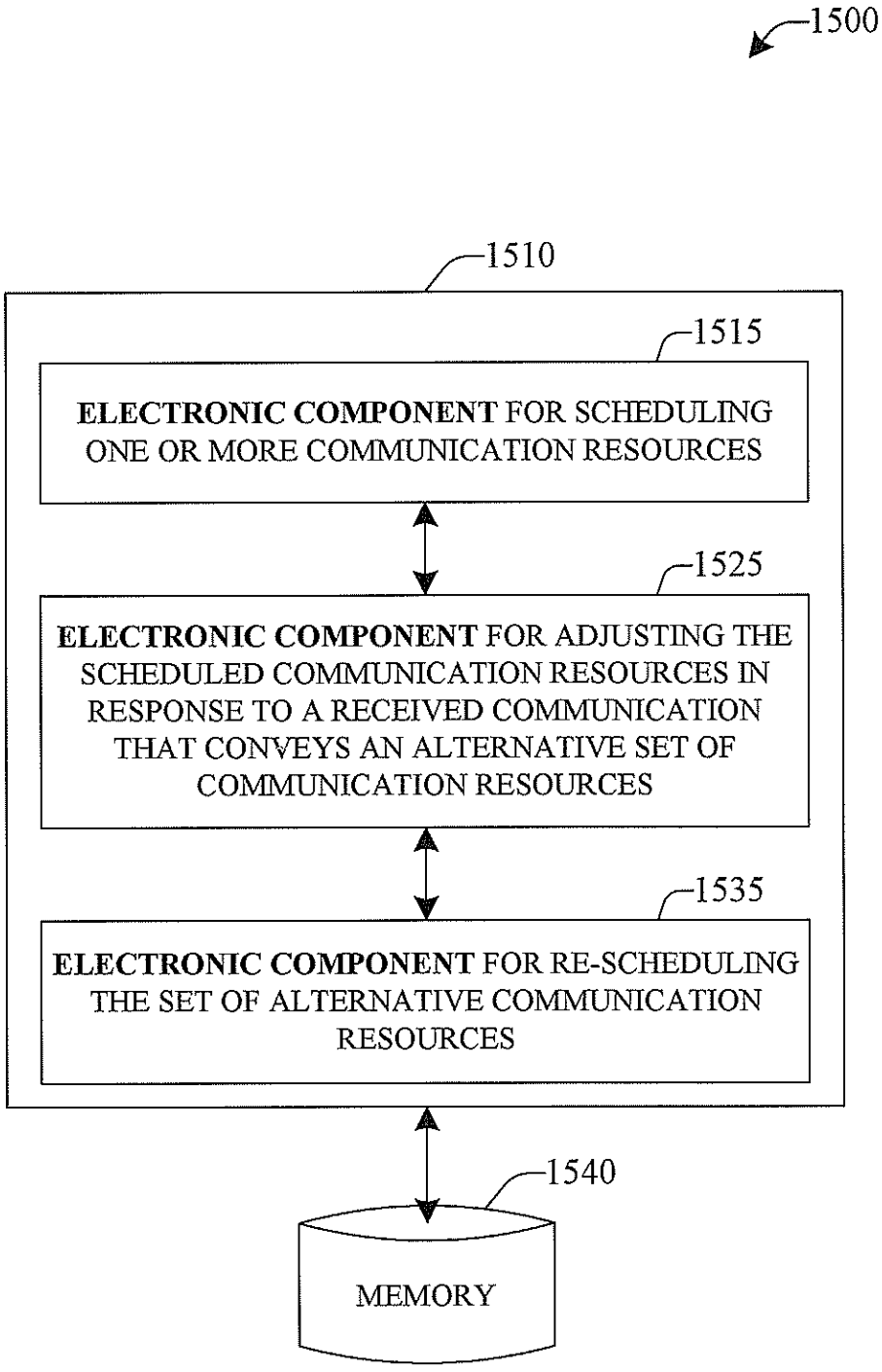

1500
1510
1515
ELECTRONIC COMPONENT FOR SCHEDULING ONE OR MORE COMMUNICATION RESOURCES
1525
ELECTRONIC COMPONENT FOR ADJUSTING THE SCHEDULED COMMUNICATION RESOURCES IN RESPONSE TO A RECEIVED COMMUNICATION THAT CONVEYS AN ALTERNATIVE SET OF COMMUNICATION RESOURCES
1535
ELECTRONIC COMPONENT FOR RE-SCHEDULING THE SET OF ALTERNATIVE COMMUNICATION RESOURCES
1540
MEMORY

FIG. 15