

(19) World Intellectual Property Organization  
International Bureau



(43) International Publication Date  
3 April 2003 (03.04.2003)

PCT

(10) International Publication Number  
**WO 03/028034 A3**

(51) International Patent Classification<sup>7</sup>: **G11C 15/04**

(21) International Application Number: PCT/US02/29749

(22) International Filing Date:  
23 September 2002 (23.09.2002)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:  
60/324,462 25 September 2001 (25.09.2001) US  
10/202,003 25 July 2002 (25.07.2002) US

(71) Applicant: **MICRON TECHNOLOGY, INC.** [US/US];  
8000 S. Federal Way, Boise, ID 83707-0006 (US).

(72) Inventors: **REGEV, Zvi**; 24217 Highlander Road, West Hills, CA 91307 (US). **REGEV, Alon**; 22716 Berdon Street, Woodland Hills, CA 91367-4303 (US).

(74) Agent: **D'AMICO, Thomas, J.**; Dickstein Shapiro Morin & Oshinsky LLP, 2101 L Street NW, Washington, DC 20037-1526 (US).

(81) Designated States (*national*): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, OM, PH, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, UZ, VC, VN, YU, ZA, ZM, ZW.

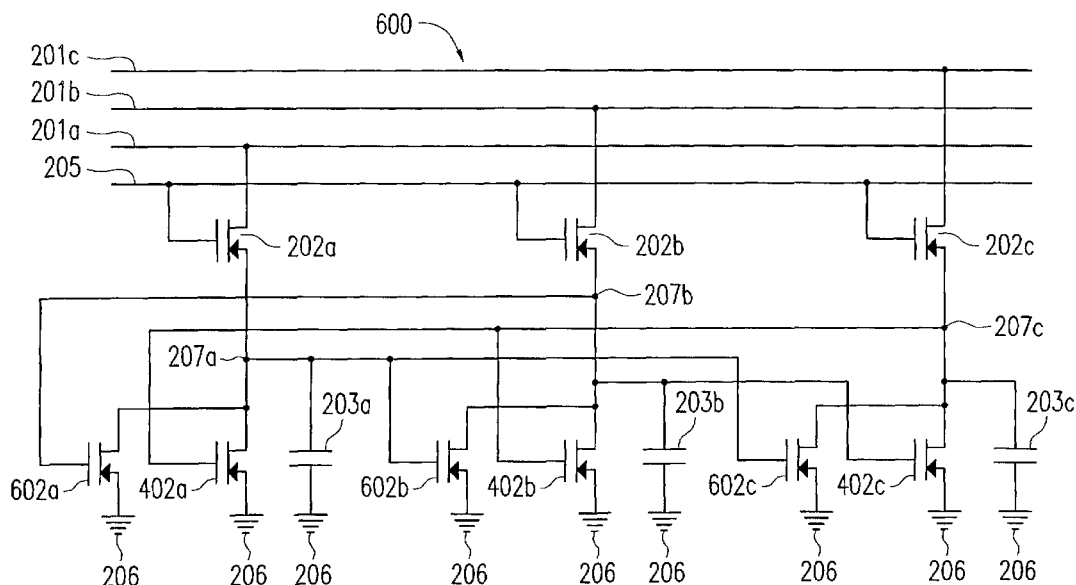
(84) Designated States (*regional*): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:  
— with international search report

(88) Date of publication of the international search report:  
30 October 2003

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: A TERTIARY CAM CELL



(57) Abstract: A tertiary CAM cell with three bits of storage is disclosed. The three bits of storage are arranged to support three stable states which can be read from the CAM cell without requiring a charge restoration operation. The three stable states are those states where one of the three bits is at a first logical state while the remaining two bits are at a second logical state. The three stable states may be used to encode the three logical states used in a ternary CAM.



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