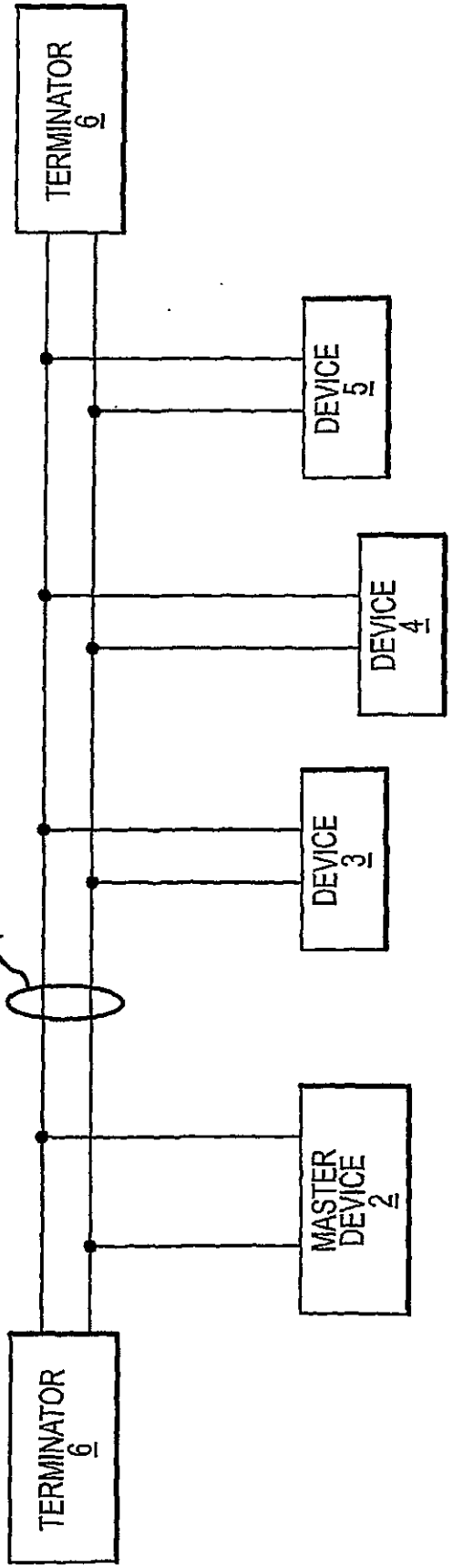
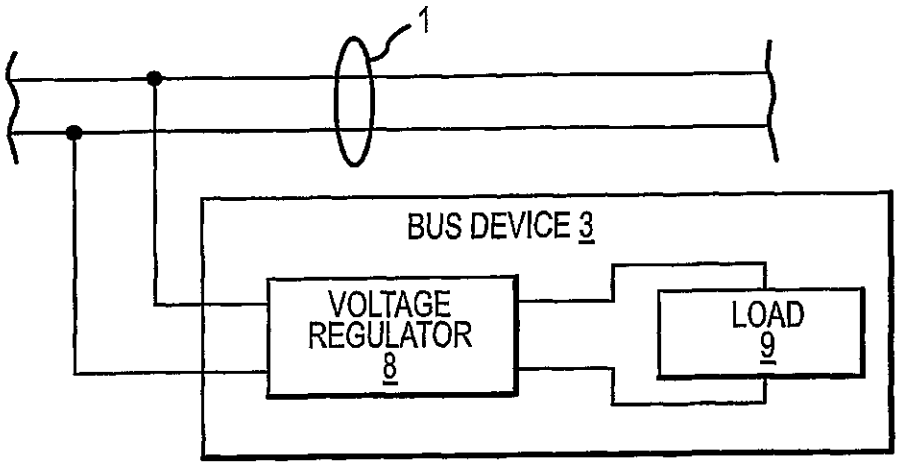


PRIOR ART
FIG. 1



PRIOR ART

FIG. 2

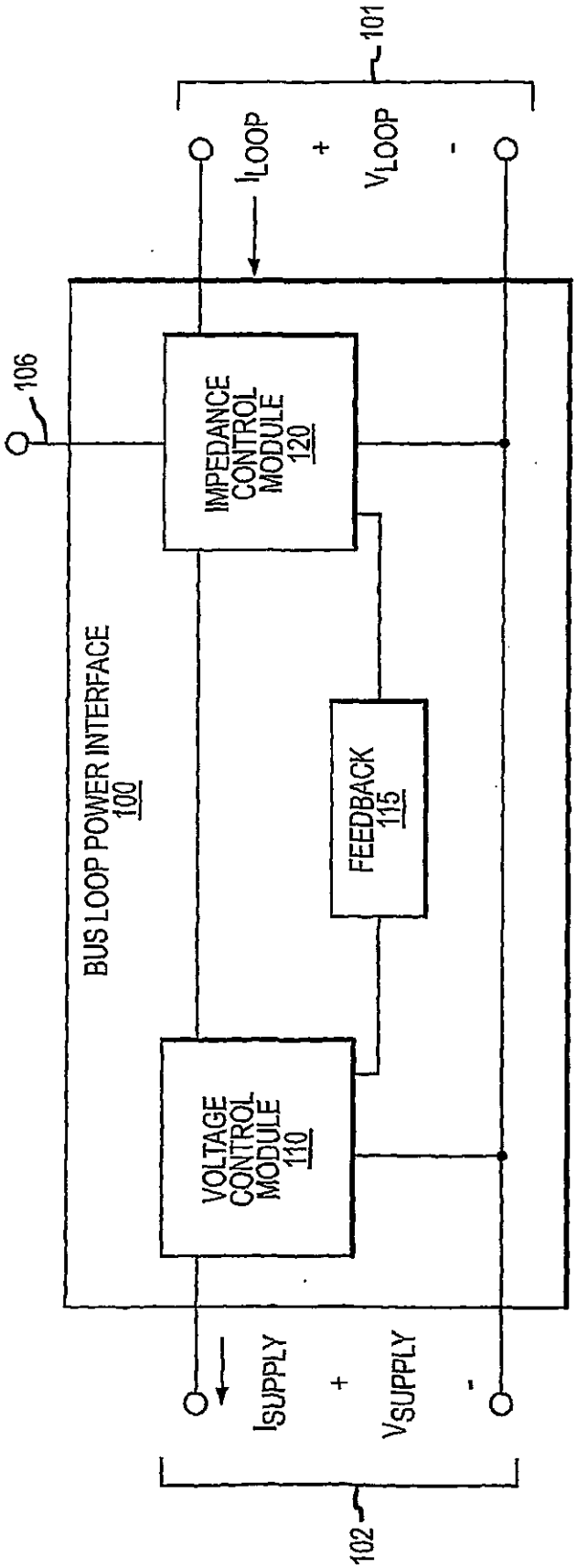

101
ILOOP
+
VLOOP
-
106
IMPEDANCE CONTROL MODULE 120
BUS LOOP POWER INTERFACE 100
FEEDBACK 115
VOLTAGE CONTROL MODULE 110
ISUPPLY
+
VSUPPLY
-
102

FIG. 3

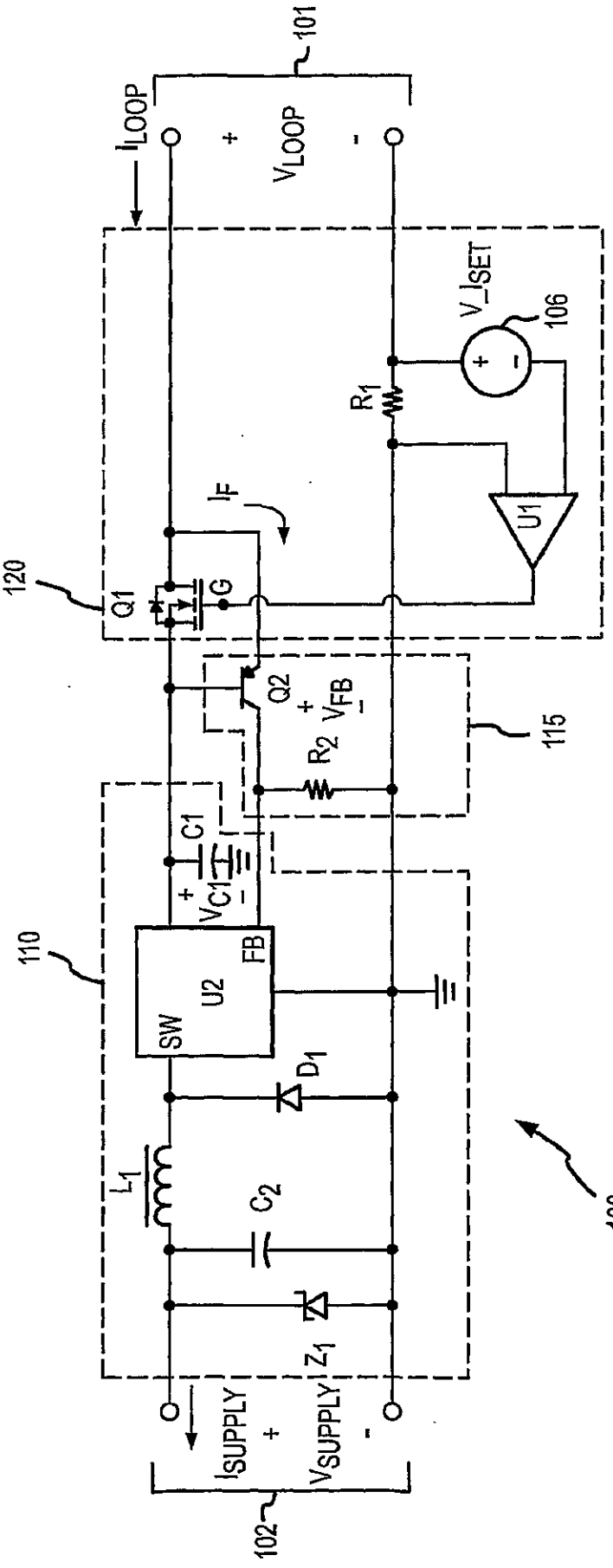

101
ILOOP
+
VLOOP
-
V_ISET
106
R1
IF
U1
120
Q1
G
Q2
VFB
R2
115
C1
VC1
110
FB
U2
SW
D1
L1
C2
Z1
100
ISUPPLY
+
VSUPPLY
-
102

FIG. 4