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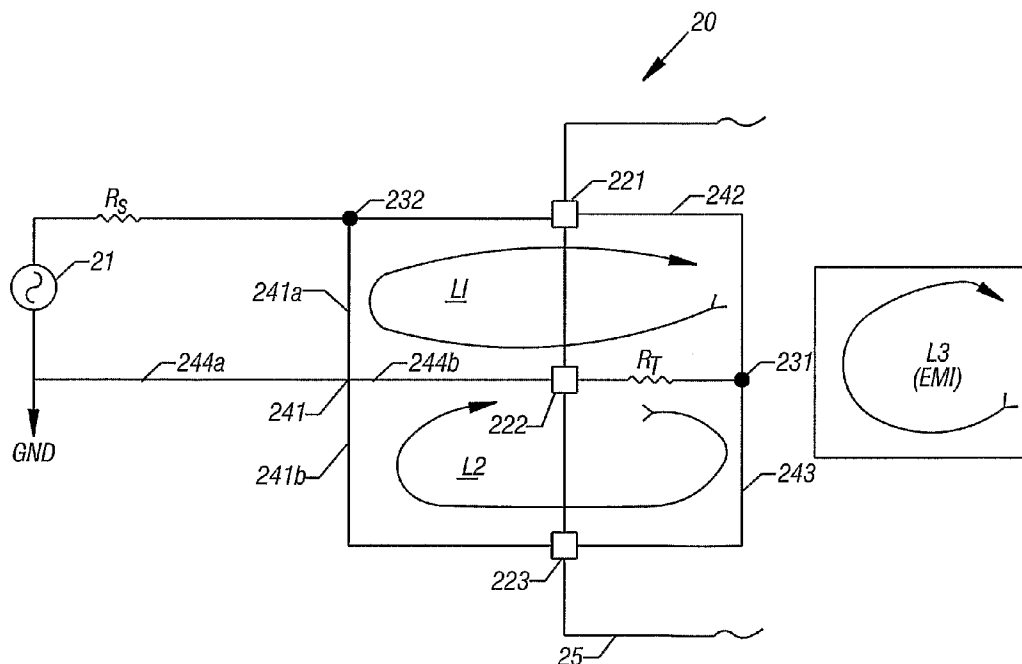
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(54) Title: MAGNETICALLY DIFFERENTIAL INPUT



(57) Abstract: A magnetically differential input circuit (20) is arranged to define at least two loops (L1 and L2), wherein each of the loops traverses the input (231) of a receiving circuit. The loops (L1 and L2) are physically arranged so that a source of interference (L3) induces opposing signals in the loops (L1 and L2), thereby effecting cancellation of the interference at the input (231) of the receiving circuit. In one embodiment, the input circuit (20) is arranged to be electrically differential as well as magnetically differential.

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