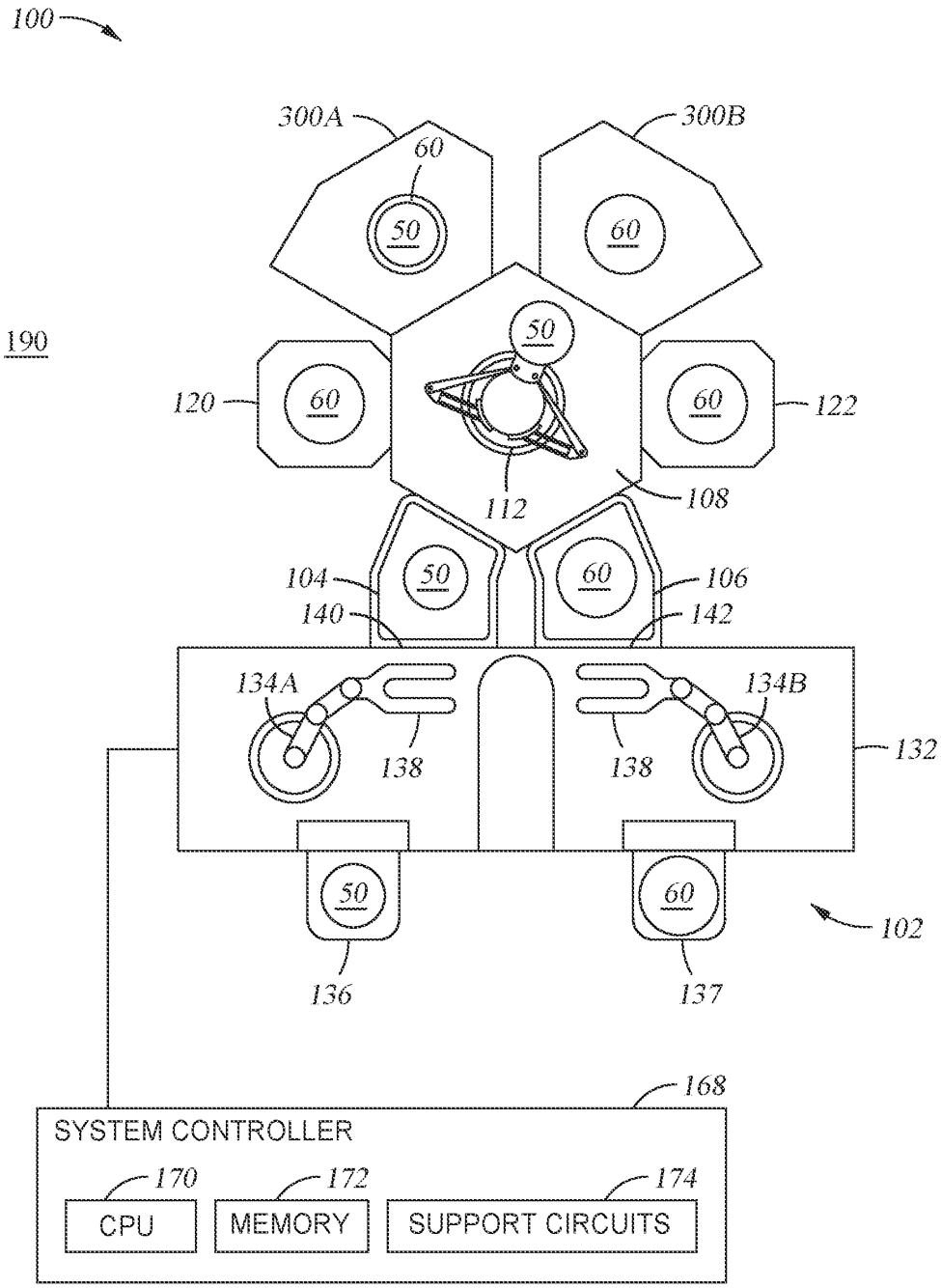

100
300A
300B
60
50
60
190
50
120
60
60
122
112
108
104
50
60
106
140
142
134A
134B
138
138
132
50
60
102
136
137
168
SYSTEM CONTROLLER
170
172
174
CPU
MEMORY
SUPPORT CIRCUITS

Fig. 1

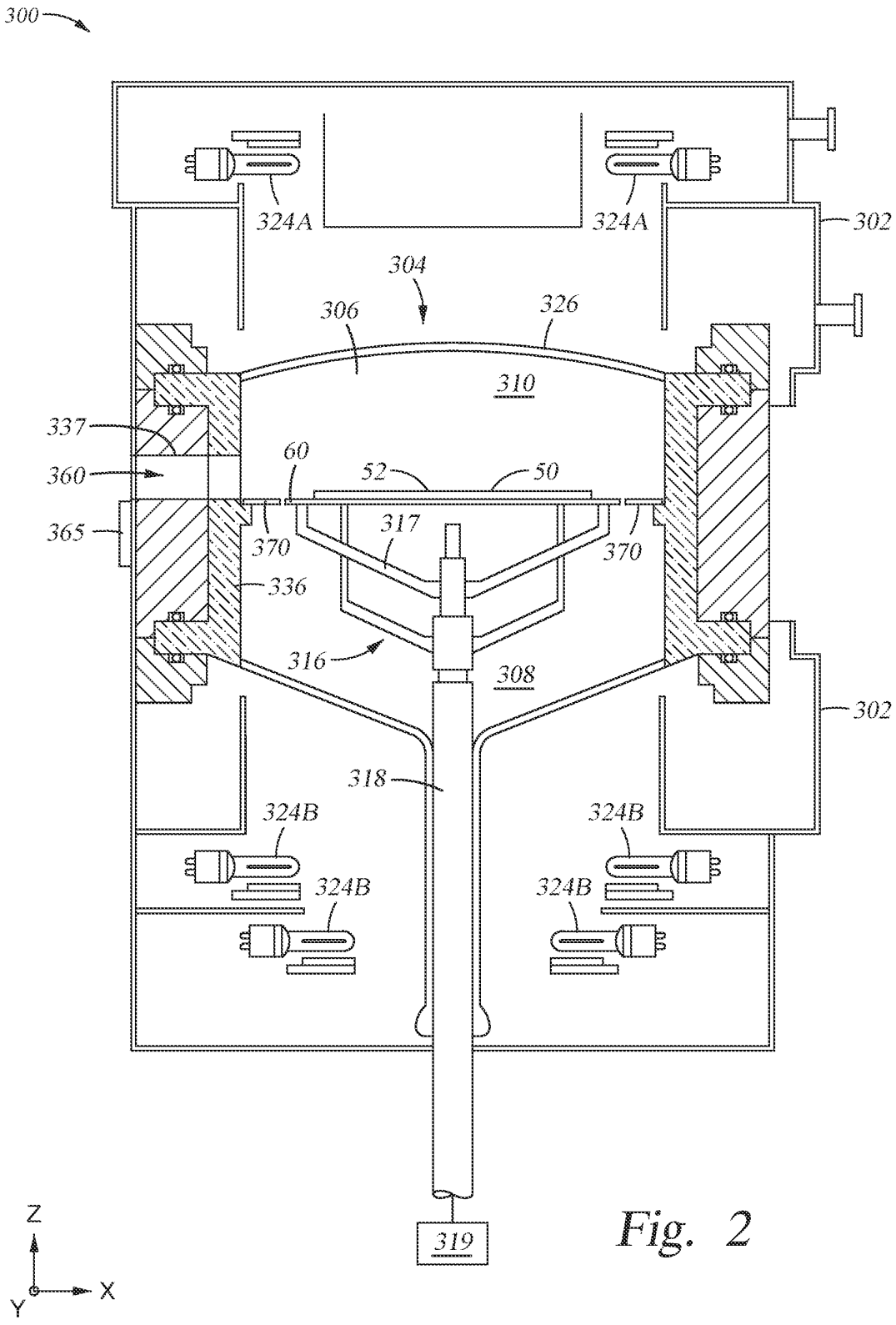

300
324A
324A
302
304
306
326
310
337
60
360
52
50
365
370
317
370
336
316
308
302
318
324B
324B
324B
324B
319
Z
X
Y

Fig. 2

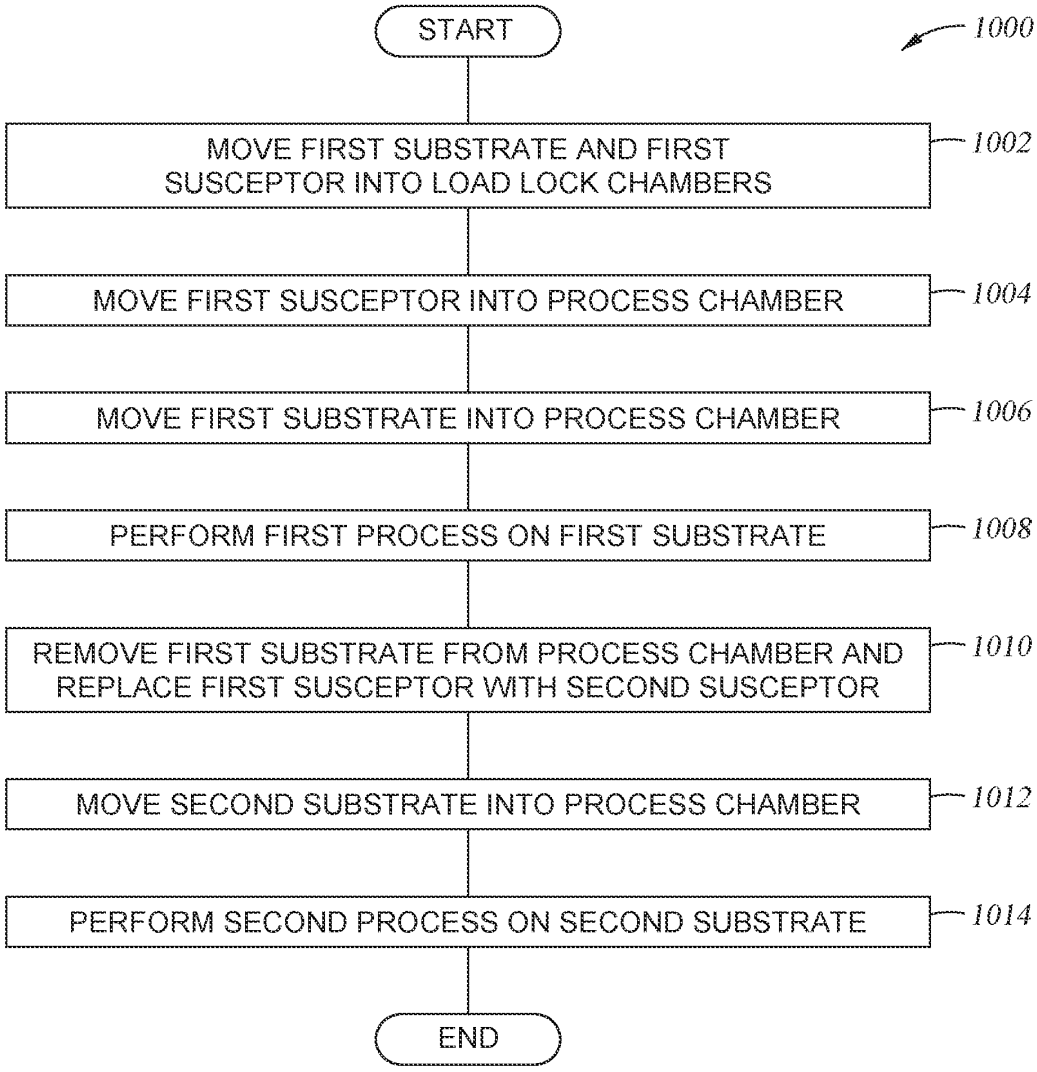

START
1000
MOVE FIRST SUBSTRATE AND FIRST SUSCEPTOR INTO LOAD LOCK CHAMBERS
1002
MOVE FIRST SUSCEPTOR INTO PROCESS CHAMBER
1004
MOVE FIRST SUBSTRATE INTO PROCESS CHAMBER
1006
PERFORM FIRST PROCESS ON FIRST SUBSTRATE
1008
REMOVE FIRST SUBSTRATE FROM PROCESS CHAMBER AND REPLACE FIRST SUSCEPTOR WITH SECOND SUSCEPTOR
1010
MOVE SECOND SUBSTRATE INTO PROCESS CHAMBER
1012
PERFORM SECOND PROCESS ON SECOND SUBSTRATE
1014
END

Fig. 3