

IMAGE PROCESSING APPARATUS AND METHOD

[ABSTRACT]

The present disclosure relates to an image processing apparatus and an image processing method that are enabled to suppress a decrease in parallelism of coding and decoding.

Coefficient data related to an image is coded in parallel in each line of coding tree units each including transformation blocks and corresponding to a topmost coding block in a tree structure, and coding of each line is performed so as to be delayed by an amount corresponding to one coding tree unit with respect to coding of a line immediately above the line to be coded. Additionally, coded data obtained by coding coefficient data related to an image is decoded in parallel in each line of coding tree units each including transformation blocks and corresponding to a topmost coding block in a tree structure, and decoding of each line is performed so as to be delayed by an amount corresponding to one coding tree unit with respect to decoding of a line immediately above the line to be decoded. The present disclosure can be applied to, for example, an image processing apparatus, an image coding apparatus, an image decoding apparatus, or the like.

Fig. 5