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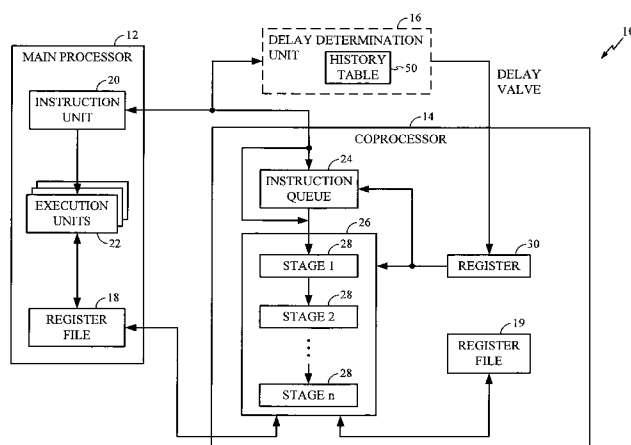
- as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii))
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(54) Title: METHOD AND APPARATUS FOR EXECUTING PROCESSOR INSTRUCTIONS BASED ON A DYNAMICALLY ALTERABLE DELAY



(57) **Abstract:** Instruction execution delay is alterable after the system design has been finalized, thus enabling the system to dynamically account for various conditions that impact instruction execution. In some embodiments, the dynamic delay is determined by an application to be executed by the processing system. In other embodiments, the dynamic delay is determined by analyzing the history of previously executed instructions. In yet other embodiments, the dynamic delay is determined by assessing the processing resources available to a given application. Regardless, the delay may be dynamically altered on a per-instruction, multiple instruction, or application basis. Processor instruction execution may be controlled by determining a first delay value for a first set of one or more instructions and a second delay value for a second set of one or more instructions. Execution of the sets of instructions is delayed based on the corresponding delay value.

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- *before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments*