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154 **METHOD AND APPARATUS FOR MULTILEVEL TRANSLATION**
155 **AND PROTECTION TABLE**
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158 **ABSTRACT OF THE DISCLOSURE**

159 A host is provided that includes a processor, a host memory coupled to the
160 processor, and a host-fabric adapter coupled to the processor and provided to interface
161 with a switched fabric including one or more fabric-attached I/O controllers. The host-
162 fabric adapter may include logic for accessing a translation and protection table from the
163 host memory for a data transaction. The translation and protection table may include a
164 plurality of translation entries and at least one region entry. The at least one region entry
165 may include multiple levels of translation entries.

166
167 Figure 8