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(71) Applicant: **CREA - COLLAUDI ELETTRONICI AUTOMATIZZATI S.R.L.** [IT/IT]; Via Marie Curie 13, I-10073 CIRIÉ (Torino) (IT).

(72) Inventors: **CIANFEROTTI, Carlo**; Via Giovanni Verga 8/B, I-57025 PIOMBINO (Livorno) (IT). **MARCINNO', Marco**; Via Rivette 7, I-10073 CIRIÉ (Torino) (IT). **RA-**

VA, Alberto; Via G. Scotti 11, I-14055 COSTIGLIOLE D'ASTI (Asti) - Frazione Motta (IT). **RUO ROCH, Massimo**; Via Molino 9, I-10040 LA LOGGIA (Torino) (IT).

(74) Agent: **CRISTINELLI, Luca** et al.; c/o JACOBACCI & PARTNERS S.P.A., Corso Emilia 8, I-10152 Torino (IT).

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(54) Title: TESTING SYSTEM AND TEST METHOD

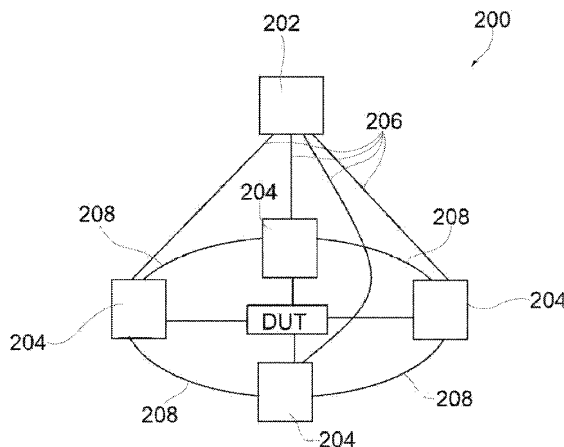


FIG. 2

(57) Abstract: A test system (200) is described for applying test stimuli and test measurements to at least one device to be tested (DUT), comprising: - electronic control means (202) and a plurality of test modules (204); - a first communication network (206) arranged to enable communication between the electronic control means (202) and the plurality of test modules (204) at a first communication speed; - a second communication network (208) arranged to enable communication between the plurality of test modules (204) at a second communication speed with latency lower than a maximum latency; the value of the maximum latency is a function of a discretization time of the test stimuli and test measurements to be applied by the test system.

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