

**METHODS OF FORMING SEMICONDUCTOR DEVICE STRUCTURES
INCLUDING TWO-DIMENSIONAL MATERIAL STRUCTURES**

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PRIORITY CLAIM

This application claims the benefit of the filing date of United States Patent Application Serial No. 15/253,454, filed August 31, 2016, for “METHODS OF FORMING SEMICONDUCTOR DEVICE STRUCTURES INCLUDING TWO-DIMENSIONAL MATERIAL STRUCTURES, AND RELATED SEMICONDUCTOR DEVICE STRUCTURES AND ELECTRONIC SYSTEMS.”

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TECHNICAL FIELD

Embodiments of the disclosure relate to the field of semiconductor device design and fabrication. More specifically, embodiments of the disclosure relate to methods of forming semiconductor device structures including two-dimensional material structures, and to related semiconductor device structures, semiconductor devices, and electronic systems.

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BACKGROUND

Two-dimensional (2D) materials are currently being investigated for use in various semiconductor devices. As used herein, a “two-dimensional material” or “2D material” refers to a crystalline material formed of and including a single (e.g., only one) monolayer of units (e.g., atoms, molecules) bonded together through intramolecular forces (e.g., covalent bonds). Adjacent 2D materials of a structure (e.g., a stack structure) including multiple 2D materials are coupled to one another through one or more intermolecular forces (e.g., Van der Waals forces). Put another way, units (e.g., atoms, molecules) of a single 2D material are coupled to one another through intramolecular forces, and may be coupled to units (e.g., atoms, molecules) of a second 2D material adjacent (e.g., thereover, thereunder) thereto (if any) through intermolecular forces. The thin structure of 2D materials, along with a direct band gap in the visible portion of the electromagnetic spectrum, suggests that 2D materials are suitable for use in a wide variety of digital electronic devices and optoelectronic devices.

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Unfortunately, problems associated with conventionally forming 2D materials can reduce the performance and reliability of semiconductor devices (e.g., digital electronic devices, optoelectronic devices) into which the 2D materials are incorporated. For example,

conventional methods of forming 2D materials can result in significant crystalline defects within the 2D materials that can negatively impact the properties of semiconductor device structures and semiconductor devices including the 2D materials. For example, conventionally formed 2D materials can exhibit undesirable interstitial and vacancy defects, such as those
5 described by Haldar, S., et al., “A systematic study of structural, electronic and optical properties of atomic scale defects in 2D transition metal dichalcogenides MX_2 ($\text{M} = \text{Mo}, \text{W}$; $\text{X} = \text{S}, \text{Se}, \text{Te}$),” *Phys. Rev. B* 92, 2015. Such crystalline defects can effectuate non-uniform local electron densities, current leakage, and shallow subthreshold slope in the 2D materials, resulting in unacceptable semiconductor device variation.

10 It would, therefore, be desirable to have improved methods of forming 2D materials permitting the fabrication of semiconductor device structures and semiconductor devices having improved performance characteristics.

BRIEF DESCRIPTION OF THE DRAWINGS

15 FIG. 1 is a partial cross-sectional view of a semiconductor device structure, in accordance with embodiments of the disclosure.

FIG. 2 is a graph illustrating vibrational spectra for a crystalline-defect-free (“perfect”) form of molybdenum disulfide (MoS_2) and a crystalline-defect-laden (“defective”) form of MoS_2 .

20 FIG. 3 is a schematic block diagram of an electronic system, in accordance with embodiments of the disclosure.

MODE(S) FOR CARRYING OUT THE INVENTION

Methods of forming semiconductor device structures including 2D material
25 structures are disclosed, as are related semiconductor device structures, semiconductor devices, and electronic systems. In some embodiments, a method of forming a semiconductor device structure includes forming at least one 2D material on or over a substrate, and subjecting the 2D material to at least one laser treatment process to selectively energize, mobilize, and at least partially (e.g., substantially) eliminate crystalline
30 defects (e.g., interstitial defects, vacancy defects) within the 2D material. The laser treatment process includes selecting at least one frequency of electromagnetic radiation to expose the 2D material to at least partially based on a comparison of vibrational spectra for a crystalline-defect-free form of the 2D material and a crystalline-defect-laden form of the 2D material, and

then exposing the 2D material to the selected frequency (or frequencies) of radiation during and/or after the formation of the 2D material on or over the substrate. The frequency (or frequencies) of electromagnetic radiation may be selected by identifying mutual (e.g., common, shared) resonant frequencies along the vibrational spectra where there is a
5 difference between the resonant peak intensities of the crystalline-defect-free form of the 2D material and the crystalline-defect-laden form of the 2D material. The at least one selected frequency of electromagnetic radiation may directly correspond to (e.g., be substantially the same as, such as exactly the same as) one or more of such mutual resonant frequencies. Optionally, one or more of thermal annealing process and a remote plasma treatment process
10 may be used in conjunction with the laser treatment process. For example, at least one thermal annealing process may be performed before, during, and/or after the laser treatment process to assist with or at least partially facilitate the removal of crystalline defects from the 2D material. The methods of the disclosure may efficiently reduce or even eliminate a crystalline defect density within the 2D material to form a 2D material structure having
15 enhanced electrically properties as compared to conventional 2D material structures formed without the use of the methods of the disclosure.

The following description provides specific details, such as material compositions and processing conditions, in order to provide a thorough description of embodiments of the present disclosure. However, a person of ordinary skill in the art would understand that the
20 embodiments of the present disclosure may be practiced without employing these specific details. Indeed, the embodiments of the present disclosure may be practiced in conjunction with conventional semiconductor fabrication techniques employed in the industry. In addition, the description provided below does not form a complete process flow for manufacturing a semiconductor device (e.g., a memory device). The semiconductor device
25 structures described below do not form a complete semiconductor device. Only those process acts and structures necessary to understand the embodiments of the present disclosure are described in detail below. Additional acts to form a complete semiconductor device from the semiconductor device structures may be performed by conventional fabrication techniques.

Drawings presented herein are for illustrative purposes only, and are not meant to be
30 actual views of any particular material, component, structure, device, or system. Variations from the shapes depicted in the drawings as a result, for example, of manufacturing techniques and/or tolerances, are to be expected. Thus, embodiments described herein are not to be construed as being limited to the particular shapes or regions as illustrated, but include

deviations in shapes that result, for example, from manufacturing. For example, a region illustrated or described as box-shaped may have rough and/or nonlinear features, and a region illustrated or described as round may include some rough and/or linear features. Moreover, sharp angles that are illustrated may be rounded, and vice versa. Thus, the regions illustrated in the figures are schematic in nature, and their shapes are not intended to illustrate the precise shape of a region and do not limit the scope of the present claims. The drawings are not necessarily to scale. Additionally, elements common between figures may retain the same numerical designation.

As used herein, the terms “vertical,” “longitudinal,” “horizontal,” and “lateral” are in reference to a major plane of a structure and are not necessarily defined by earth’s gravitational field. A “horizontal” or “lateral” direction is a direction that is substantially parallel to the major plane of the structure, while a “vertical” or “longitudinal” direction is a direction that is substantially perpendicular to the major plane of the structure. The major plane of the structure is defined by a surface of the structure having a relatively large area compared to other surfaces of the structure.

As used herein, spatially relative terms, such as “beneath,” “below,” “lower,” “bottom,” “above,” “upper,” “top,” “front,” “rear,” “left,” “right,” and the like, may be used for ease of description to describe one element’s or feature’s relationship to another element(s) or feature(s) as illustrated in the figures. Unless otherwise specified, the spatially relative terms are intended to encompass different orientations of the materials in addition to the orientation depicted in the figures. For example, if materials in the figures are inverted, elements described as “below” or “beneath” or “under” or “on bottom of” other elements or features would then be oriented “above” or “on top of” the other elements or features. Thus, the term “below” can encompass both an orientation of above and below, depending on the context in which the term is used, which will be evident to one of ordinary skill in the art. The materials may be otherwise oriented (e.g., rotated 90 degrees, inverted, flipped) and the spatially relative descriptors used herein interpreted accordingly.

As used herein, the singular forms “a,” “an,” and “the” are intended to include the plural forms as well, unless the context clearly indicates otherwise.

As used herein, the term “configured” refers to a size, shape, material composition, and arrangement of one or more of at least one structure and at least one apparatus facilitating operation of one or more of the structure and the apparatus in a pre-determined way.

As used herein, the phrase “coupled to” refers to structures operatively connected with each other, such as electrically connected through a direct ohmic connection or through an indirect connection (e.g., via another structure).

As used herein, the term “substantially” in reference to a given parameter, property, or condition means and includes to a degree that one of ordinary skill in the art would understand that the given parameter, property, or condition is met with a degree of variance, such as within acceptable manufacturing tolerances. By way of example, depending on the particular parameter, property, or condition that is substantially met, the parameter, property, or condition may be at least 90.0% met, at least 95.0% met, at least 99.0% met, or even at least 99.9% met.

As used herein, the term “about” in reference to a given parameter is inclusive of the stated value and has the meaning dictated by the context (e.g., it includes the degree of error associated with measurement of the given parameter).

An embodiment of the disclosure will now be described with reference to FIG. 1, which illustrates a semiconductor device structure 100 including a substrate 102 and a 2D material structure 104 on or over the substrate 102. While FIG. 1 depicts a particular configuration of the semiconductor device structure 100, one of ordinary skill in the art will appreciate that different semiconductor device structure configurations (e.g., shapes, sizes, etc.) are known in the art and that may be adapted to be employed in embodiments of the disclosure. FIG. 1 illustrates just one non-limiting example of the semiconductor device structure 100.

The substrate 102 may comprise any base material or construction upon which additional materials may be formed. The substrate 102 may be a semiconductor substrate, a base semiconductor material on a supporting structure, a metal electrode, or a semiconductor substrate having one or more materials, structures, or regions formed thereon. The substrate 102 may be a conventional silicon substrate or other bulk substrate comprising a layer of semiconductive material. As used herein, the term “bulk substrate” means and includes not only silicon wafers, but also silicon-on-insulator (SOI) substrates, such as silicon-on-sapphire (SOS) substrates and silicon-on-glass (SOG) substrates, epitaxial layers of silicon on a base semiconductor foundation, and other semiconductor or optoelectronic materials, such as silicon-germanium, germanium, gallium arsenide, gallium nitride, and indium phosphide. The substrate 102 may be doped or undoped. By way of non-limiting example, a substrate 102 may comprise at least one of silicon, silicon dioxide, silicon with native oxide, silicon nitride,

a carbon-containing silicon nitride, glass, semiconductor, metal oxide, metal, titanium nitride, carbon-containing titanium nitride, tantalum, tantalum nitride, carbon-containing tantalum nitride, niobium, niobium nitride, carbon-containing niobium nitride, molybdenum, molybdenum nitride, carbon-containing molybdenum nitride, tungsten, tungsten nitride,
5 carbon-containing tungsten nitride, copper, cobalt, nickel, iron, aluminum, and a noble metal.

The 2D material structure 104 is formed of and includes one or more 2D materials. By way of non-limiting example, the 2D material structure 104 may be formed of and include one or more of graphene; graphene-oxide; stanene; phosphorene; hexagonal boron nitride (h-BN); borophene; silicene; graphyne; germanene; germanane; a 2D supracrystal; a transition
10 metal di-chalcogenide (TMDC) having the general chemical formula MX_2 , wherein M is a transition metal (e.g., molybdenum (Mo), tungsten (W), niobium (Nb), zirconium (Zr), hafnium (Hf), rhenium (Re), platinum (Pt), titanium (Ti), tantalum (Ta), vanadium (V), cobalt (Co) cadmium (Cd), or chromium (Cr)) and X is a chalcogen (e.g., sulfur (S), selenium (Se), or tellurium (Te)); a carbide or carbonitride having the general chemical formula $M_{n+1}X_n$ (also
15 referred to as an “MXene”) and including oxygen (–O), hydroxyl (–OH), or fluoro (–F) surface termination, wherein M is a transition metal from Groups IV or V of the Periodic Table of Elements (e.g., Ti, Hf, Zr, V, Nb, Ta) and X is selected from carbon (C) and nitrogen (N); and a monolayer of a metal material (e.g., palladium (Pd), rhodium (Rh)), a semi-metal material, or a semiconductive material. In some embodiments, the 2D material structure 104 is formed
20 of and includes one or more TMDC monolayer(s), such as one or more monolayer(s) of MoS_2 , $MoSe_2$, $MoTe_2$, WS_2 , WSe_2 , WTe_2 , $NbSe_2$, ZrS_2 , $ZrSe_2$, HfS_2 , $HfSe_2$, and $ReSe_2$.

The 2D material structure 104 may include a single (e.g., only one) 2D material, or may include multiple (e.g., more than one) 2D materials. By way of non-limiting example, the 2D material structure 104 may be formed of and include a stack of at least two different
25 2D materials. A first of the 2D materials may be formed on or over the substrate 102, and a second of the 2D materials may be formed on the first of the 2D materials. In addition, one or more additional 2D materials may, optionally, be formed on or over the second of the 2D materials. If the 2D material structure 104 comprises a stack of different 2D materials, longitudinally adjacent 2D materials of the stack may be coupled to one another through one
30 or more intermolecular forces, such as Van der Waals forces (e.g., as opposed to intramolecular forces, such as covalent bonds). In some embodiments, the 2D material structure 104 is formed of and includes only one 2D material. In additional embodiments, the 2D material structure 104 is formed of and includes more than one 2D

material. While the 2D material structure 104 is illustrated in FIG. 1 as a planar structure, in additional embodiments the 2D material structure 104 may exhibit a different structural configuration, such as a non-planar structure.

As described in further detail below, the 2D material structure 104 exhibits reduced
5 crystalline defects as compared to conventional 2D material structures having the same 2D material composition (e.g., formed of and including the same 2D material(s)) but formed through conventional processes. The 2D material structure 104 may, for example, exhibit a reduced density of interstitial defects and/or vacancy defects as compared to conventional 2D material structures having the same 2D material composition. By way of non-limiting
10 example, if the 2D material structure 104 is formed of and includes at least one TMDC having the general chemical formula MX_2 (e.g., MoS_2 , MoSe_2 , MoTe_2 , WS_2 , WSe_2 , WTe_2 , NbSe_2 , ZrS_2 , ZrSe_2 , HfS_2 , HfSe_2 , ReSe_2), the TMDC may have a reduced number of one or more of X-interstitial defects, X-vacancy defects, M-interstitial defects, M-vacancy defects, MX-vacancy defects, and XX-vacancy defects as compared to a 2D material structure having the
15 same 2D material composition, but formed through a conventional process. The above-mentioned defects are collectively referred to herein as crystalline defects. In some embodiments, the 2D material structure 104 is formed of at least one TMDC at least exhibiting a reduced number of X-interstitial and X-vacancy defects as compared to a 2D material structure formed of the at least one TMDC through a conventional process. In
20 additional embodiments, the 2D material structure 104 is formed of and includes at least one 2D material substantially free (e.g., completely free) of all crystalline defects.

The 2D material structure 104 may be formed on or over the substrate 102 by forming (e.g., growing, depositing) at least one 2D material on or over the substrate 102, and
25 subjecting the 2D material to at least one laser treatment process employing one or more selected frequencies of electromagnetic radiation corresponding to (e.g., the same as) one or more resonant frequencies of crystalline defects of the 2D material. Exposing the 2D material to the selected frequencies of electromagnetic radiation at least partially (e.g., substantially) eliminates the crystalline defects from the 2D material. The selected frequencies of radiation selectively energize, dissociate, and mobilize the crystalline defects of the 2D material. The
30 crystalline defects migrate (e.g., diffuse) in-plane toward terminal ends (e.g., peripheral sides, peripheral edges) of the 2D material, where they may be removed from (e.g., eliminated from) the 2D material. In addition, as the crystalline defects move across the 2D material, complementary defects (e.g., oppositely charged defects) coming into geometric proximity to

one another may be attracted to each other (e.g., by way of coulombic attraction) and interact to eliminate one another. For example, if the 2D material comprises a TMDC, a mobile X-interstitial defect may be attracted to and interact with a mobile X-vacancy defect in close geometric proximity thereto and eliminate both the X-interstitial defect and the X-vacancy defect, and/or a mobile M-interstitial defect may be attracted to and interact with a mobile M-vacancy defect in close geometric proximity thereto and eliminate both the M-interstitial defect and the M-vacancy defect.

The at least one 2D material may be formed on or over the substrate 102 using one or more of a growth process and a deposition process. By way of non-limiting example, the 2D material may be formed on or over the substrate 102 using one or more of an in situ growth process, a physical vapor deposition (“PVD”) process, a chemical vapor deposition (“CVD”) process, a metallorganic chemical vapor deposition (“MOCVD”) process, a plasma-enhanced chemical vapor deposition (PECVD) process, an atomic layer deposition (“ALD”) process, a spin-coating process, and a blanket coating process. In situ growth processes include, but are not limited to, epitaxial growth processes, such as atomic layer epitaxy (ALE), pulsed atomic layer epitaxy (PALE), molecular beam epitaxy (MBE), gas source MBE, organometallic MBE, and chemical beam epitaxy (CBE). PVD processes include, but are not limited to, one or more of sputtering, evaporation, and ionized PVD. The process utilized to form the 2D material on or over the substrate 102 may at least partially depend on the material properties of the 2D material and the substrate 102, and may affect the number (e.g., density) of crystalline defects present in the 2D material. In some embodiments, the 2D material is formed on or over the substrate 102 using a CVD process.

The laser treatment process includes selecting at least one frequency of electromagnetic radiation to expose the at least one 2D material to, and exposing the 2D material to the selected frequency of electromagnetic radiation. The frequency of electromagnetic radiation may be selected at least partially based on a comparison of vibrational spectra for a crystalline-defect-free (e.g., perfect, pristine) form of the 2D material and a crystalline-defect-laden (e.g., imperfect, defective) form of the 2D material. Mutual resonant frequencies along the vibrational spectra (e.g., as compared by a vibrational difference spectrum) for the crystalline-defect-free form of the 2D material and the crystalline-defect-laden form of the 2D material that do not exhibit substantial overlap between resonant peak intensities of the crystalline-defect-free form of the 2D material and the crystalline-defect-laden form of the 2D material identify frequencies of electromagnetic

radiation that may be used to selectively energize, mobilize, and at least partially (e.g., substantially) eliminate crystalline defects within the 2D material.

As a non-limiting example, FIG. 2 is a graph plotting vibrational spectra for a crystalline-defect-free (“perfect”) form of MoS₂ and a crystalline-defect-laden (“defective”) form of MoS₂. As shown in FIG. 2, the graph identifies different resonant frequencies where the resonant peak intensity of the crystalline-defect-laden form of MoS₂ is substantially offset from (e.g., substantially different than) the resonant peak intensity of the crystalline-defect-free form of MoS₂. For example, the crystalline-defect-laden form of MoS₂ exhibits relatively high resonant peak intensities at least at resonant frequencies of about 224.3 cm⁻¹, about 345.1 cm⁻¹, and about 382.2 cm⁻¹, whereas the crystalline-defect-free form of MoS₂ exhibits relatively low resonant peak intensities at those resonant frequencies. Accordingly, based on the vibrational spectra for the crystalline-defect-free form of MoS₂ and the crystalline-defect-laden form of MoS₂, one or more of an electromagnetic radiation frequency of about 224.3 cm⁻¹, an electromagnetic radiation frequency of about 345.1 cm⁻¹, and an electromagnetic radiation frequency of about 382.2 cm⁻¹ may, for example, be selected to treat MoS₂ formed on or over the substrate 102 to reduce a crystalline defect density of the MoS₂. Vibrational spectra for crystalline-defect-free and crystalline-defect-laden forms of MoS₂ may be different (e.g., resonant peaks may shift to different vibrational frequencies) than those depicted in FIG. 2 depending, for example, on the properties (e.g., material composition) of a substrate (e.g., the substrate 102) upon which the MoS₂ is formed. Nonetheless, even if, for a given underlying substrate, vibrational spectra for crystalline-defect-free and crystalline-defect-laden forms of MoS₂ are different than shown in FIG. 2, mutual resonant frequencies where the resonant peak intensity of a crystalline-defect-laden form of MoS₂ is substantially different than the resonant peak intensity of a crystalline-defect-free form of MoS₂ identify frequencies of electromagnetic radiation that may be selected and used to treat MoS₂ formed over the given substrate to reduce the number of crystalline defects within the MoS₂.

A vibrational spectrum for the crystalline-defect-free form of the 2D material may be generated (e.g., calculated) using one or more conventional modeling processes, such as one or more conventional computational quantum mechanical modeling processes, which are not described in detail herein. The modeling process may include geometry optimization followed by vibrational analysis. By way of non-limiting example, the vibrational spectrum for the crystalline-defect-free form of the 2D material may be calculated using a conventional

density functional theory (DFT) analysis process. The unit structure of the 2D material may be defined and analyzed to determine how the crystalline-defect-free form of the 2D material would theoretically be formed on a given substrate. Computational analysis of the crystalline-defect-free form of the 2D material may then be performed to evaluate resonance thereof at
5 different vibrational frequencies and develop the vibrational spectrum thereof.

A vibrational spectrum for the crystalline-defect-laden form of the 2D material may be generated using one or more conventional vibrational spectroscopy processes, such as one or more of a conventional infrared spectroscopy process and a conventional Raman spectroscopy process, which are not described in detail herein. An actual (e.g., non-modeled, non-
10 theoretical) crystalline-defect-laden form of the 2D material may be subjected to a conventional vibrational spectroscopy process (e.g., a conventional 2D infrared spectroscopy process) to generate a vibrational spectrum for the crystalline-defect-laden form of the 2D material that may be compared against a vibrational spectrum generated for the crystalline-defect-free form of the 2D material through the conventional computational quantum
15 mechanical modeling process. Non-limiting examples, of suitable vibrational spectroscopy processes for generating a vibrational spectrum for the crystalline-defect-laden form of the 2D material include four-wave mixing processes and pump-probe processes (e.g., two-pulse photon echo processes, three-pulse photon echo processes, heterodyned four-wave mixing processes, homodyne four-wave mixing processes, dual-frequency heterodyned transient
20 grating processes, frequency resolved four-wave mixing processes, spectrally resolved four-wave mixing processes, pulse shaping four-wave mixing processes, narrow band four-wave mixing processes, broad band four-wave mixing processes, time-gated four-wave mixing processes), six-wave mixing process, and eight-wave mixing processes. In additional embodiments, a vibrational spectrum for the crystalline-defect-laden form of the
25 2D material may be generated using one or more conventional modeling processes, such as one or more conventional computational quantum mechanical modeling processes.

After selecting at least one frequency of electromagnetic radiation with which to expose the at least one 2D material, the 2D material may be treated with (e.g., exposed to, subjected to) the selected frequency of electromagnetic radiation using at least one laser
30 source configured and operated to generate one or more laser beams having the selected frequency of electromagnetic radiation. Any laser source capable of generating and directing at least one laser beam having the selected frequency of electromagnetic radiation toward the 2D material may be employed. By way of non-limiting example, the laser

source may comprise one or more of a gas laser (e.g., a carbon monoxide (CO) laser; a carbon dioxide (CO₂) laser), a semiconductor laser (e.g., a lead (Pb) salt semiconductor laser; a quantum cascade laser (QCL)), and a solid-state laser.

The selected frequency of electromagnetic radiation may be produced by a single
5 (e.g., only one) laser source, or may be produced by multiple (e.g., more than one) laser sources. If multiple laser sources are utilized, each of the multiple laser sources may be substantially the same as one another and may produce substantially the same selected frequency of electromagnetic radiation, or at least one of the multiple laser sources may be different than and/or produce a different selected frequency of electromagnetic radiation
10 than at least one other of the multiple laser sources. In some embodiments, a single selected frequency of electromagnetic radiation is produced by a single laser source. In additional embodiments, a single selected frequency of electromagnetic radiation is produced by multiple laser sources. In further embodiments, multiple selected frequencies of radiation are produced by multiple laser sources. If multiple laser sources are utilized,
15 the multiple laser sources may be used simultaneously, sequentially, or combinations thereof. For example, one or more of the multiple laser sources may be used to treat the 2D material(s) with a first selected frequency of electromagnetic radiation corresponding to a first resonant frequency where resonant peak intensities of crystalline-defect-free and crystalline-defect-laden forms of the 2D material(s) are different than (e.g., offset from)
20 one another, and then one or more other of the multiple laser sources may subsequently be used to treat the 2D material(s) with a second selected frequency of electromagnetic radiation corresponding to a second resonant frequency where resonant peak intensities of the crystalline-defect-free and the crystalline-defect-laden forms of the 2D material(s) are different than one another. As another example, one or more of the multiple laser sources
25 may be used to treat the 2D material(s) with a first selected frequency of electromagnetic radiation corresponding to a first resonant frequency where resonant peak intensities of crystalline-defect-free and crystalline-defect-laden forms of the 2D material(s) are different than one another, and one or more other of the multiple laser sources may be used simultaneously with the one or more of the multiple laser sources to treat the 2D
30 material(s) with a second selected frequency of electromagnetic radiation corresponding to a second resonant frequency where resonant peak intensities of the crystalline-defect-free and the crystalline-defect-laden forms of the 2D material(s) are different than one another.

The laser treatment process may employ any laser beam power(s) and duration(s) of exposure sufficient to reduce or even eliminate a crystalline defect density of the 2D material(s). The laser beam power(s) and duration(s) for a given application may at least partially depend on the electromagnetic radiation frequency (or frequencies) utilized, and the properties (e.g., material composition, dimensions) of the 2D material(s). An attenuator device may be used to control a laser beam power level (e.g., intensity) at a surface of the 2D material(s) to a level facilitating selective energization, mobilization, and at least partial (e.g., substantial) elimination of crystalline defects within the 2D material(s). A laser beam power level at a surface of the 2D material(s) may, for example, be within a range of from about 1 Watt (W) to about 1000 W. In addition, a modulator device (e.g., an acousto-optical modulator device) may be used to control a duration of laser beam exposure. A duration of laser beam exposure may, for example, be within a range of from about 1 millisecond (ms) to about 30 seconds (s).

The laser treatment process may expose the 2D material(s) to a single (e.g., only one) dose of one or more selected frequencies of electromagnetic radiation to form the 2D material structure 104 (FIG. 1), or may expose the 2D material(s) multiple doses of one or more selected frequencies of radiation to form the 2D material structure 104. If multiple doses are utilized, an initial dose may partially reduce a crystalline defect density of the 2D material(s), and at least one other dose may further reduce the crystalline defect density of the 2D material(s) to form the 2D material structure 104. Each of the multiple doses may be substantially the same (e.g., employ substantially the same selected radiation frequency, power, and duration), or at least one of the multiple doses may be different than at least one other of the multiple doses (e.g., employ a different selected radiation frequency, a different power, and/or a different duration).

Exposing a 2D material to a selected frequency (or frequencies) of electromagnetic radiation corresponding to a specific resonant frequency (or frequencies) along the multidirectional vibrational spectra of crystalline-defect-free and crystalline-defect-laden forms of the 2D material exhibiting differences between respective resonant peak intensities facilitates a high degree of selectivity as to what types of chemical bonds within the 2D material are modified using the radiation. When the 2D material is exposed to the selected frequency of electromagnetic radiation only chemical bonds that resonate at or near the selected frequency will be given the most energy by the radiation. The selected frequency of electromagnetic radiation may be absorbed by chemical bonds forming defective regions of

(e.g., crystalline defects within) a 2D material, without being substantially absorbed by chemical bonds forming non-defective regions of the 2D material, chemical bonds of the substrate 102 (FIG. 1), chemical bonds between the 2D material and the substrate 102, and chemical bonds between the 2D material and another 2D material (if any). Accordingly, the
5 selected frequencies of electromagnetic radiation may selectively modify only the chemical bonds forming crystalline defects of the 2D material. Electromagnetic radiation that is not absorbed by chemical bonds forming defective regions of the 2D material may be reflected or may be transmitted through the 2D material and the substrate 102. Consequently, using the selected frequencies of electromagnetic radiation to reduce the crystalline defect density of the
10 2D material may generate little to no heat in areas of the 2D material (and the substrate 102) not proximate the chemical bonds that the electromagnetic radiation has been selected to resonate with. By controlling the generation and use of heat in semiconductor device manufacturing, introduction of thermal defects can be avoided, and low-temperature manufacturing techniques can be more easily achieved.

15 Thus, in accordance with embodiments of the disclosure, a method of forming a semiconductor device structure comprises forming at least one 2D material over a substrate. The at least one 2D material is treated with at least one laser beam having a frequency of electromagnetic radiation corresponding to a resonant frequency of crystalline defects within the at least one 2D material to selectively energize and remove the crystalline defects from the
20 at least one 2D material.

In addition, in accordance with additional embodiments of the disclosure, another method of forming a semiconductor device structure comprises subjecting a 2D material on a substrate to a laser treatment process to reduce a crystalline defect density of the 2D material. The laser treatment process comprises exposing the 2D material to at least one frequency of
25 electromagnetic radiation substantially the same as at least one resonant frequency of crystalline-defect-free and crystalline-defect-laden forms of the 2D material where resonant peak intensities of the crystalline-defect-free and crystalline-defect-laden forms of the 2D material are different than one another.

In some embodiments, a thermal annealing process may be used in conjunction with
30 the laser treatment process to facilitate or enhance a reduction in the crystalline defect density of the 2D material(s) and form the 2D material structure 104 (FIG. 1). If employed, the thermal annealing process may heat the 2D material(s) to a temperature greater than a formation (e.g., growth, deposition) temperature of the 2D material(s) to raise a ground state

of all atoms and crystalline defects and increase a rate of diffusion of the crystalline defects out of the 2D material(s). By way of non-limiting example, if the 2D material (e.g., MoS₂) is formed at a temperature less than or equal to about 225°C, the thermal annealing process may subject the 2D material to a temperature greater than about 225°C (e.g., greater than or equal to about 250°C, greater than or equal to about 300°C, or greater than or equal to about 400°C) to raise a ground state of all atoms (e.g., Mo atoms, S atoms) and crystalline defects (e.g., S-interstitial defects, S-vacancy defects, Mo-interstitial defects, Mo-vacancy defects, MoS-vacancy defects, and SS-vacancy defects) and increase a rate of diffusion of the crystalline defects out of the 2D material(s) during the laser treatment process. Suitable thermal annealing processes include, but are not limited to, furnace annealing processes, chamber annealing processes, and laser annealing processes. In additional embodiments, the laser treatment process may be performed without performing a thermal annealing process in conjunction therewith. In further embodiments, one or more of a magnetic field, an electrical field, a bias, or a combination thereof, may be employed to increase the rate of diffusion of the crystalline defects out of the 2D material(s).

The 2D material(s) may be subjected to the laser treatment process and the thermal annealing process (if any) during the formation (e.g., deposition, growth) of the 2D material(s) on or over the substrate 102, after the formation of the 2D material(s) on or over the substrate 102, or a combination thereof. If a thermal annealing process is employed in conjunction with the laser treatment process, the laser treatment process and the thermal annealing process may be performed in any order, and with any amount of temporal overlap therebetween. The laser treatment process and the thermal annealing process (if any) may be performed simultaneously, sequentially, or a combination thereof. In addition, there is no limit to the number of times a laser treatment process and/or a thermal annealing process can be performed. Accordingly, one or more of the laser treatment process and the thermal annealing process (if any) may be performed multiple times, and not necessarily in the same order nor with the same temporal overlap for each repetition of the laser treatment process and the thermal annealing process (if any).

In some embodiments, at least one 2D material is subjected to the laser treatment process during the formation of the 2D material on or over the substrate 102 to selectively energize, mobilize, and at least partially (e.g., substantially) eliminate crystalline defects within the 2D material. In addition, the laser treatments process is performed without performing the thermal annealing process concurrent therewith. Put another way, the 2D

material is subjected to the laser treatment process without using a separate thermal annealing process (e.g., a furnace annealing process, a chamber annealing process, a laser annealing process) to heat the 2D material to a temperature greater than a formation temperature thereof. In such embodiments, heat supplied by the process (e.g., deposition process, growth process) used to form the 2D material may, by itself, be sufficient to raise a ground state of all atoms and crystalline defects of the 2D material and facilitate a desirable rate of diffusion of the crystalline defects out of the 2D material.

In additional embodiments, at least one 2D material is subjected to the laser treatment process and the thermal annealing process during the formation of the 2D material on or over the substrate 102 to selectively energize, mobilize, and at least partially (e.g., substantially) eliminate crystalline defects within the 2D material. The thermal annealing process (e.g., furnace annealing process, chamber annealing process, laser annealing process) is performed concurrent with the laser treatment process. The thermal annealing process is employed to heat the 2D material to a temperature greater than that which would otherwise be achieved by the heat supplied by the process (e.g., deposition process, growth process) used to form the 2D material alone. The separate thermal annealing process raises a ground state of all atoms and crystalline defects of the 2D material to increase rate of diffusion of the crystalline defects out of the 2D material during the formation thereof.

In yet additional embodiments, at least one 2D material is formed on or over the substrate 102, and is then subjected to the laser treatment process and the thermal annealing process to selectively energize, mobilize, and at least partially (e.g., substantially) eliminate crystalline defects within the 2D material. Put another way, the laser treatment process and the thermal annealing process are each performed only after the 2D material has already been formed (e.g., deposited, grown) on or over the substrate 102. The laser treatment process and the thermal annealing process may, for example, be used to repair the 2D material after an intervening period of time has passed subsequent to the formation of the 2D material. The 2D material may have cooled from a formation temperature thereof during the intervening period of time. The thermal annealing process (e.g., furnace annealing process, chamber annealing process, laser annealing process) is performed concurrent with the laser treatment process. The thermal annealing process raises a ground state of all atoms and crystalline defects of the 2D material to increase rate of diffusion of the crystalline defects out of the 2D material during the concurrent laser treatment process.

In further embodiments, at least one 2D material is formed on or over the substrate 102, the formed 2D material is then subjected to thermal annealing process, and then the thermally treated 2D material is subjected to the laser treatment process to selectively energize, mobilize, and at least partially (e.g., substantially) eliminate crystalline defects within the thermally treated 2D material. Put another way, the thermal annealing process is performed only after the 2D material has already been formed (e.g., deposited, grown) on or over the substrate 102, and the laser treatment process is performed after the thermal annealing process. The sequence of the thermal annealing process and the laser treatment process may, for example, be used to repair the 2D material after an intervening period of time has passed subsequent to the formation of the 2D material. The 2D material may have cooled from a formation temperature thereof during the intervening period of time. The thermal annealing process raises a ground state of all atoms and crystalline defects of the 2D material to increase rate of diffusion of the crystalline defects out of the 2D material during the subsequent laser treatment process.

In yet further embodiments, at least one 2D material is formed on or over the substrate 102, the formed 2D material is then subjected to the laser treatment process, and then the laser treated 2D material is subjected to the thermal annealing process. Put another way, the laser treatment process is performed only after the 2D material has already been formed (e.g., deposited, grown) on or over the substrate 102, and the thermal annealing process is performed after the laser treatment process. The sequence of the laser treatment process and the thermal annealing process may, for example, be used to repair the 2D material after an intervening period of time has passed subsequent to the formation of the 2D material. The 2D material may have cooled from a formation temperature thereof during the intervening period of time. In some embodiments, the thermal annealing process is effectuated after the start of but prior to the completion of the laser treatment process, and raises a ground state of all atoms and crystalline defects of the 2D material to increase rate of diffusion of the crystalline defects out of the 2D material during the remainder laser treatment process. In additional embodiments, the thermal annealing process is effectuated after the completion of the laser treatment process.

In addition, a remote plasma treatment process may be used in conjunction with at least the laser treatment process (and the thermal treatment process, if any) to promote the formation the 2D material structure 104 (FIG. 1). The remote plasma treatment process may, for example, be used to treat a surface of one or more 2D material(s) to promote the

formation of one or more additional 2D material(s) thereon or thereover. The remote plasma treatment process may promote nucleation of the additional 2D material(s) on or over the surface(s) of the 2D material(s). If employed, the remote plasma treatment process may be performed after the formation of a 2D material on or over the substrate 102. The remote plasma treatment process may, for example, be performed after at least the laser treatment process (and the thermal treatment process, if any) of the 2D material on or over the substrate 102.

With continued reference to FIG. 1, following the formation of the 2D material structure 104, the semiconductor device structure 100 including the 2D material structure 104 may be subjected to additional processing (e.g., material removal processes, additional deposition processes), as desired. The additional processing may be conducted using conventional processes and conventional processing equipment, and is not illustrated or described in detail herein.

Semiconductor device structures (e.g., the semiconductor device structure 100) formed in accordance with embodiments of the disclosure may be used in various semiconductor devices including, but not limited to, memory (e.g., random access memory (RAM), read only memory (ROM)), transistors (e.g., field-effect transistors (FETs), thin film transistors (TFTs), bipolar transistors), diodes, inverters, logic gates, junctions, photodetectors, photovoltaic cells, light-emitting diodes (LEDs), electronic sensors, integrated circuits, and microprocessors. By way of non-limiting example, semiconductor device structures formed in accordance with embodiments of the disclosure may be used in various FETs including, but not limited to, tunnel field-effect transistors (TFETs) (e.g., single gate TFETs, double gate TFETs, lateral TFETs, vertical TFETs, synthetic electric field TFETs (SE-TFETs)), and vertical FETs (VFETs). The 2D material structures (e.g., 2D material structure 104) of the semiconductor device structures (e.g., the semiconductor device structure 100) formed in accordance with embodiments of the disclosure may, for example, be employed as channels of the FETs.

In addition, semiconductor devices (e.g., memory, transistors, diodes, inverters, logic gates, junctions, photodetectors, photovoltaic cells, LEDs, electronic sensors, integrated circuits, microprocessors) formed in accordance with embodiments of the disclosure may be included in various electronic systems. By way of non-limiting example, FIG. 3 is a block diagram of an electronic system 300 according to embodiments of disclosure. The electronic system 300 may comprise, for example, a computer or computer hardware component, a

server or other networking hardware component, a cellular telephone, a digital camera, a personal digital assistant (PDA), a portable media (e.g., music) player, a WiFi or cellular-enabled tablet such as, for example, an iPad® or SURFACE® tablet, an electronic book, a navigation device, etc. The electronic system 300 includes at least one memory device 302.

5 The at least one memory device 302 may include, for example, an embodiment of the semiconductor device structure 100 shown in FIG. 1. The electronic system 300 may further include at least one electronic signal processor device 304 (e.g., microprocessor). The electronic signal processor device 304 may, optionally, include a semiconductor device structure similar to an embodiment of the semiconductor device structure 100 shown in
10 FIG. 1. The electronic system 300 may further include one or more input devices 306 for inputting information into the electronic system 300 by a user, such as, for example, a mouse or other pointing device, a keyboard, a touchpad, a button, or a control panel. The electronic system 300 may further include one or more output devices 308 for outputting information (e.g., visual or audio output) to a user such as, for example, a monitor, a display, a printer, an
15 audio output jack, a speaker, etc. In some embodiments, the input device 306 and the output device 308 may comprise a single touchscreen device that can be used both to input information to the electronic system 300 and to output visual information to a user. The one or more input devices 306 and output devices 308 may communicate electrically with at least one of the memory device 302 and the electronic signal processor device 304.

20 Thus, a semiconductor device according to embodiments of the disclosure comprises at least one semiconductor device structure comprising a 2D material structure overlying a substrate and comprising a 2D material substantially free of interstitial defects and vacancy defects.

Furthermore, an electronic system according to embodiments of the disclosure
25 comprises at least one semiconductor device and peripheral circuitry electrically connected to the at least one semiconductor device. The at least one semiconductor device includes at least one semiconductor device structure comprising a substrate, and a 2D material structure over the substrate and comprising one or more 2D materials substantially free of crystalline defects

The methods of the disclosure may effectively reduce crystalline defects (e.g.,
30 interstitial defects, vacancy defects) in one or more 2D material(s) to facilitate the formation of semiconductor device structures (e.g., the semiconductor device structure 100) having improved electrical properties as compared to conventional semiconductor device structures not formed in accordance with embodiments of the disclosure. The laser

treatment processes of the disclosure may reduce crystalline defect densities of the 2D material(s) while avoiding undesirable changes (e.g., undesirable structural deformations, undesirable material diffusion) to the 2D material(s) and/or other structures (e.g., the substrate 100) operatively associated therewith (e.g., bound thereto). Semiconductor device structures (e.g., the semiconductor device structure 100) according to embodiments of disclosure may, in turn, improve one or more properties of devices into which they are at least partially incorporated. Semiconductor devices and electronic systems including semiconductor device structures formed using the methods of the disclosure may have enhanced performance, reliability, and durability as compared to many conventional semiconductor devices and electronic systems.

While the disclosure is susceptible to various modifications and alternative forms, specific embodiments have been shown by way of example in the drawings and have been described in detail herein. However, the disclosure is not limited to the particular forms disclosed. Rather, the disclosure is to cover all modifications, equivalents, and alternatives falling within the scope of the following appended claims and their legal equivalents.

CLAIMS

What is claimed is :

1. A semiconductor device structure, comprising a 2D material structure comprising at least one 2D material substantially free of one or more of X-interstitial defects, X-vacancy defects, M-interstitial defects, M-vacancy defects, MX-vacancy defects, and XX-vacancy defects.
2. The semiconductor device structure of claim 1, wherein the at least one 2D material comprises at least one transition metal di-chalcogenide.
3. The semiconductor device structure of claim 1, wherein the at least one 2D material comprises a stack of at least two different 2D materials.
4. The semiconductor device structure of claim 3, wherein each of the at least two different 2D materials is substantially free of crystalline defects.
5. A semiconductor device, comprising at least one semiconductor device structure comprising a 2D material structure comprising a 2D material substantially free of interstitial defects and vacancy defects.
6. An electronic system, comprising:
at least one semiconductor device including at least one semiconductor device structure comprising a 2D material structure comprising one or more 2D materials substantially free of crystalline defects; and
peripheral circuitry electrically connected to the at least one semiconductor device.
7. The semiconductor device structure of claim 1, wherein the 2D material comprises one or more of graphene, graphene-oxide, stanene, phosphorene, hexagonal boron nitride, borophene, silicene, graphyne, germanene, germanane, and a 2D supracrystal.

8. The semiconductor device structure of claim 1, wherein the at least one 2D material comprises at least one transition metal dichalcogenide having the general chemical formula:



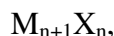
where M is Mo, W, Nb, Zr, Hf, Re, Pt, Ti, Ta, V, Co, Cd, or Cr; and

X is S, Se, or Te.

9. The semiconductor device structure of claim 1, wherein the at least one 2D material comprises one or more monolayers of one or more of MoS₂, MoSe₂, MoTe₂, WS₂, WSe₂, WTe₂, NbSe₂, ZrS₂, ZrSe₂, HfS₂, HfSe₂, and ReSe₂.

10. The semiconductor device structure of claim 1, wherein the at least one 2D material comprises a crystalline-defect-free form of MoS₂.

11. The semiconductor device structure of claim 1, wherein the at least one 2D material comprises a substantially crystalline-defect-free form of one or more of a carbide and a carbonitride having the general chemical formula:



where M is a transition metal from Groups IV or V of the Periodic Table of Elements; and

X is one or more of C and N.

12. The semiconductor device structure of claim 1, wherein the at least one 2D material comprises a monolayer of a metal material, a semi-metal material, or a semiconductive material.

13. The semiconductor device structure of claim 1, wherein the at least one 2D material comprises multiple 2D materials coupled to one another through one or more intermolecular forces.

14. The semiconductor device structure of claim 13, wherein at least some of the multiple 2D materials are coupled to one another through Van der Waals forces.

15. The semiconductor device structure of claim 1, wherein the at least one 2D material is substantially free of X-interstitial and X-vacancy defects.

16. The semiconductor device structure of claim 1, wherein the at least one 2D material is substantially free of all crystalline defects.

17. The semiconductor device of claim 5, wherein the 2D material is selected from graphene, graphene-oxide, stanene, phosphorene, hexagonal boron nitride, borophene, silicene, graphyne, germanene, germanane, a 2D supracrystal, a transition metal dichalcogenide, a carbide of a transition metal from Groups IV or V of the Periodic Table of Elements, and a carbonitride of a transition metal from Groups IV or V of the Periodic Table of Elements.

18. The semiconductor device of claim 5, wherein the 2D material structure further comprises another 2D material coupled to the 2D material through intermolecular forces, the another 2D material substantially free of interstitial defects and vacancy defects.

19. The semiconductor device of claim 18, wherein the 2D material comprises a first transition metal dichalcogenide monolayer, and the another 2D material comprises a second transition metal dichalcogenide monolayer having a different material composition than the first transition metal dichalcogenide monolayer.

20. The semiconductor device of claim 17, wherein the 2D material structure comprises a channel of a transistor.