

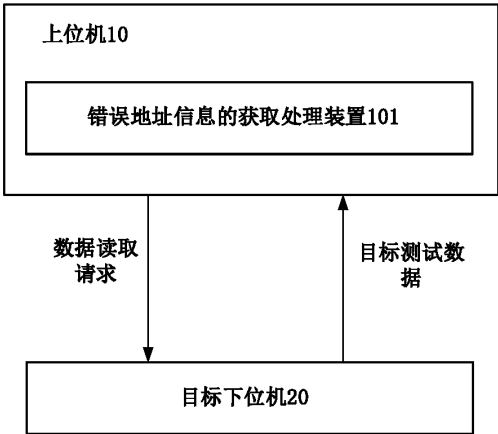


图 1

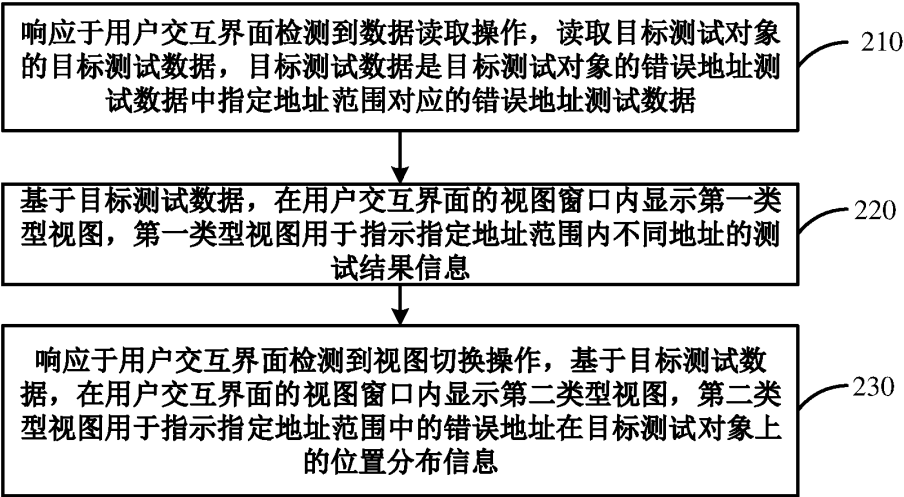


图 2

State

Compress

PASS

FAIL

Physical

DUT (4)

201

202

203

204

DUT

201

Pin

DQL0

DQL1

DQL2

DQL2

DQL4

DQL5

DQL6

DQL7

DQU0

DQU1

DQU2

DQU3

DQU4

DQU5

DQU6

DQU7

图 3

State

Compress

PASS

FAIL

Physical

DUT (4)

201

202

203

204

DUT

201

Pin

DQL0

DQL4

DQU0

DQU4

DQL1

DQL5

DQU1

DQU5

DQL2

DQL6

DQU2

DQU6

DQL2

DQL7

DQU3

DQU7

图 4

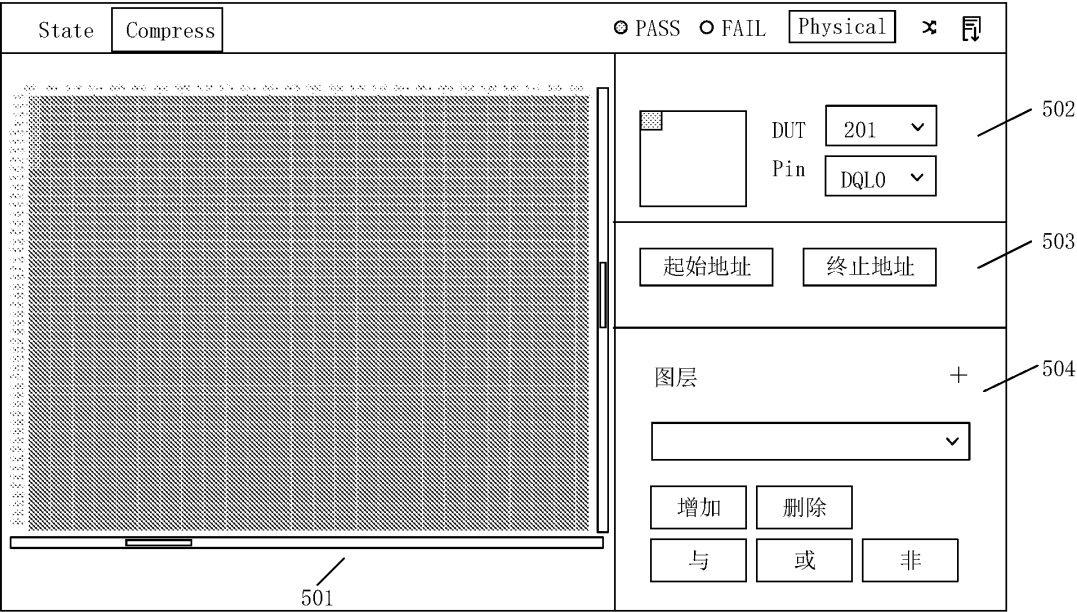


图 5

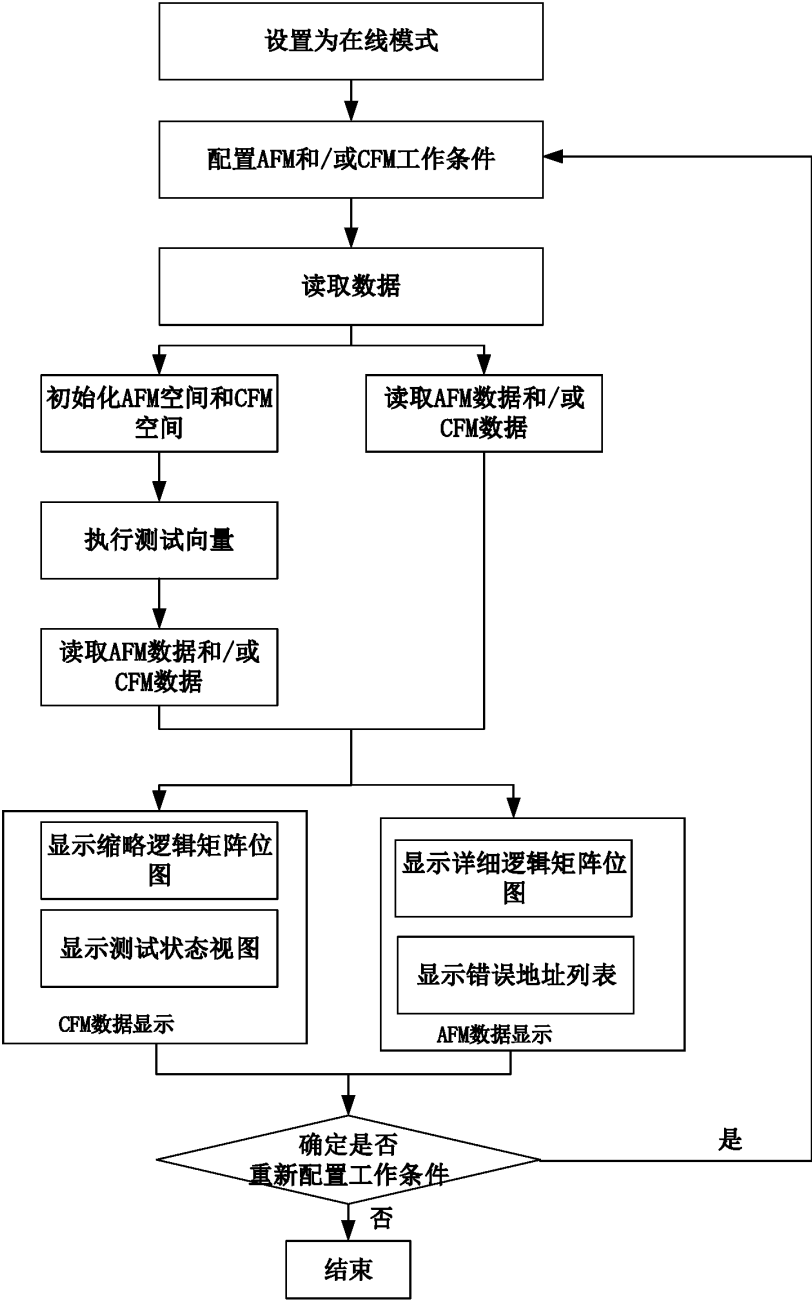


图 6

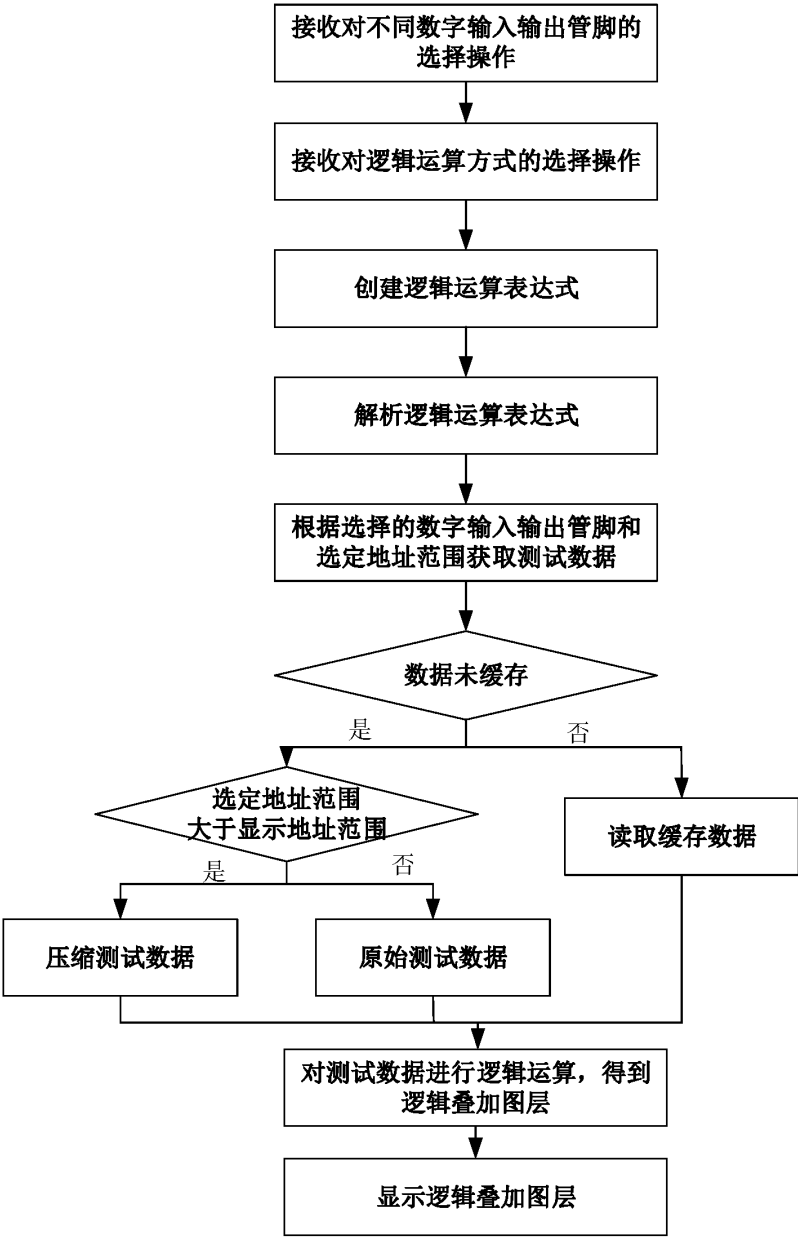


图 7

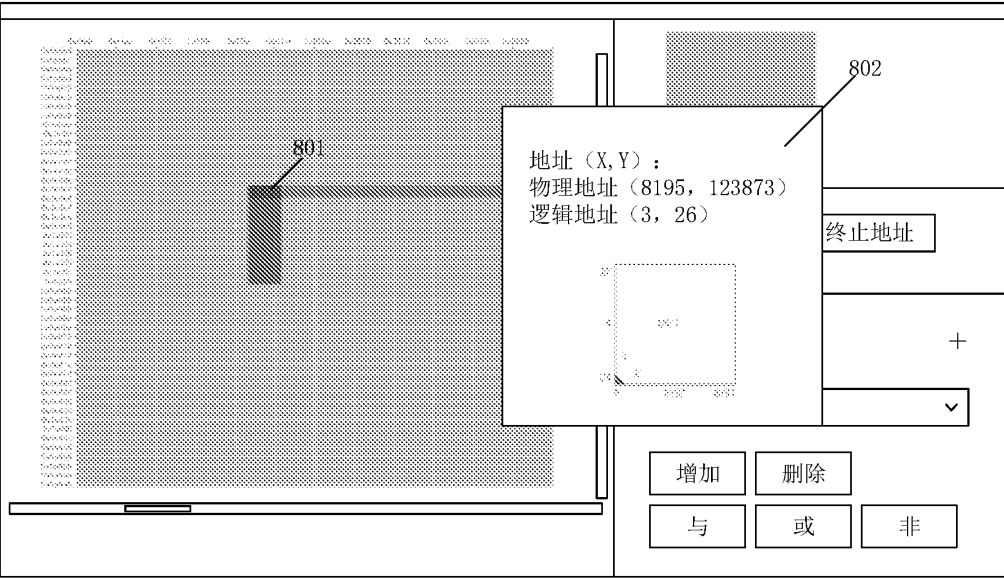


图 8

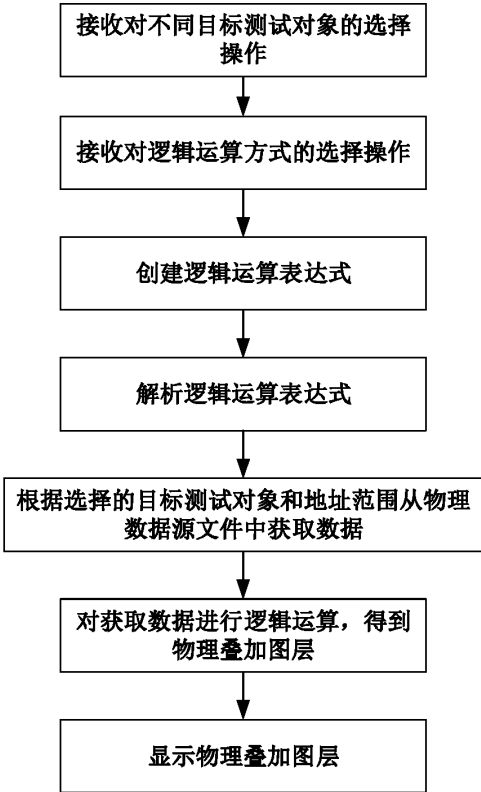


图 9

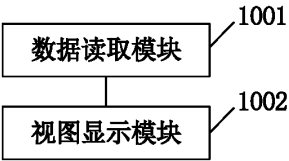


图 10

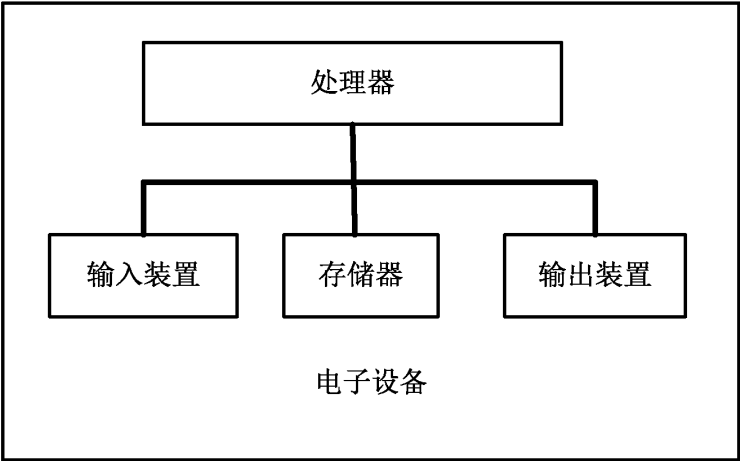


图 11