

FIG. 1

10

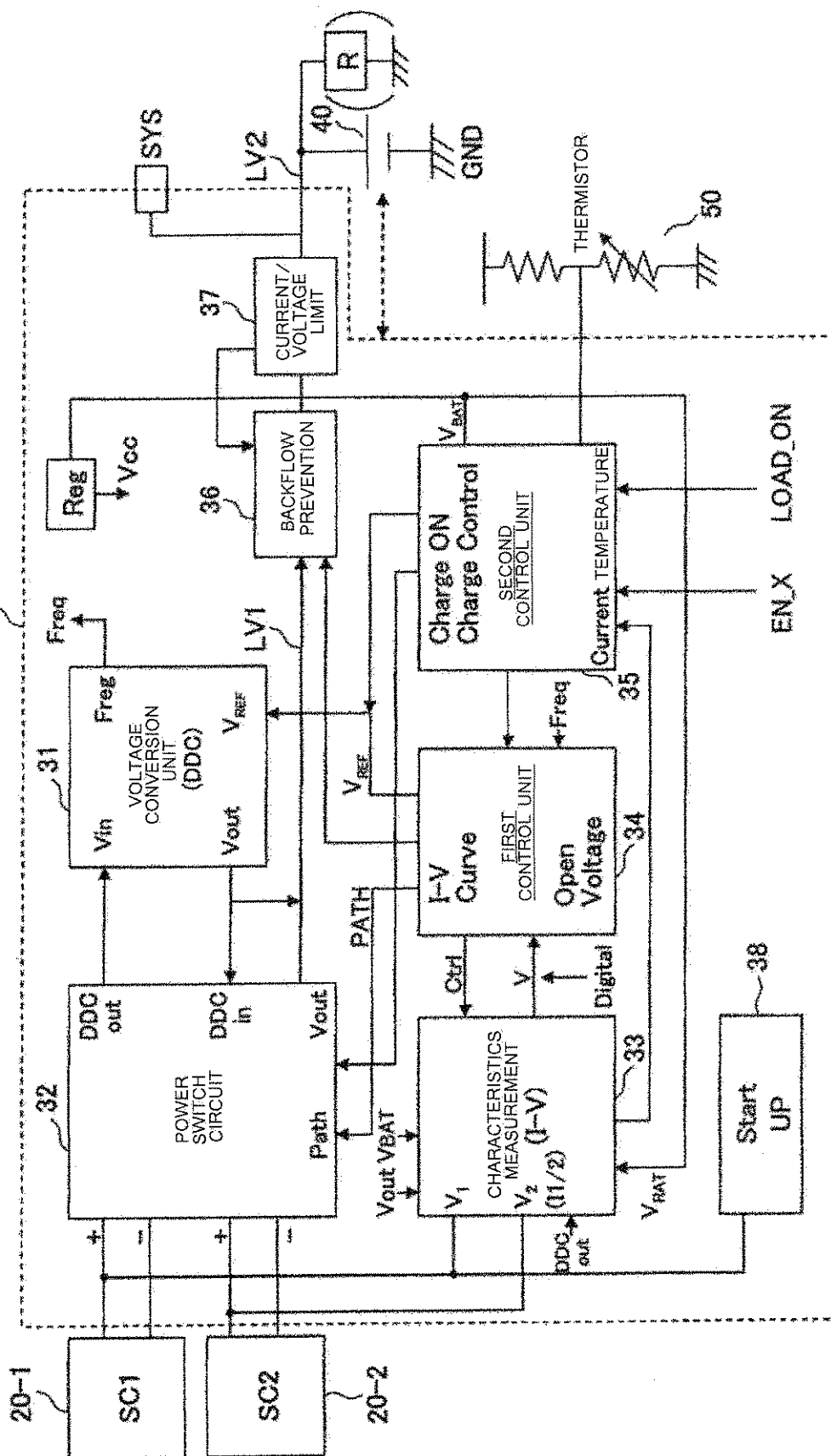


FIG. 2

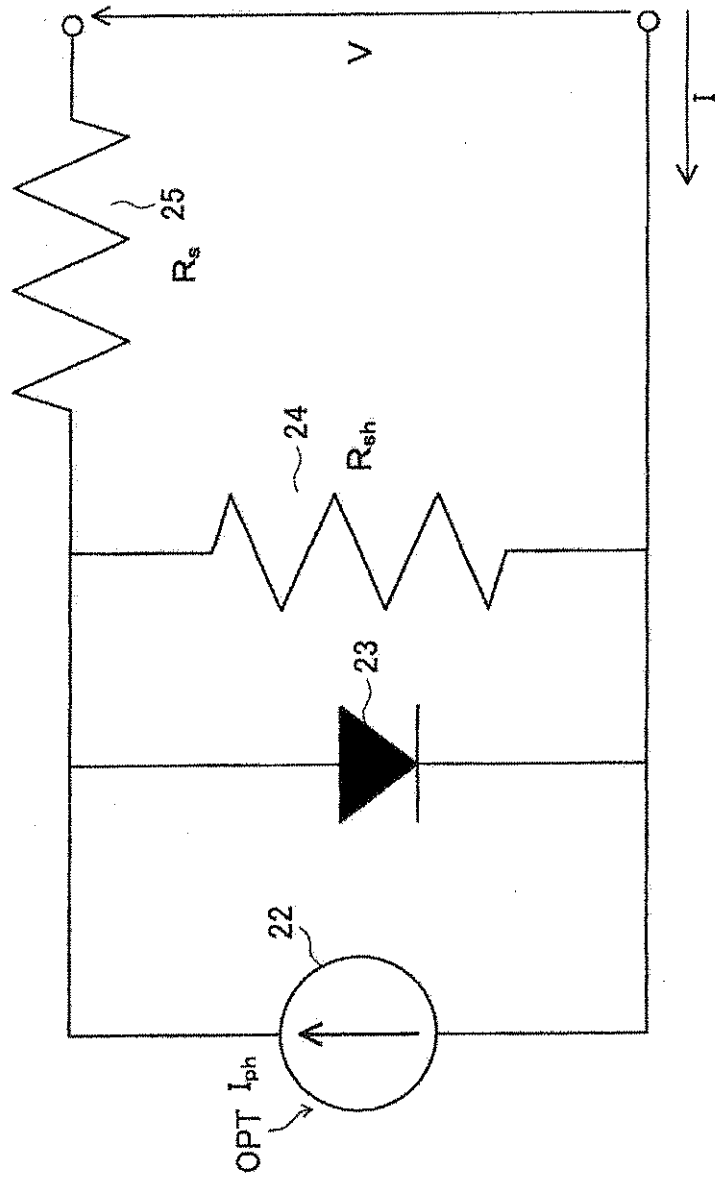
21

FIG. 3

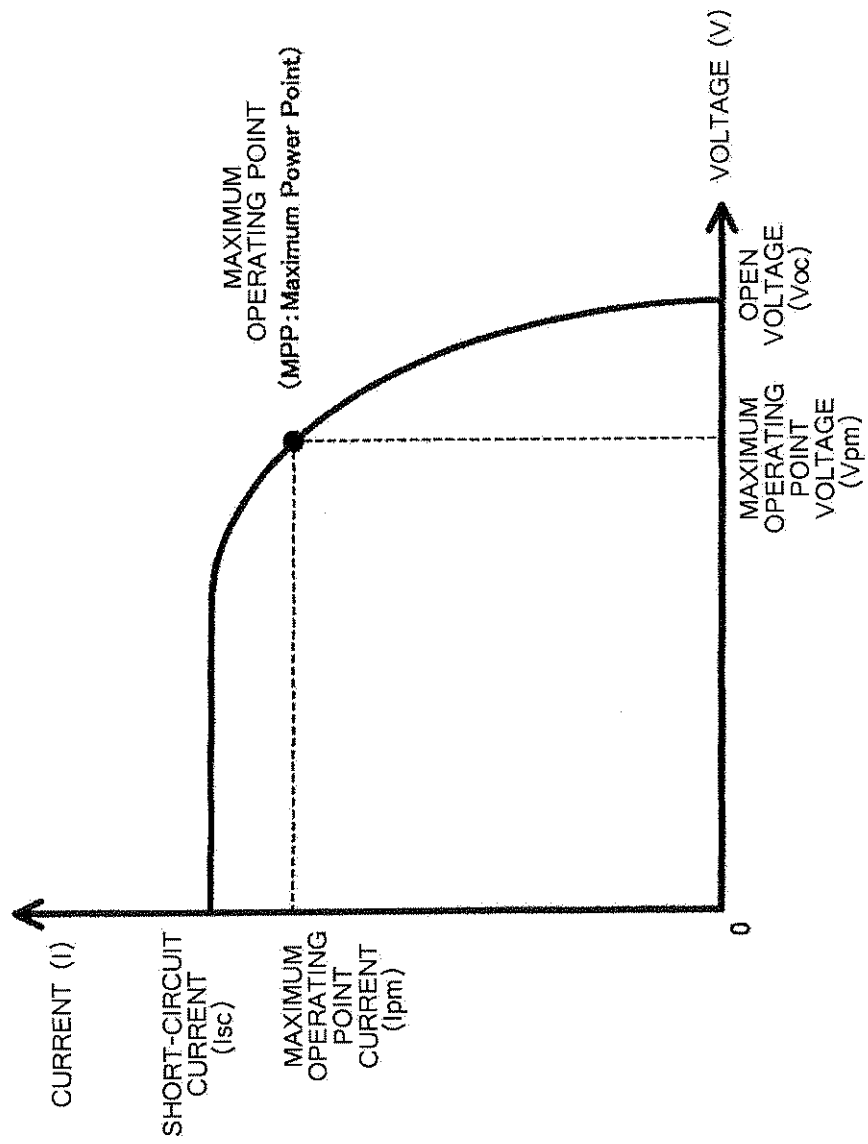


FIG. 4

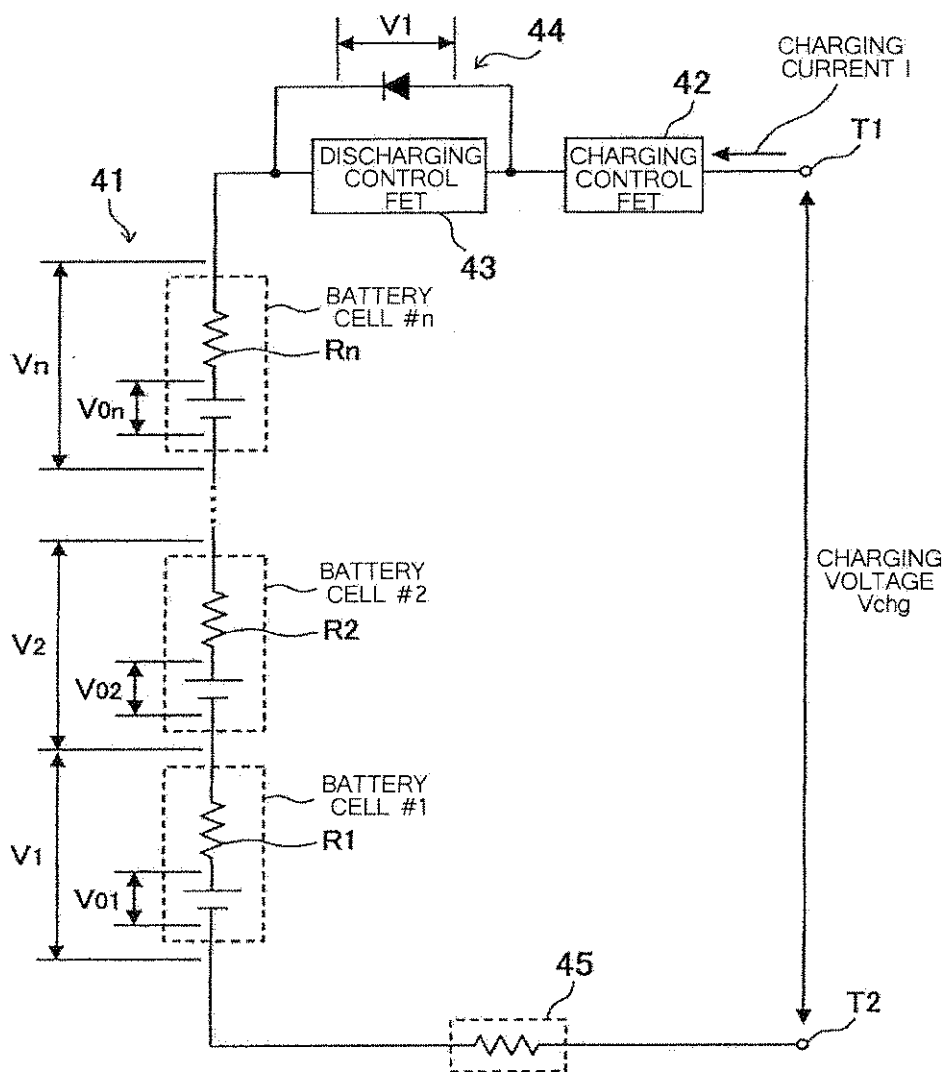


FIG. 5

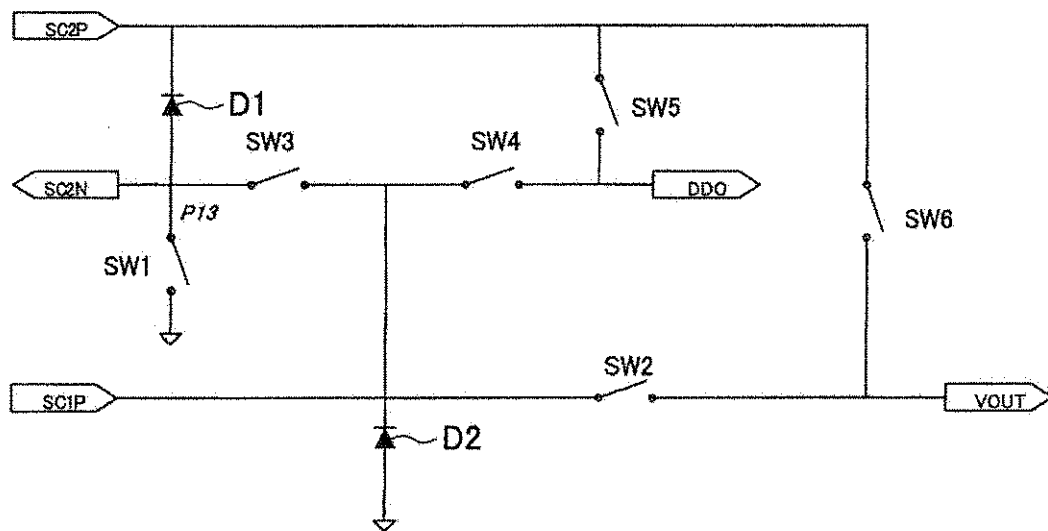
32

FIG. 6

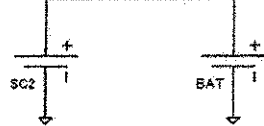
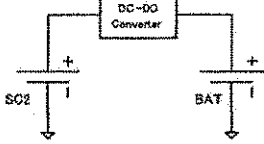
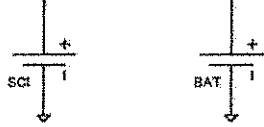
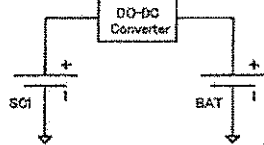
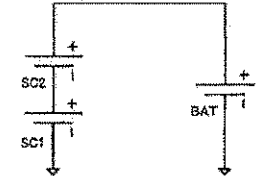
PATH NAME	DESCRIPTION	FIGURE
PTA	CONNECT SOLAR BATTERY 2 AND SECONDARY BATTERY DIRECTLY.	
PTB	SUPPLY OUTPUT FROM SOLAR BATTERY 2 VIA STEP-UP CIRCUIT TO SECONDARY BATTERY.	
PTC	CONNECT SOLAR BATTERY 1 AND SECONDARY BATTERY DIRECTLY.	
PTD	SUPPLY OUTPUT FROM SOLAR BATTERY 1 VIA STEP-UP CIRCUIT TO SECONDARY BATTERY.	
PTE	CONNECT SOLAR BATTERY 1 AND SOLAR BATTERY 2 IN SERIES, AND CONNECT SECONDARY BATTERY THERETO	

FIG. 7

PTF	SUPPLY OUTPUT FROM SERIES CONNECTION OF SOLAR BATTERY 1 AND SOLAR BATTERY 2 TO SECONDARY BATTERY VIA STEP-UP CIRCUIT.	
PTG	CONNECT SOLAR BATTERY 1 AND SOLAR BATTERY 2 IN PARALLEL, AND CONNECT SECONDARY BATTERY THERETO DIRECTLY.	
PTH	SUPPLY OUTPUT FROM PARALLEL CONNECTION OF SOLAR BATTERY 1 AND SOLAR BATTERY 2 TO SECONDARY BATTERY VIA STEP-UP CIRCUIT.	
PTI	PASS OUTPUT FROM SOLAR BATTERY 1 THROUGH STEP-UP CIRCUIT, CONNECT OUTPUT FROM SOLAR BATTERY 2 IN PARALLEL, AND SUPPLY THEM TO SECONDARY BATTERY.	
PTJ	PASS OUTPUT FROM SOLAR BATTERY 2 THROUGH STEP-UP CIRCUIT, CONNECT OUTPUT FROM SOLAR BATTERY 1 IN PARALLEL, AND SUPPLY THEM TO SECONDARY BATTERY.	
PTK	OPEN ALL OF SOLAR BATTERY 1, SOLAR BATTERY 2, AND SECONDARY BATTERY. GROUND MINUS SIDE OF SOLAR BATTERY 2 (PATH FOR OPEN VOLTAGE MEASUREMENT OF SOLAR BATTERY).	
PTL	OPEN ALL OF SOLAR BATTERY 1, SOLAR BATTERY 2, AND SECONDARY BATTERY (ALL SWITCHES IN OFF-STATE). LEAVE MINUS SIDE OF SOLAR BATTERY 2 IN OPEN STATE.	

FIG. 8

	SW1	SW2	SW3	SW4	SW5	SW6
A	H	L	L	L	L	H
B	H	L	L	L	H	L
C	L	H	L	L	L	L
D	L	L	L	H	L	L
E	L	L	H	L	L	H
F	L	L	H	L	H	L
G	H	H	L	L	L	H
H	H	L	L	H	H	L
I	H	L	L	H	L	H
J	H	H	L	L	H	L
K	H	L	L	L	L	L
L	L	L	L	L	L	L

FIG. 10

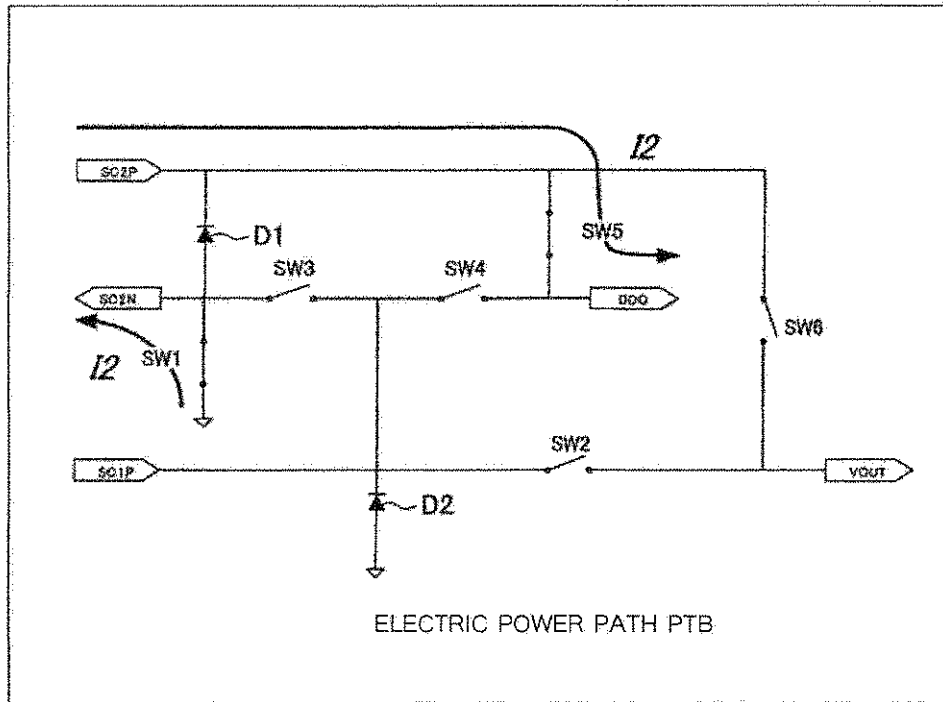


FIG. 11

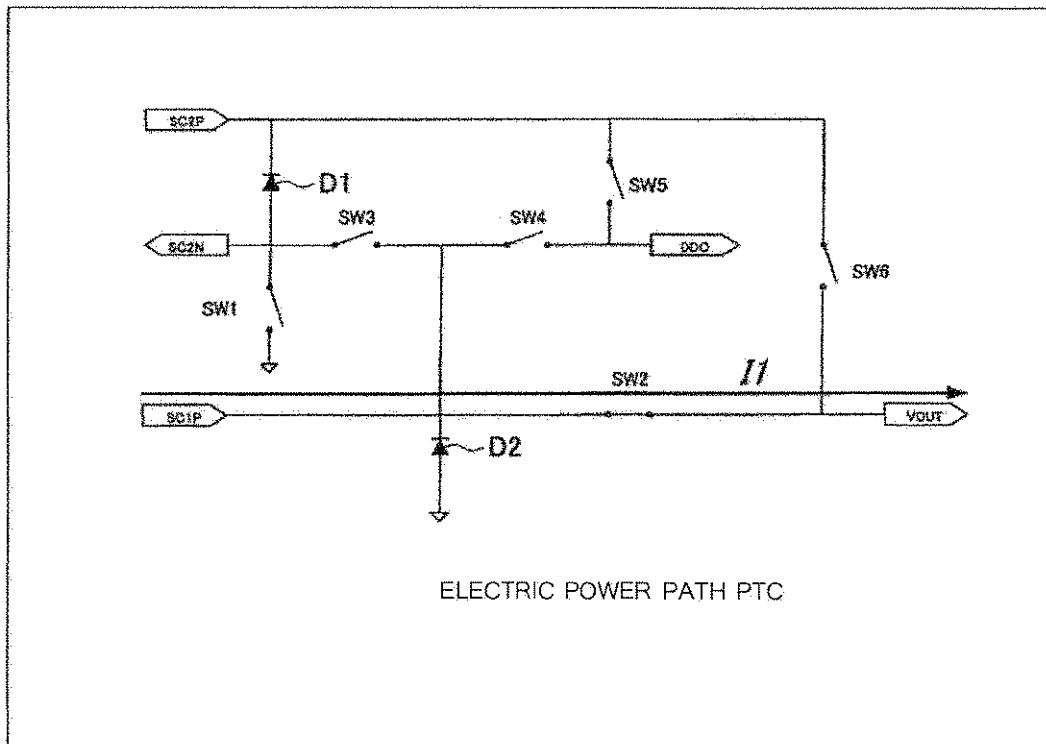


FIG. 12

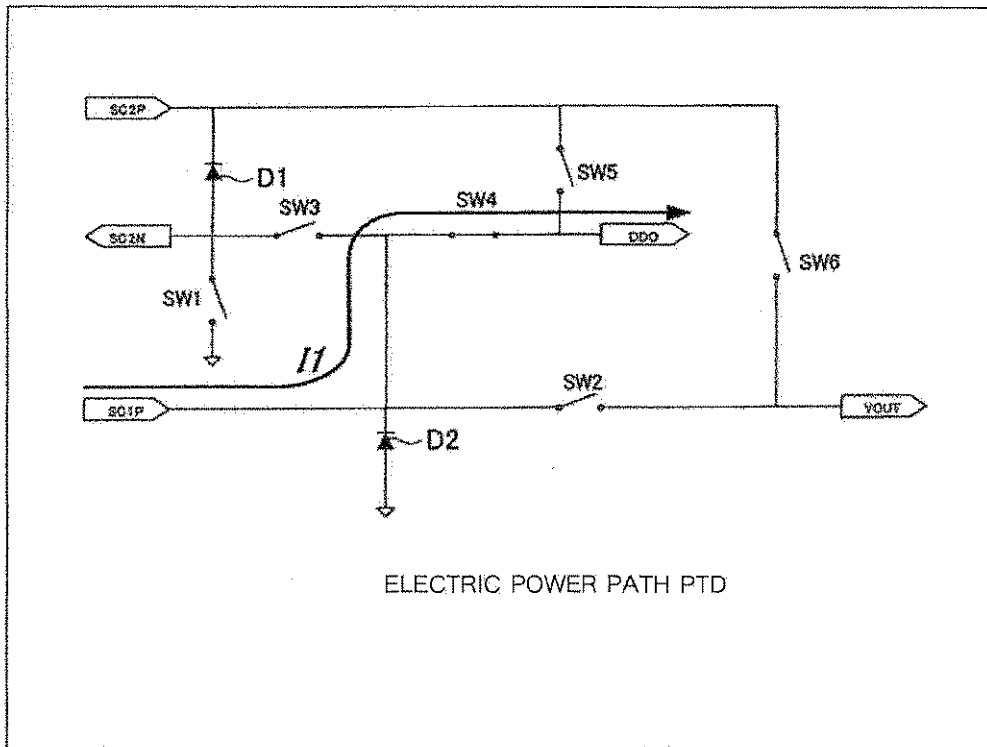


FIG. 13

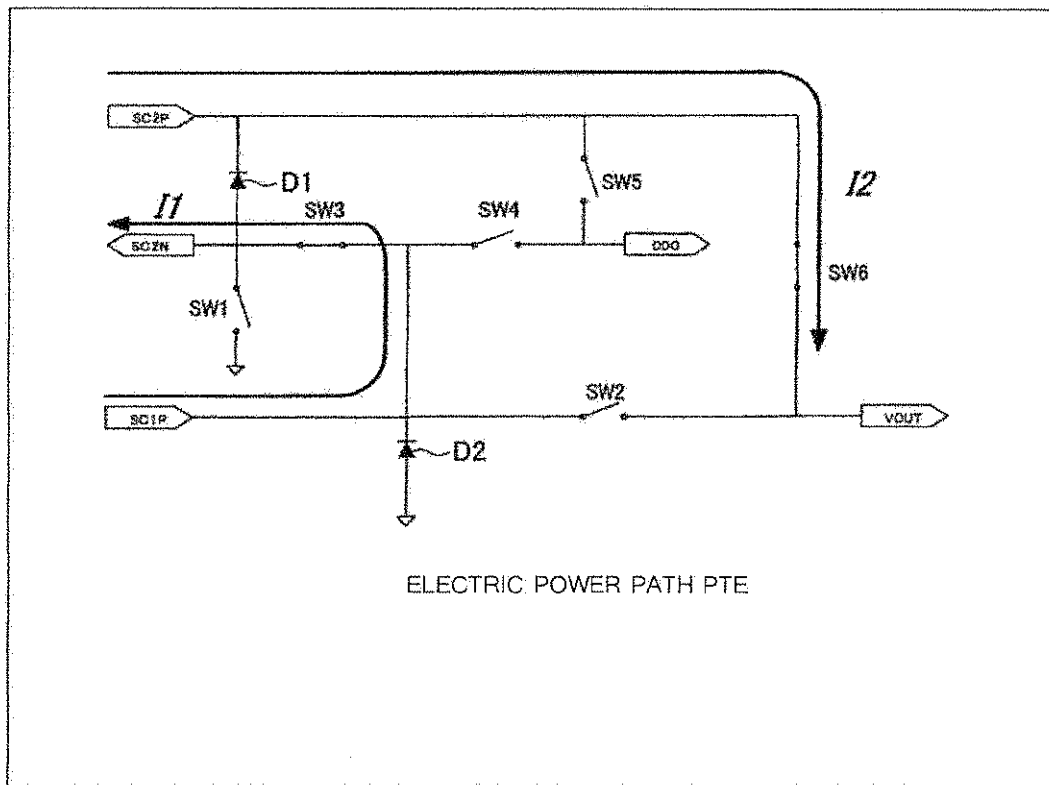


FIG. 14

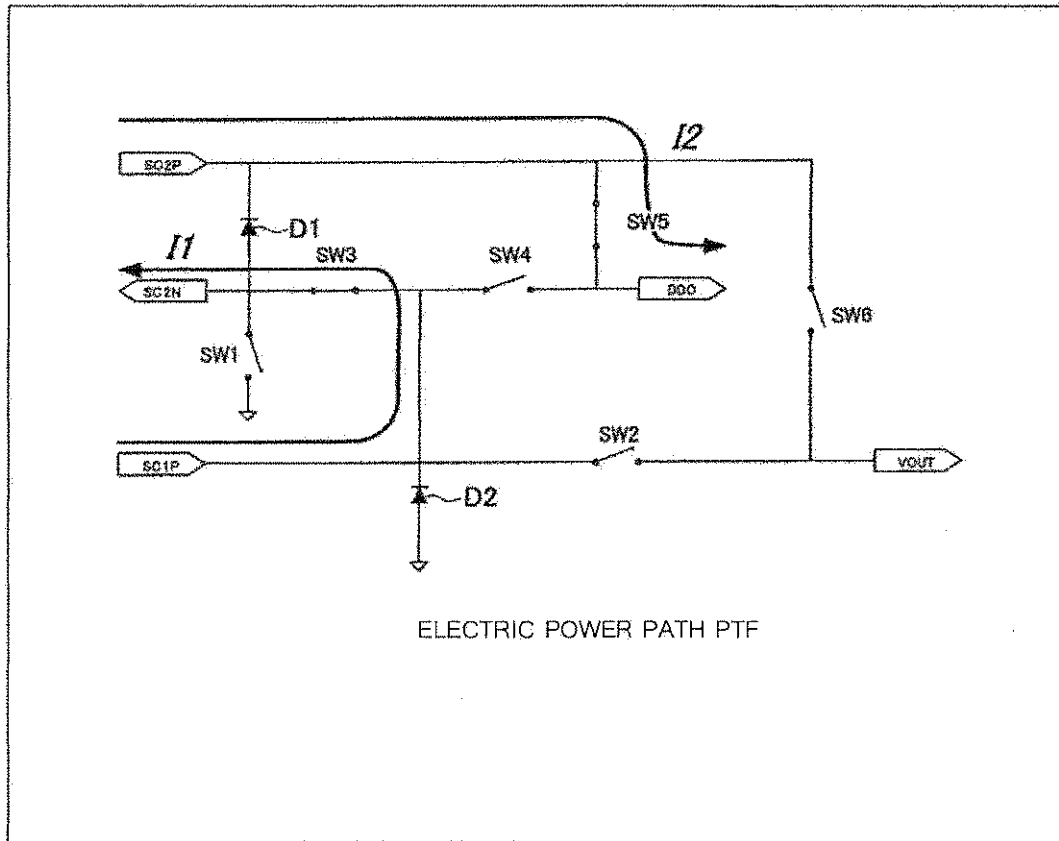


FIG. 15

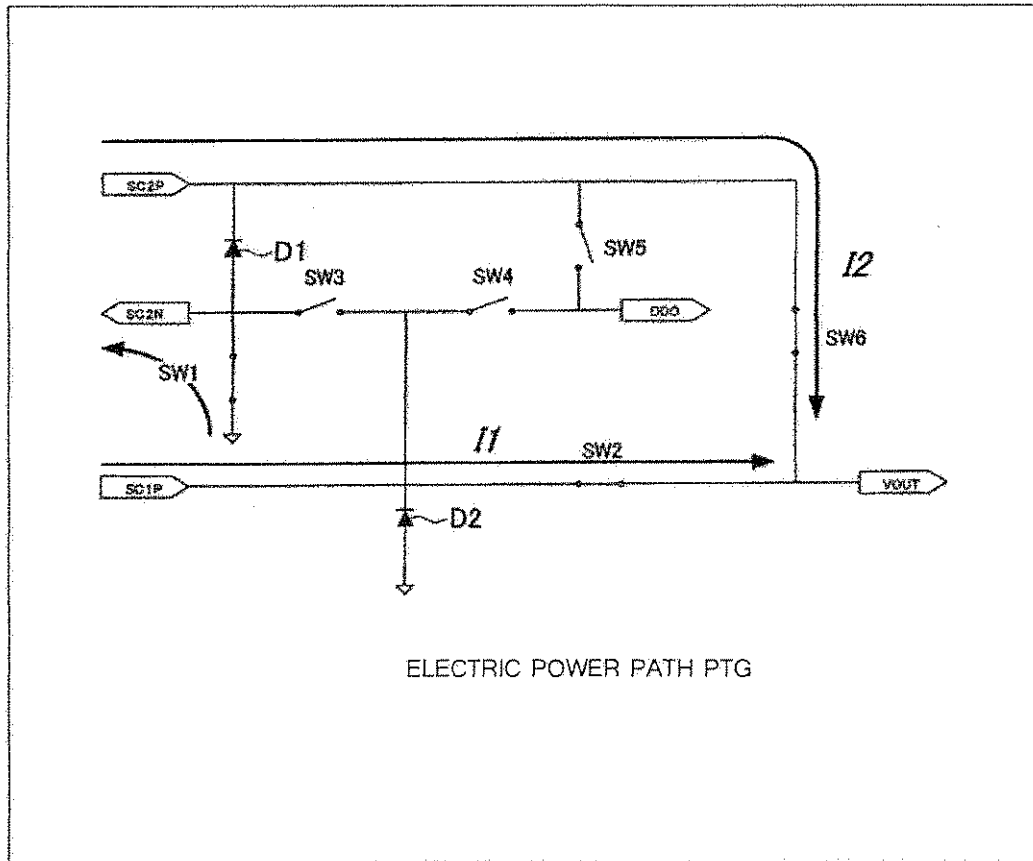


FIG. 16

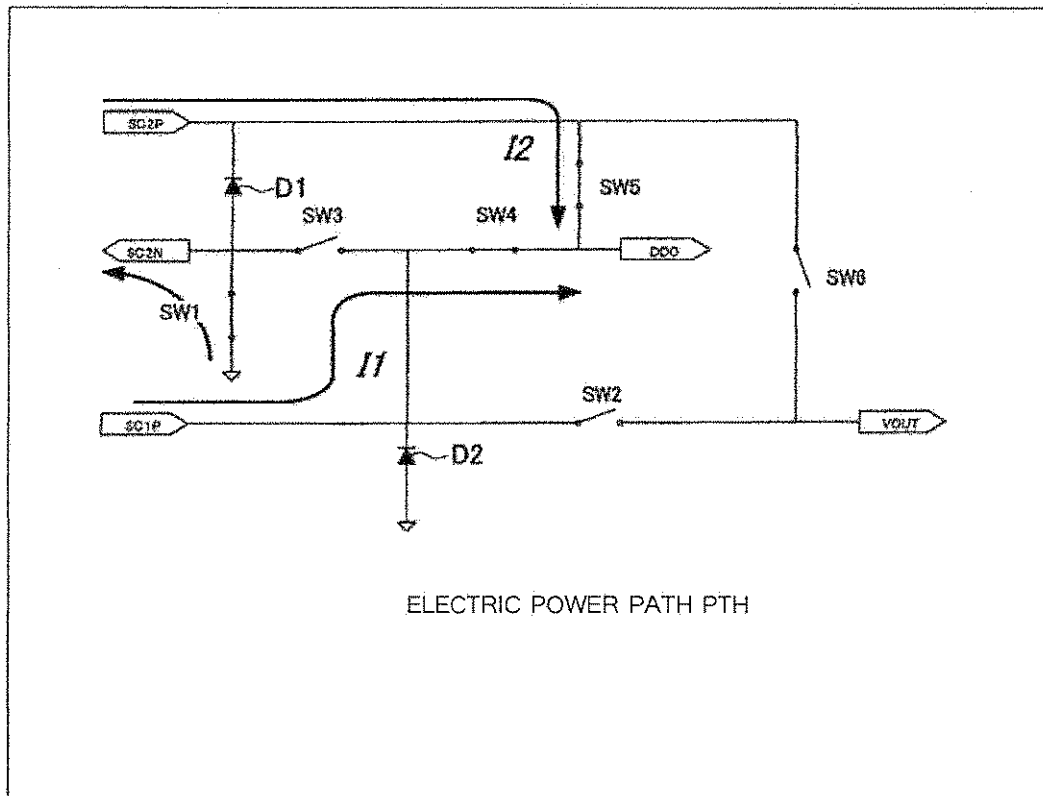


FIG. 17

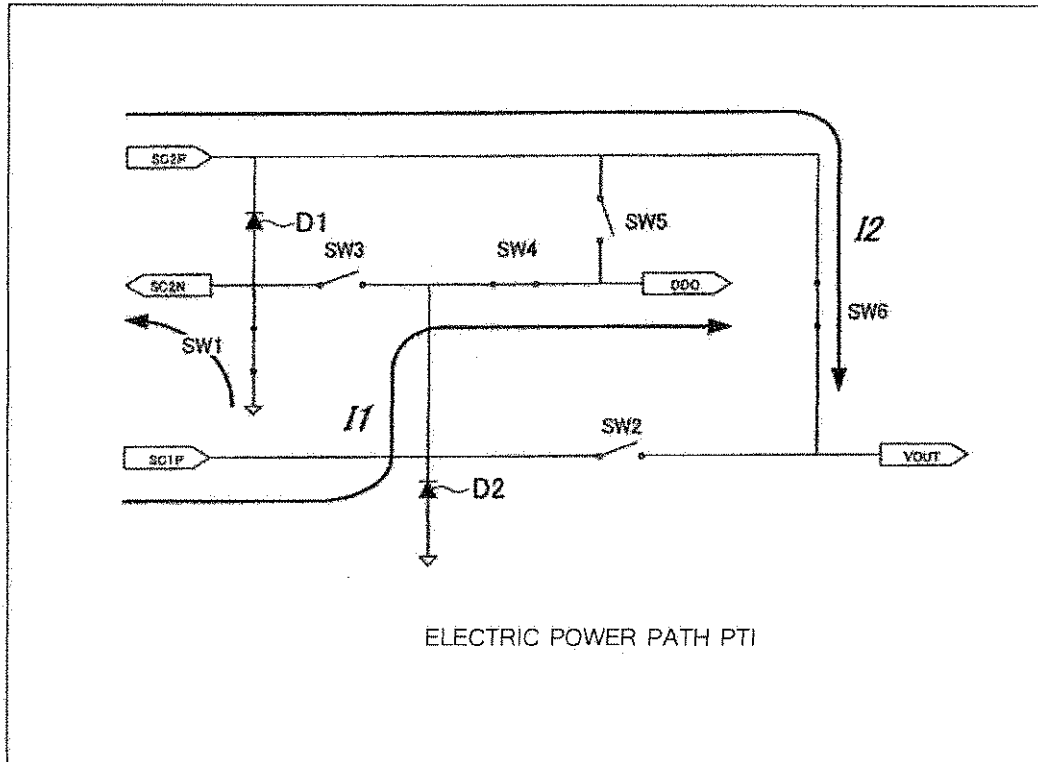


FIG. 18

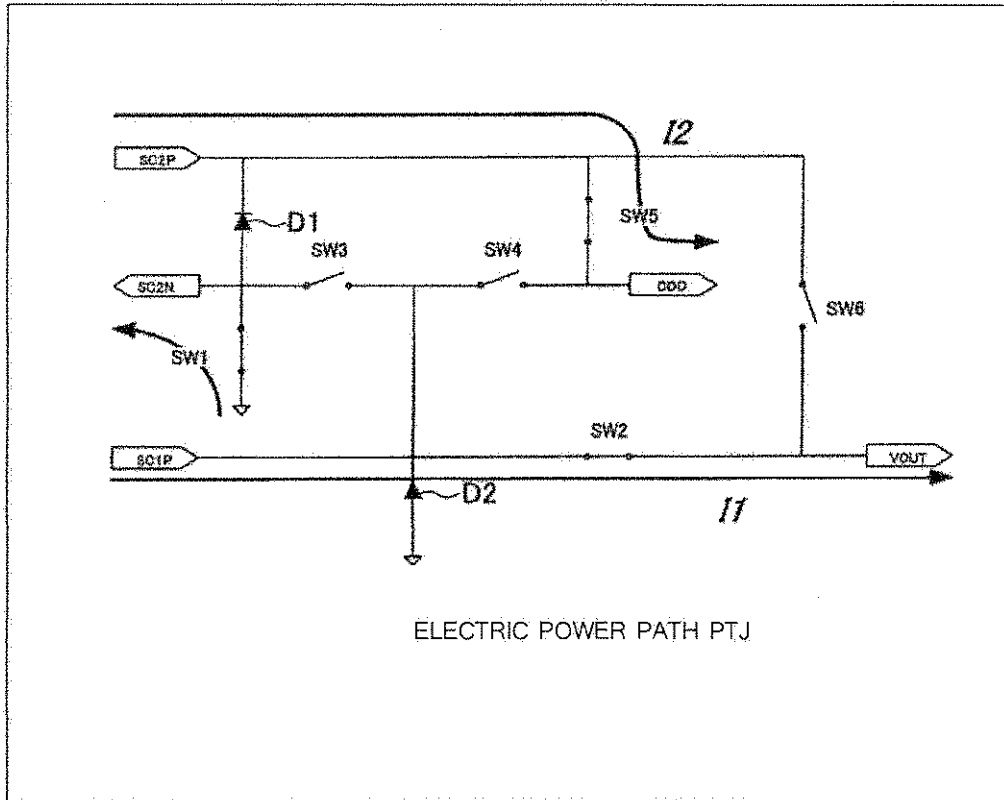


FIG. 19

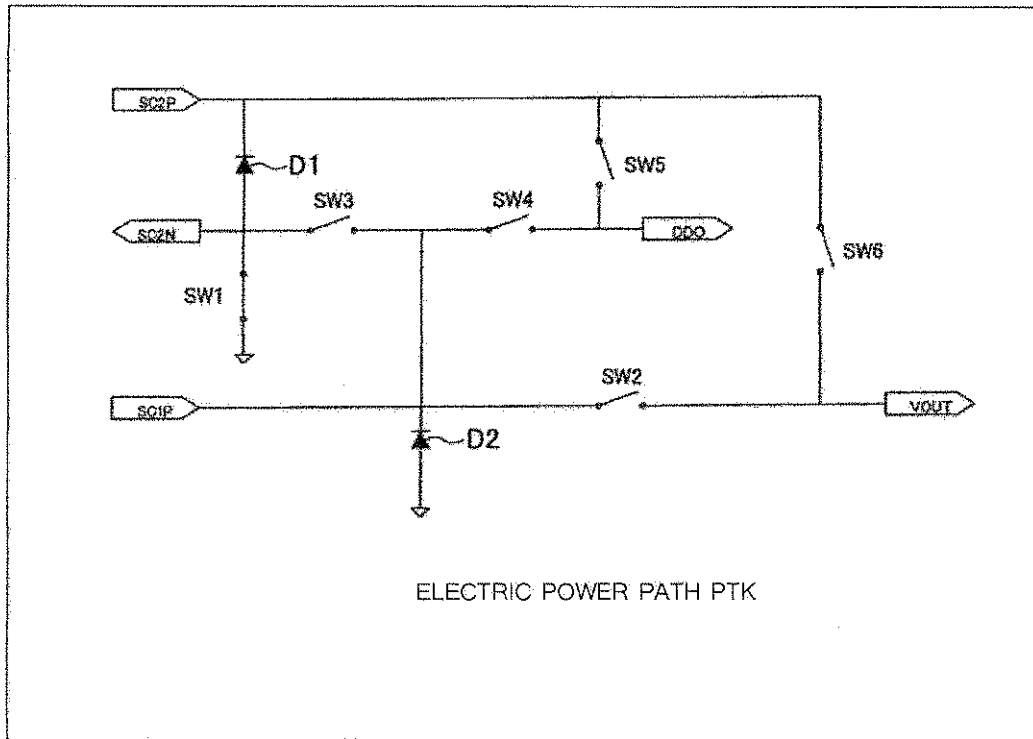


FIG. 20

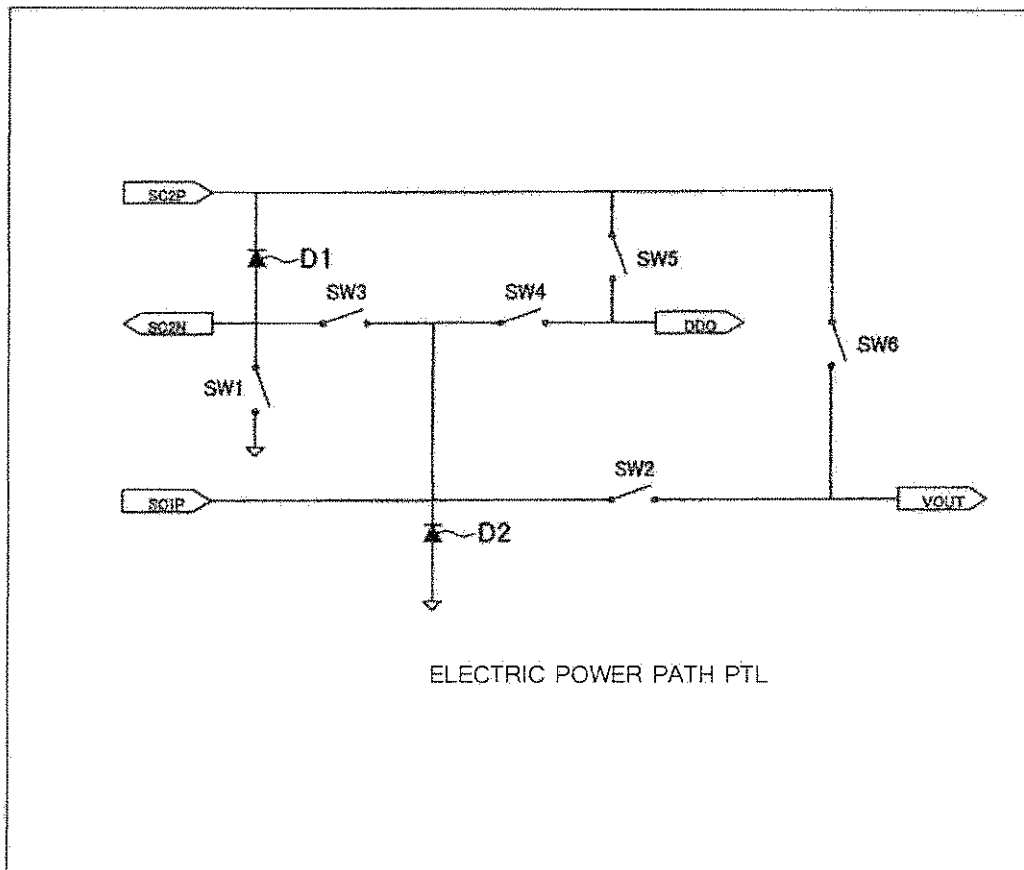


FIG. 21

PATH NAME	DESCRIPTION	FIGURE
PTE	CONNECT SOLAR BATTERY 1 AND SOLAR BATTERY 2 IN SERIES, AND CONNECT SECONDARY BATTERY THERETO DIRECTLY.	
PTF	SUPPLY OUTPUT FROM SERIES CONNECTION OF SOLAR BATTERY 1 AND SOLAR BATTERY 2 TO SECONDARY BATTERY VIA STEP-UP CIRCUIT.	
PTG	CONNECT SOLAR BATTERY 1 AND SOLAR BATTERY 2 IN PARALLEL, AND CONNECT SECONDARY BATTERY THERETO DIRECTLY.	
PTH	SUPPLY OUTPUT FROM PARALLEL CONNECTION OF SOLAR BATTERY 1 AND SOLAR BATTERY 2 TO SECONDARY BATTERY VIA STEP-UP CIRCUIT.	
PTM (H)	SUPPLY OUTPUT FROM PARALLEL CONNECTION OF SOLAR BATTERY 1 AND SOLAR BATTERY 2 TO SECONDARY BATTERY VIA STEP-DOWN CIRCUIT.	
PTN (F)	SUPPLY OUTPUT FROM SERIES CONNECTION OF SOLAR BATTERY 1 AND SOLAR BATTERY 2 TO SECONDARY BATTERY VIA STEP-DOWN CIRCUIT.	

FIG. 22

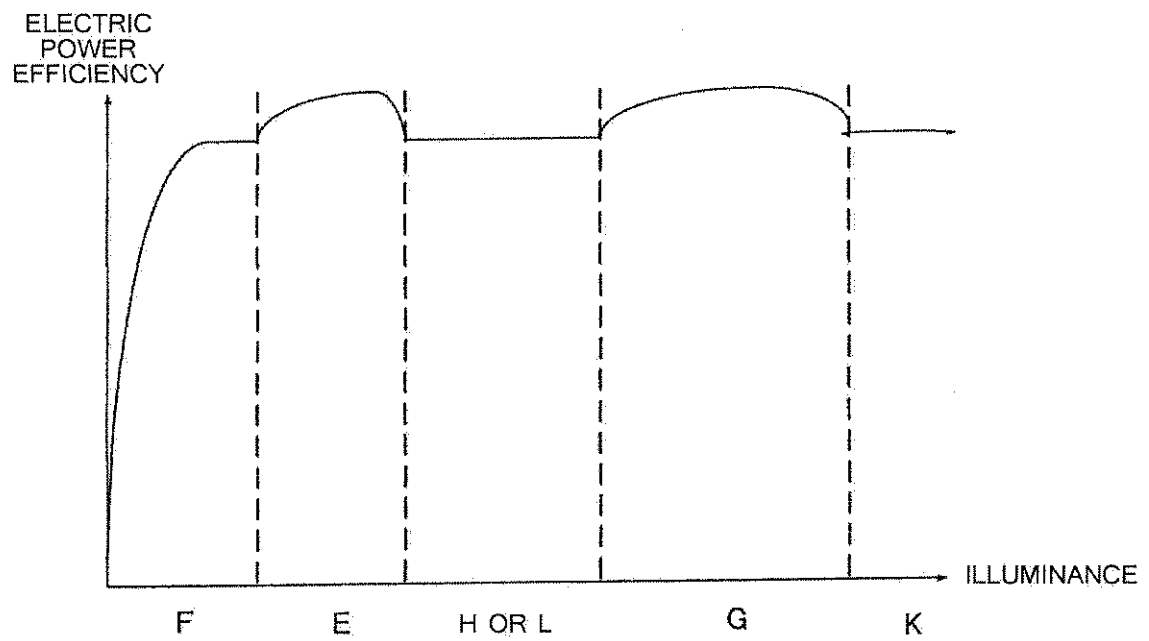


FIG. 23

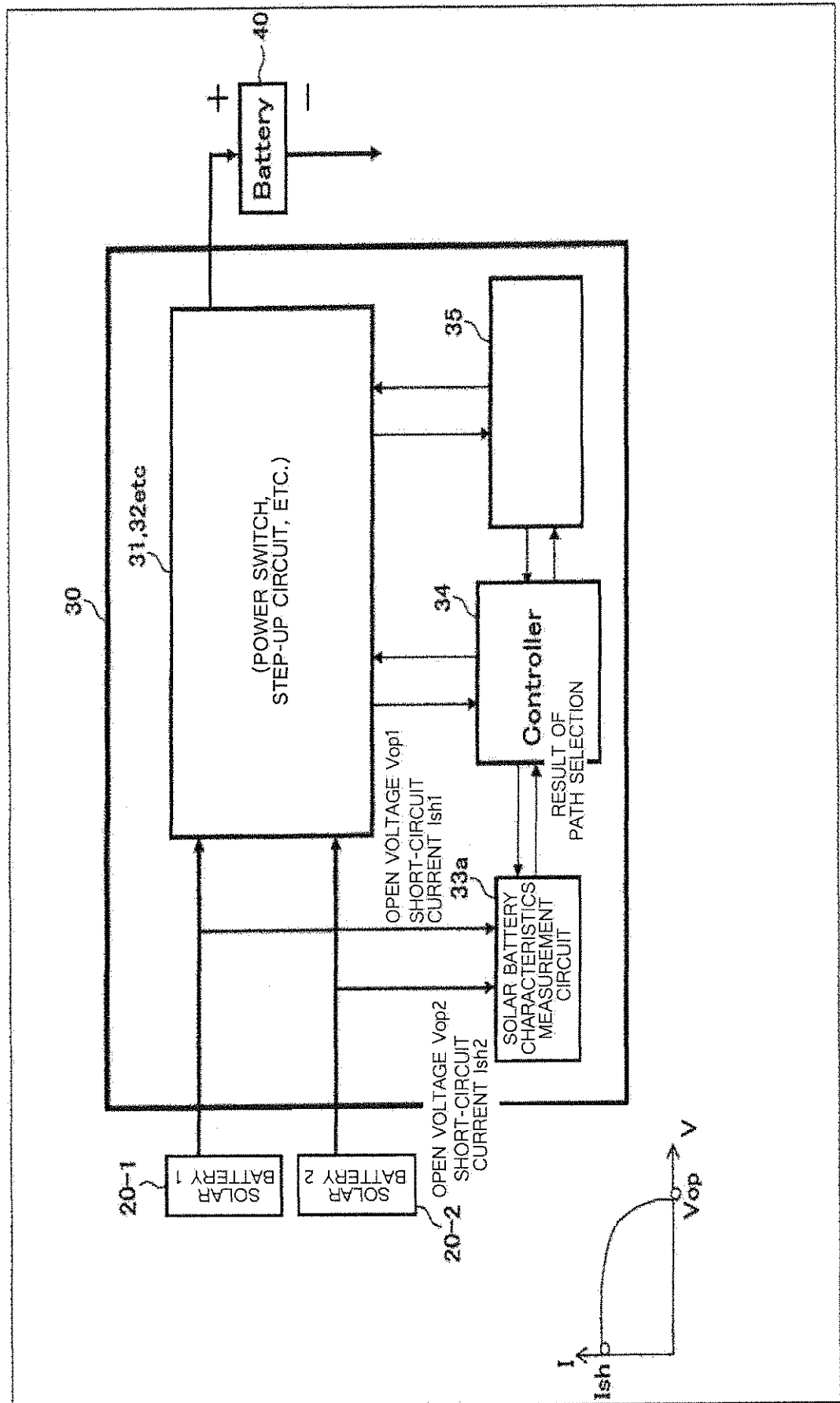


FIG. 24

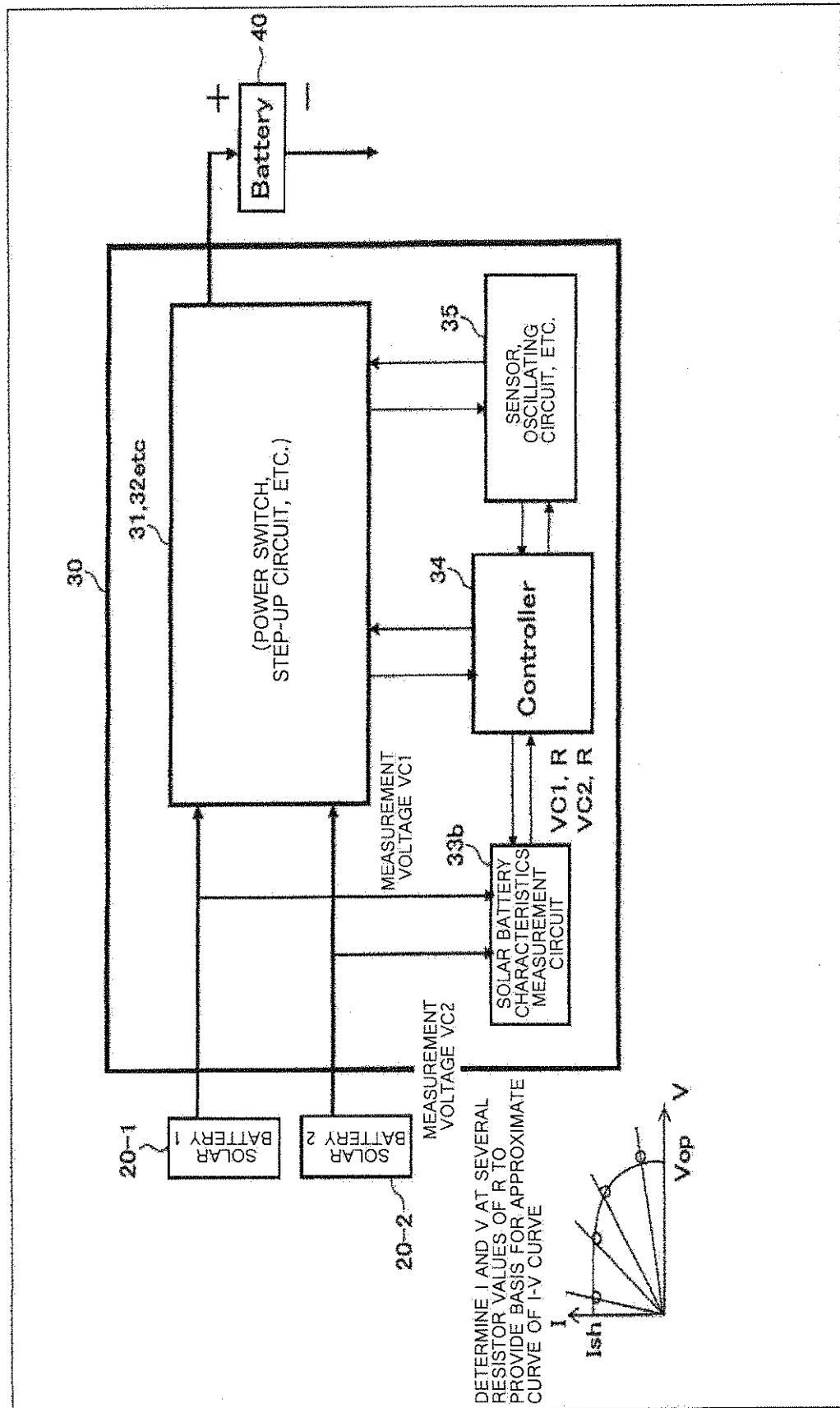


FIG. 25

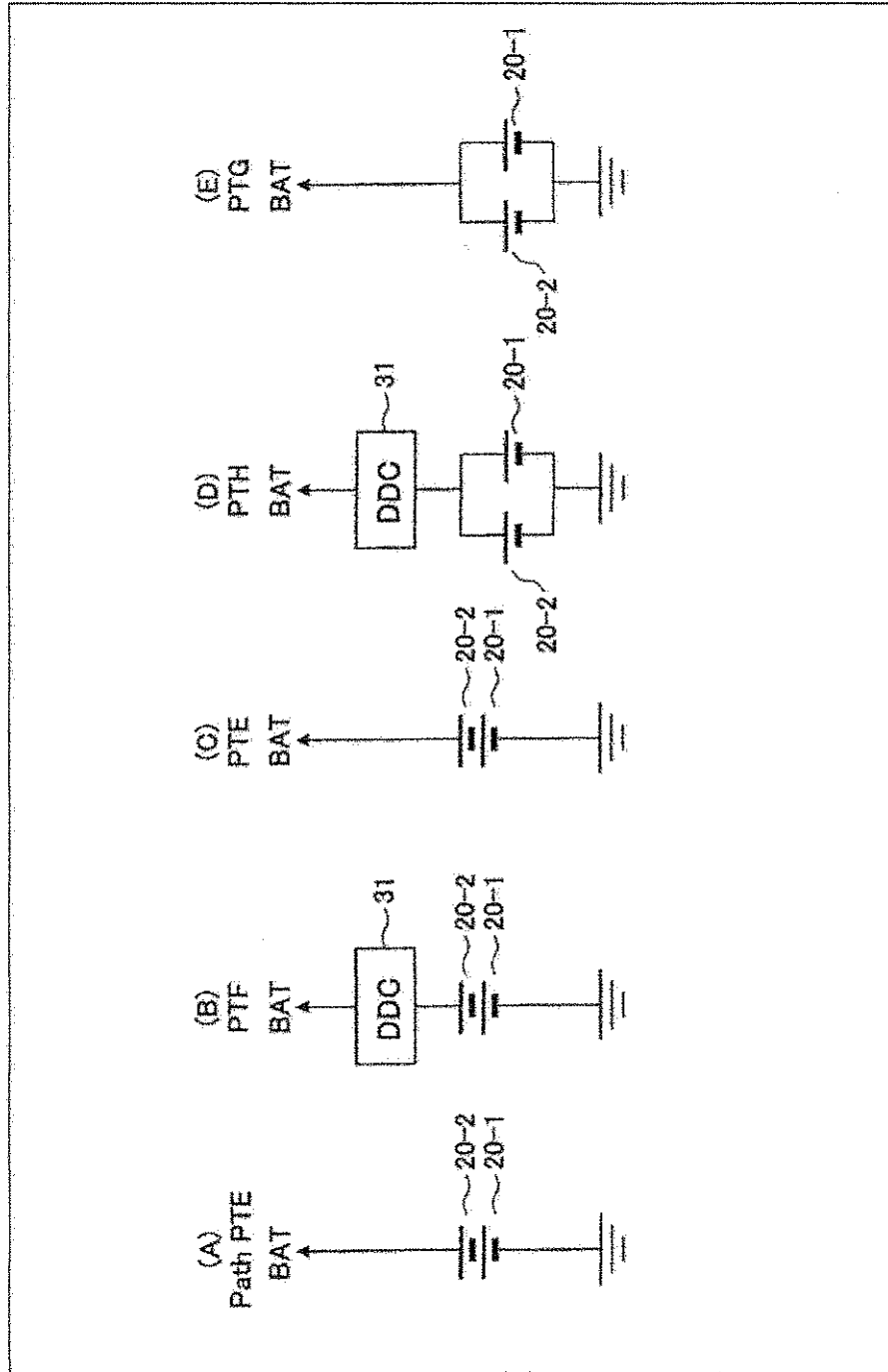


FIG. 26

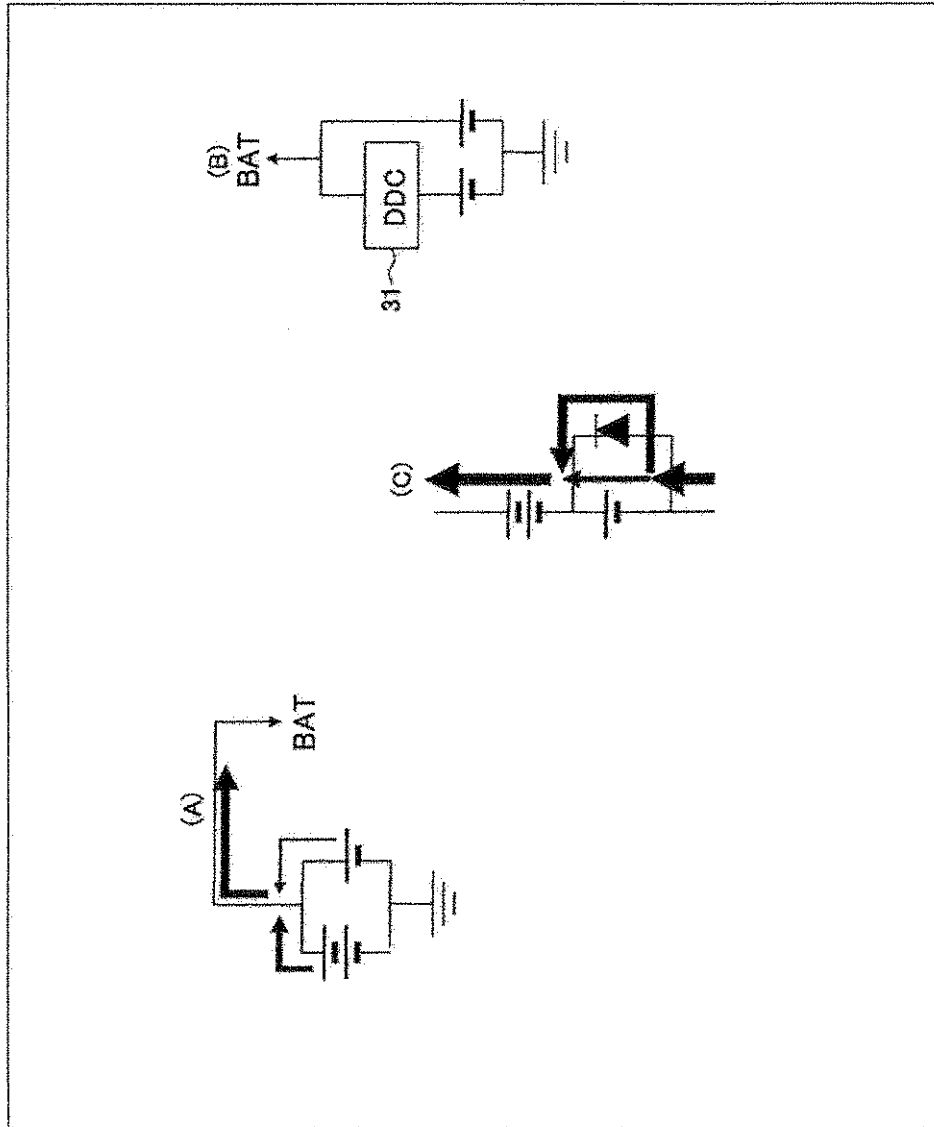


FIG. 28

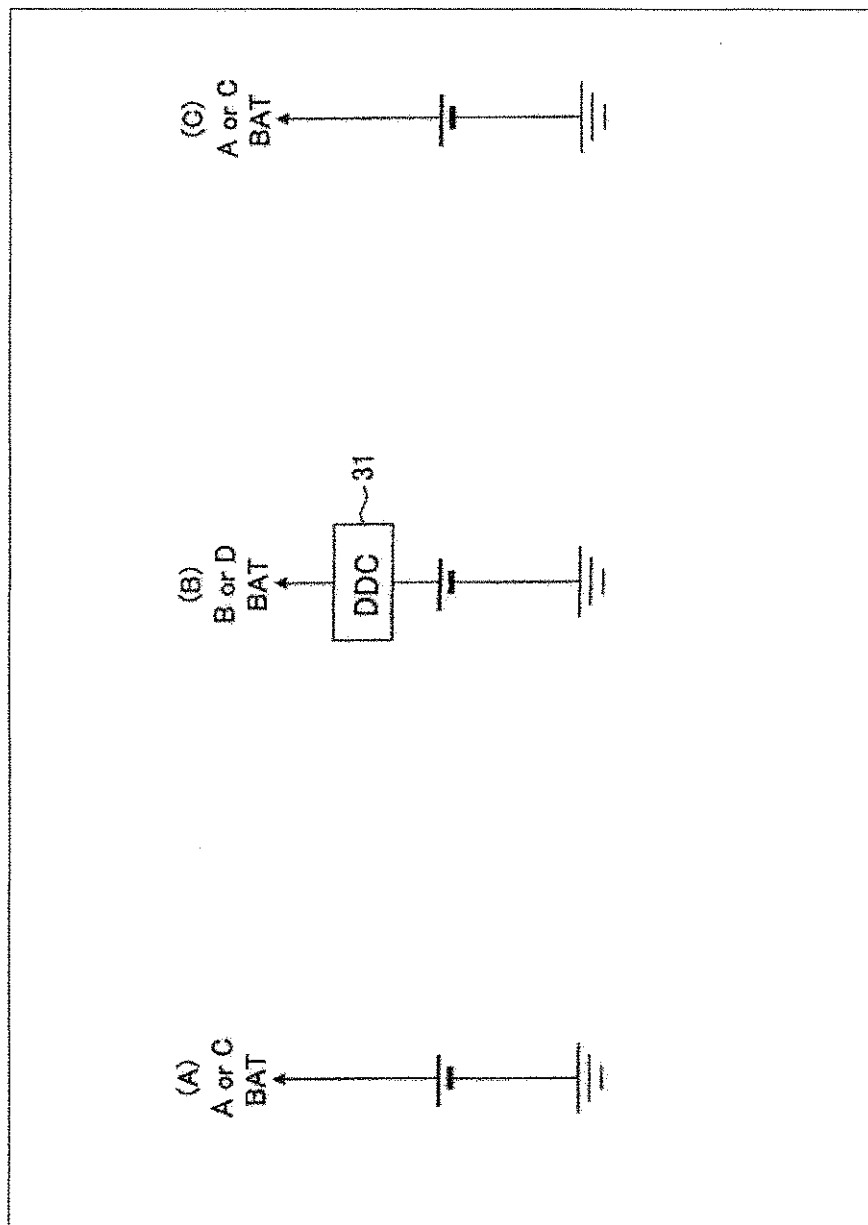


FIG. 29

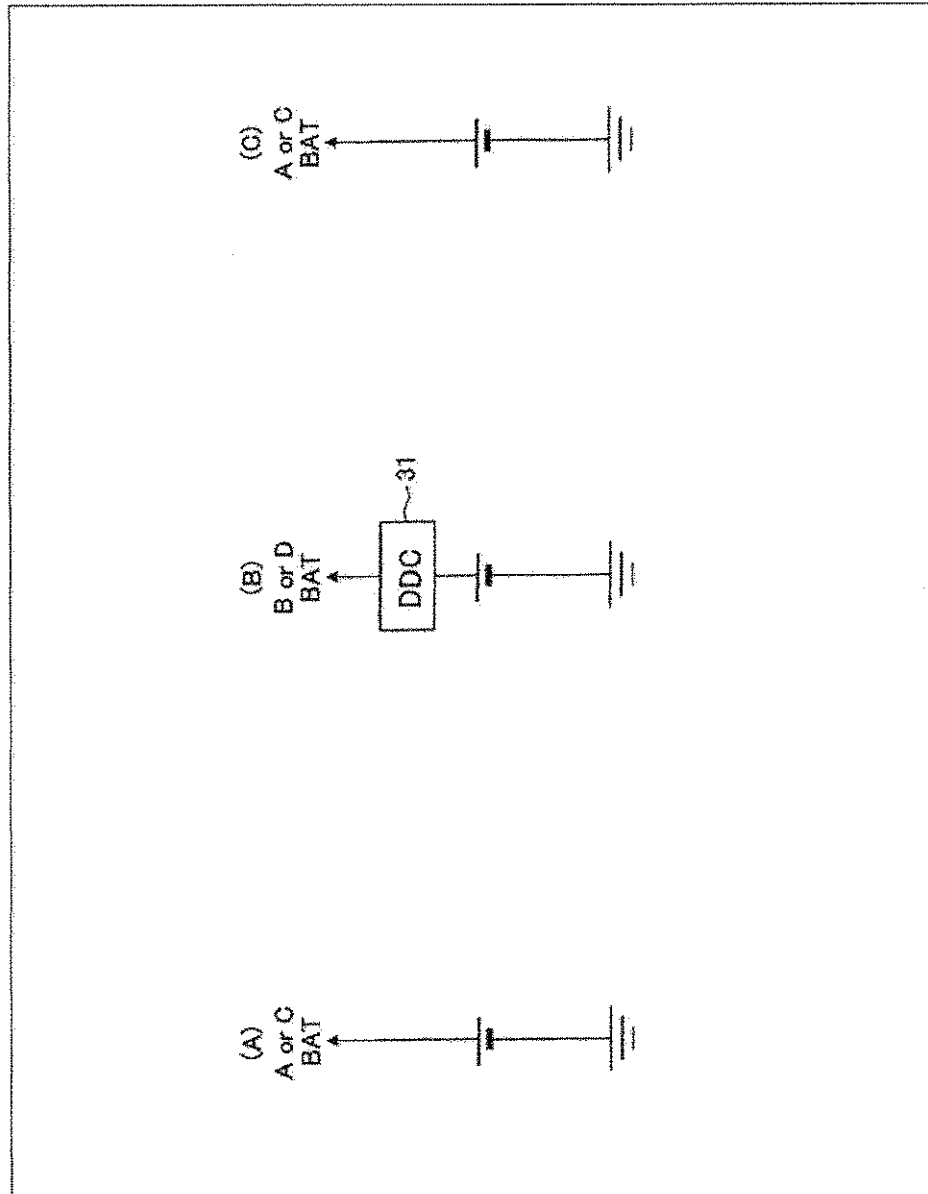


FIG. 30

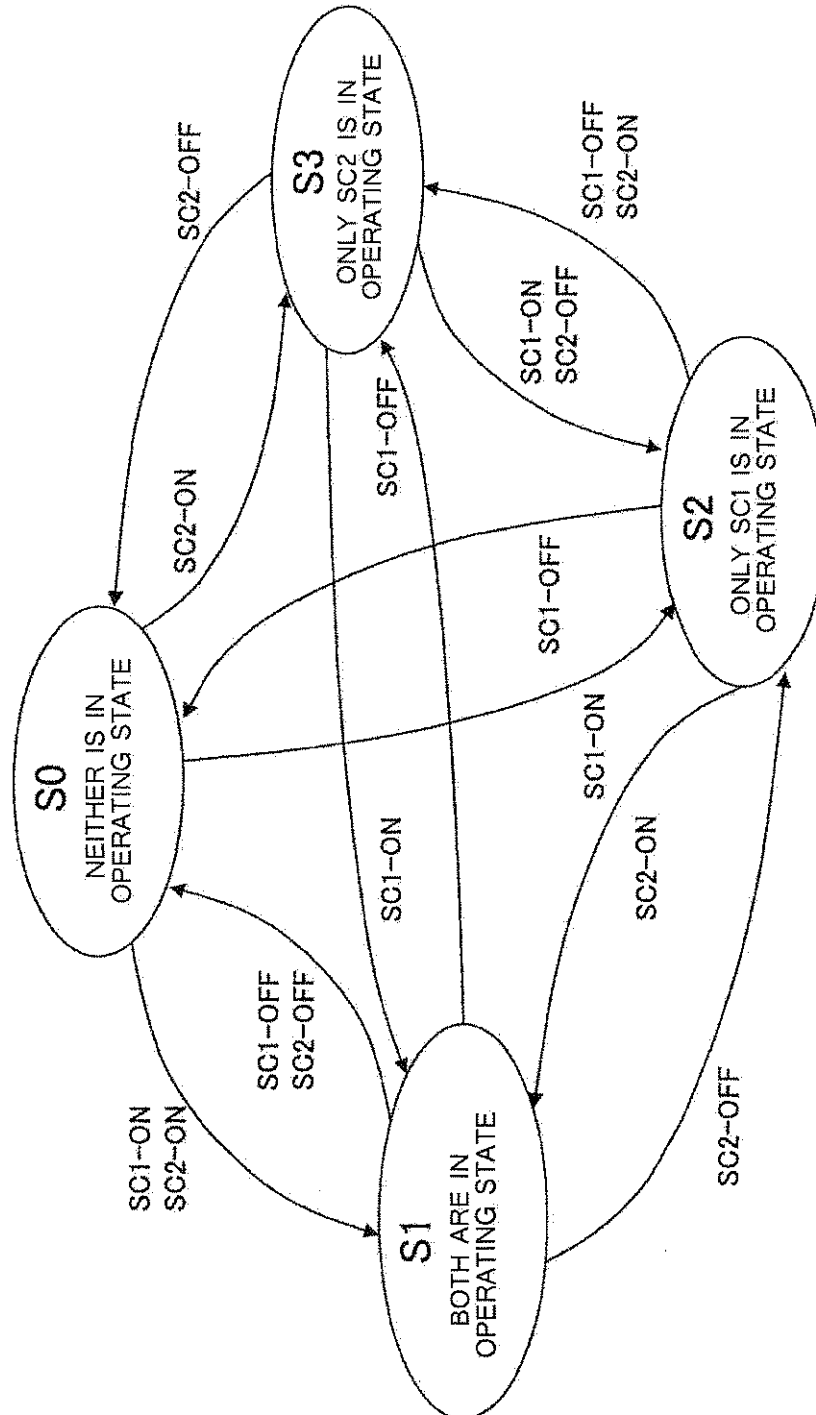


FIG. 31

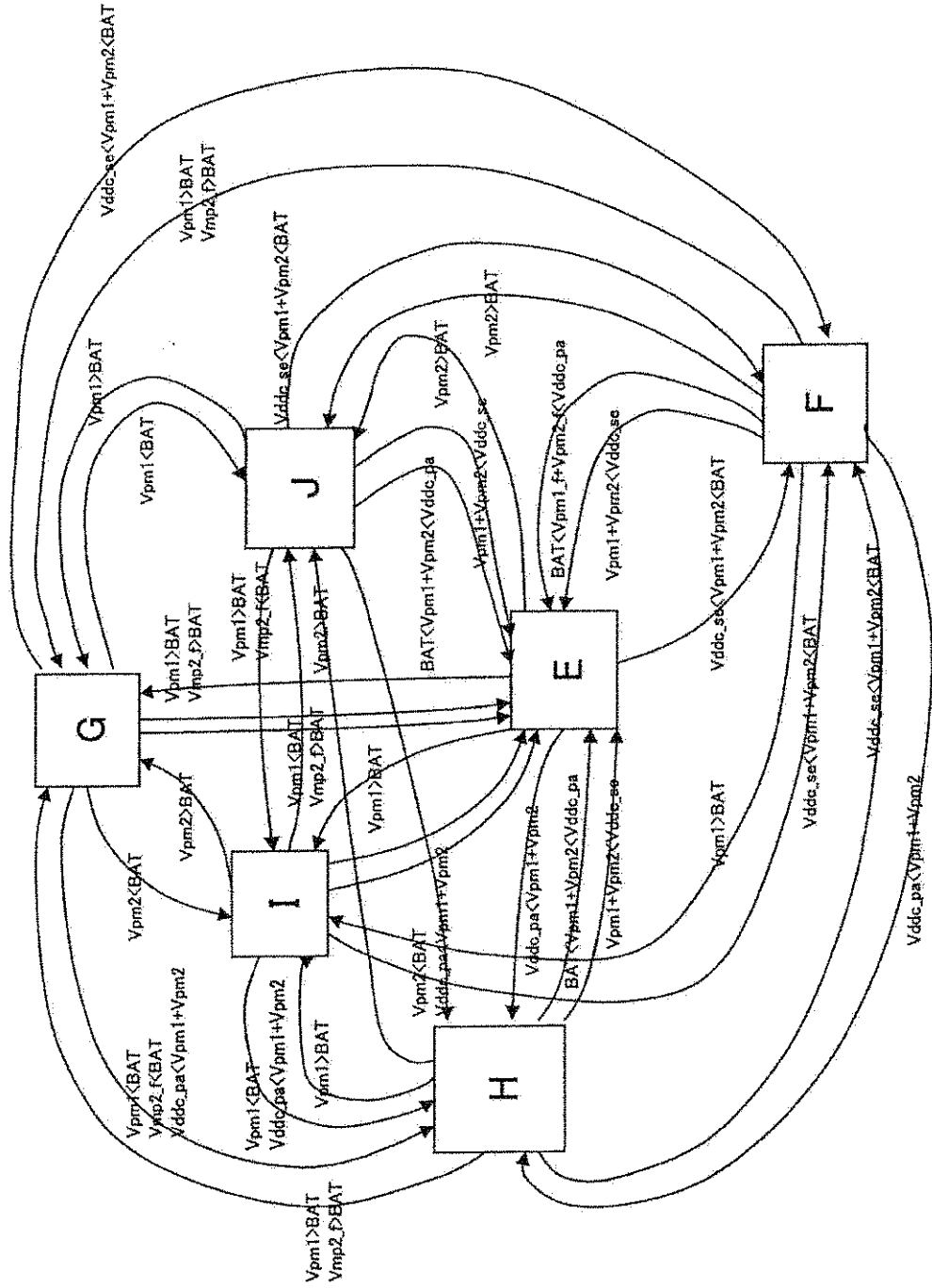


FIG. 32

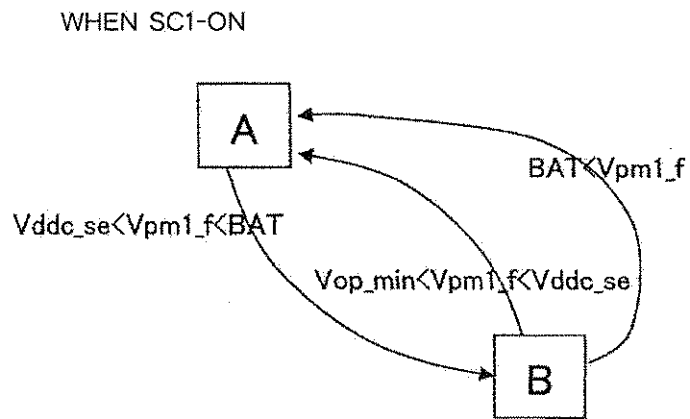


FIG. 33

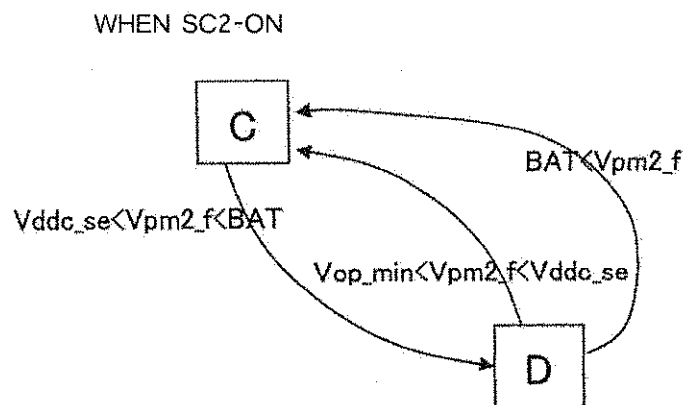


FIG. 34

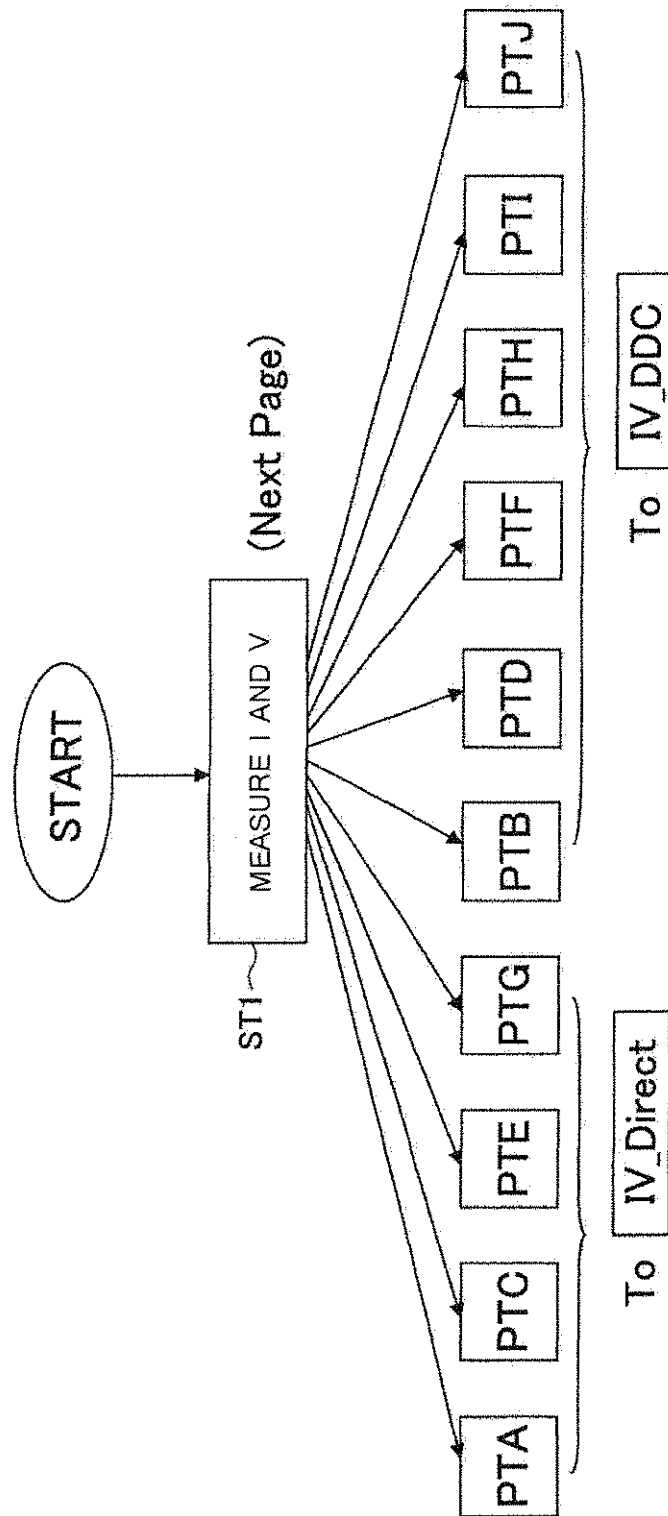


FIG. 35

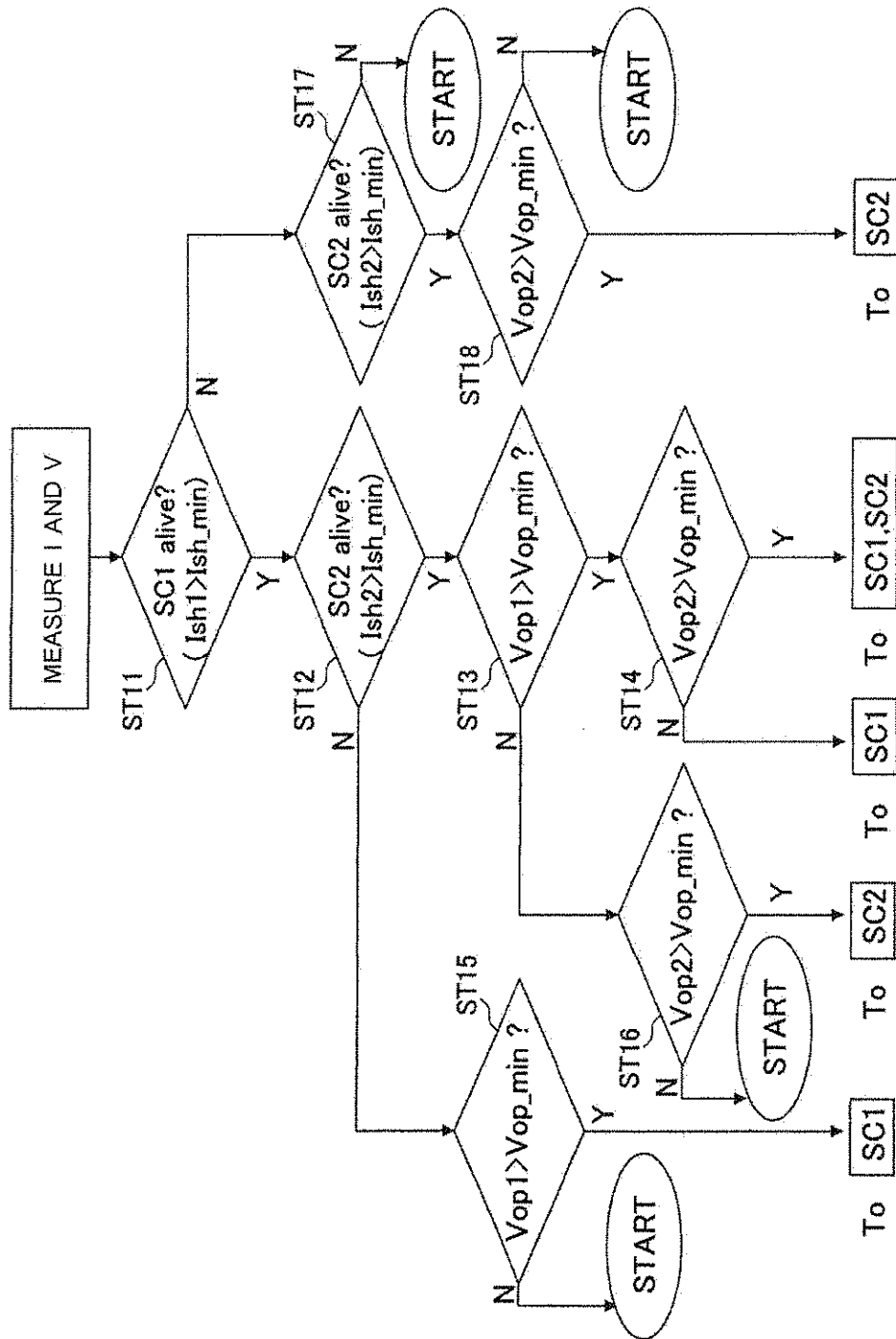


FIG. 36

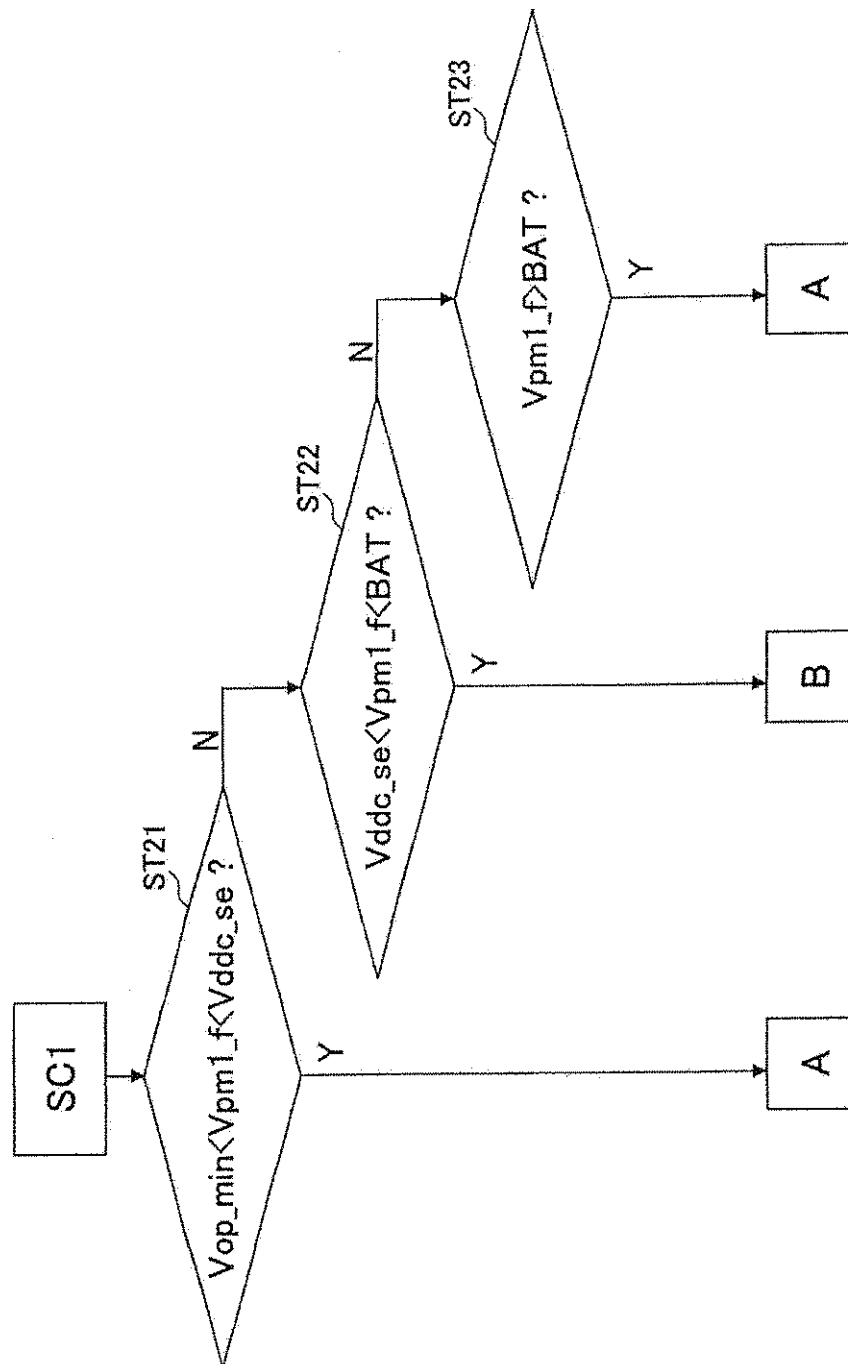


FIG. 37

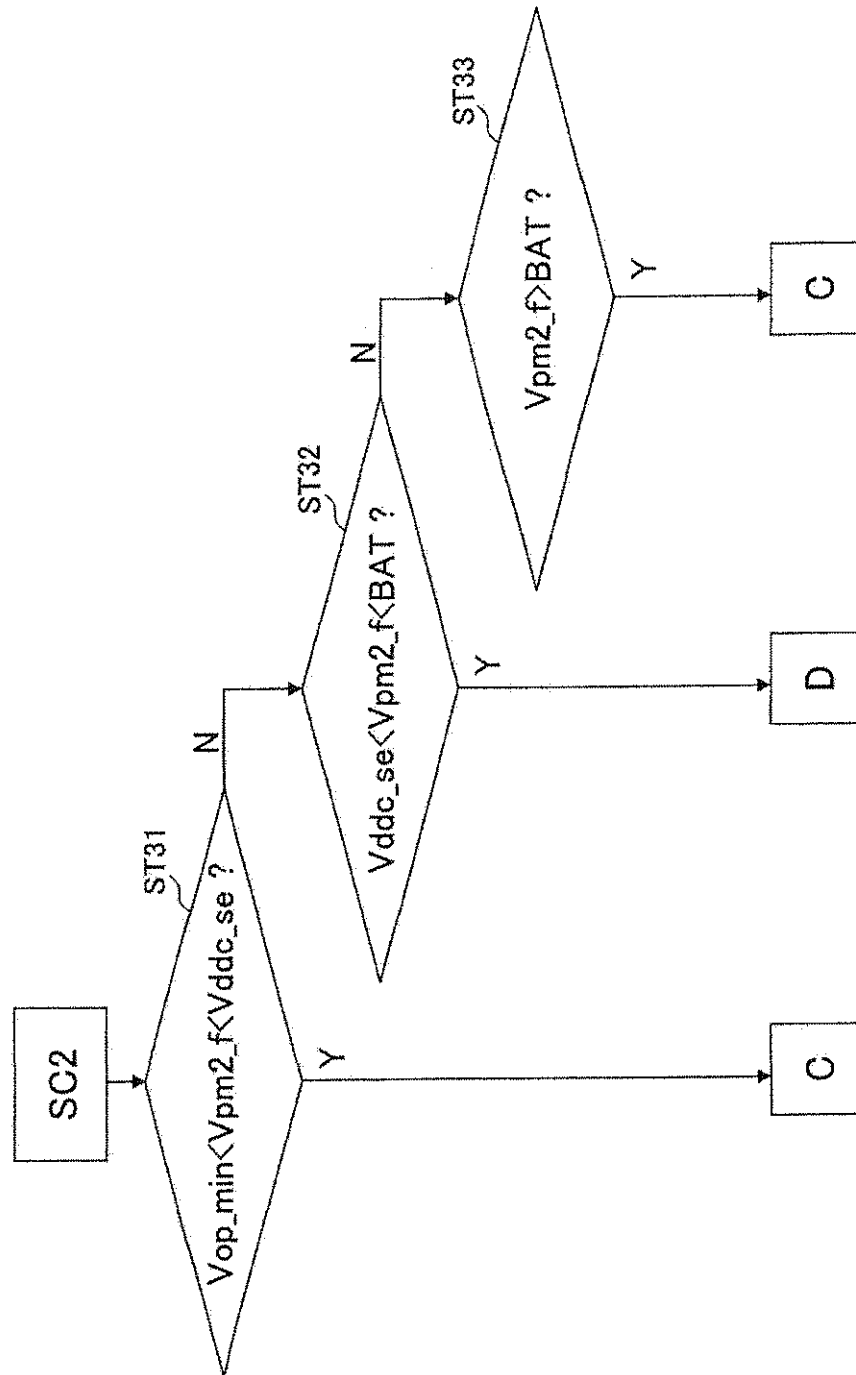


FIG. 38

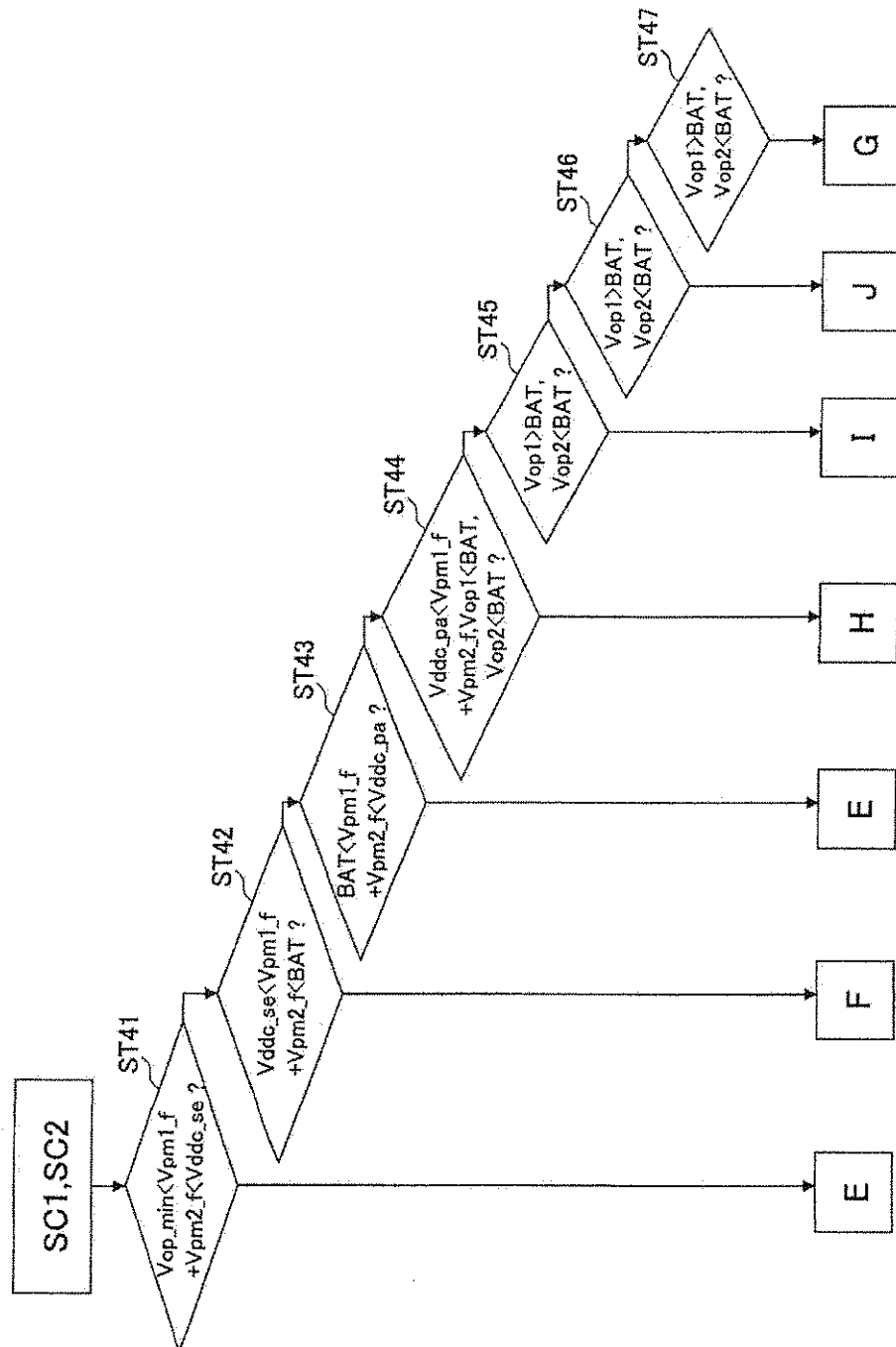


FIG. 39

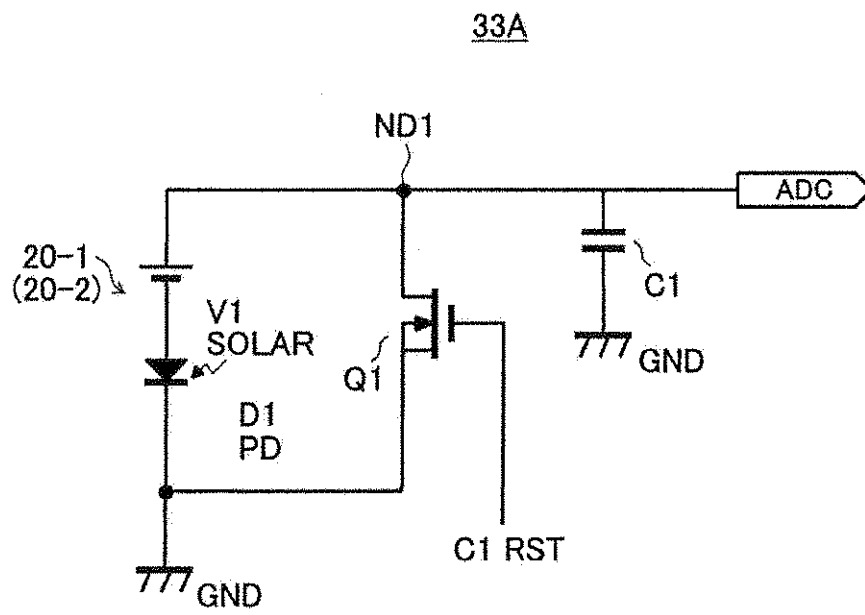


FIG. 40

ACQUISITION OF I-V CHARACTERISTICS
BY DIVERTING CAPACITOR OF STEP-UP CIRCUIT

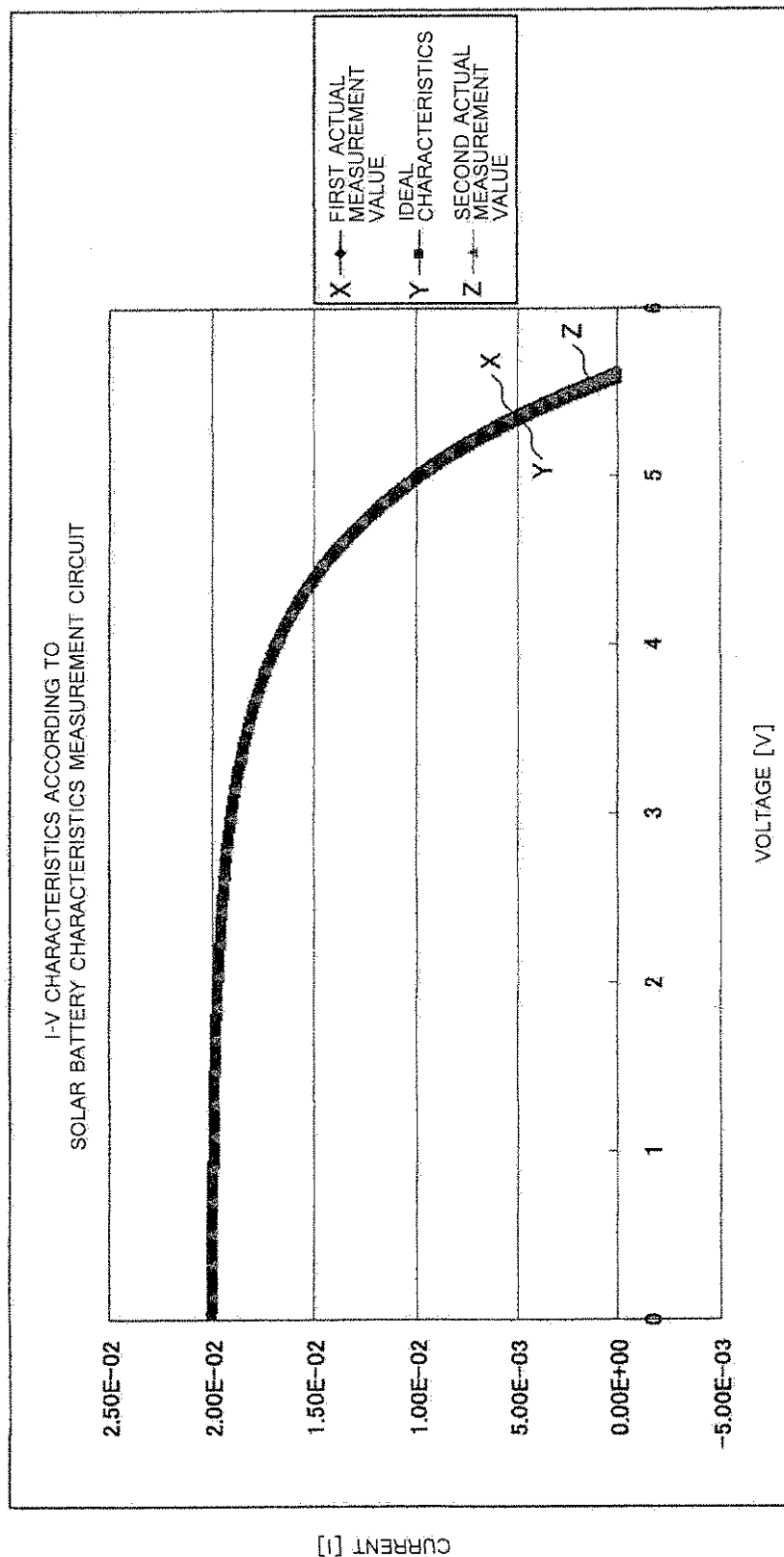
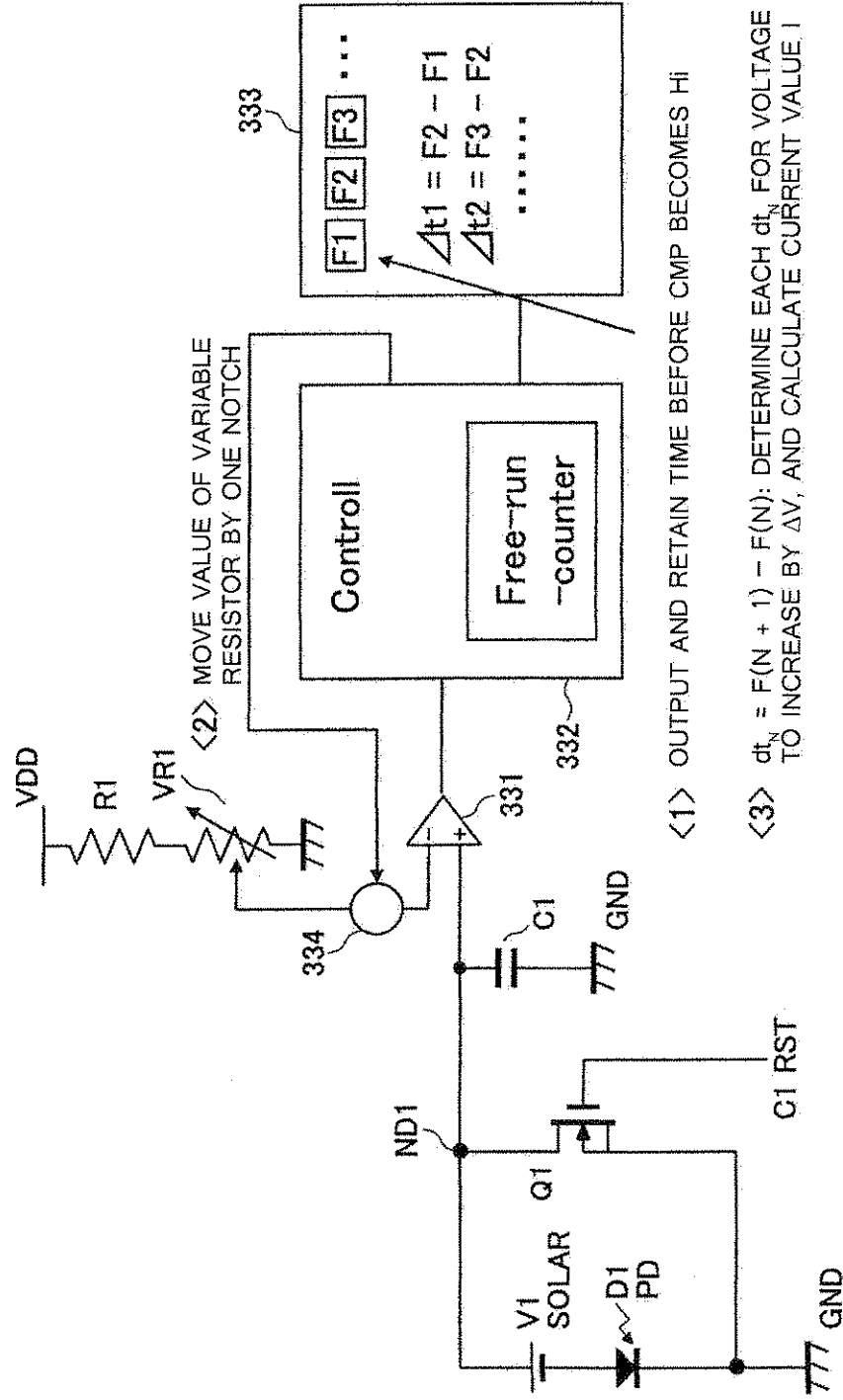


FIG. 41



$$V = Q / C, Q = \int i(t) dt$$

FIG. 42

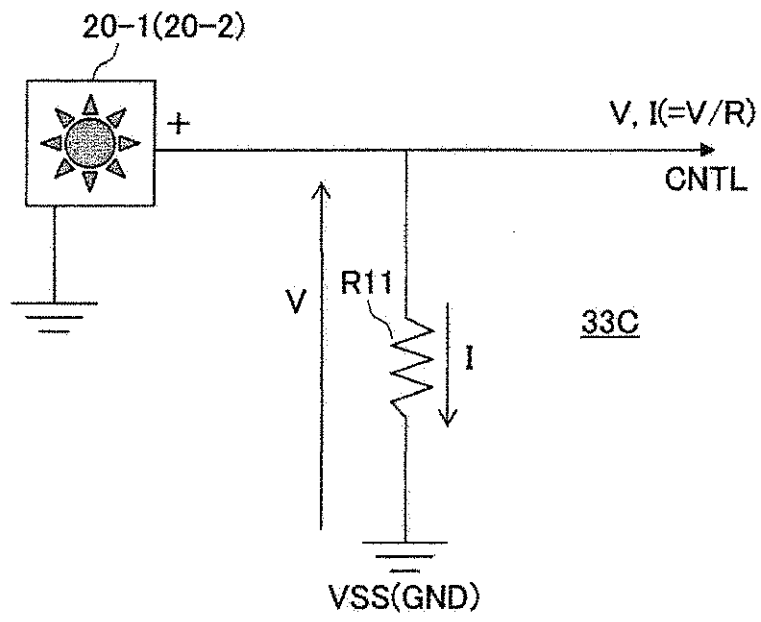


FIG. 43

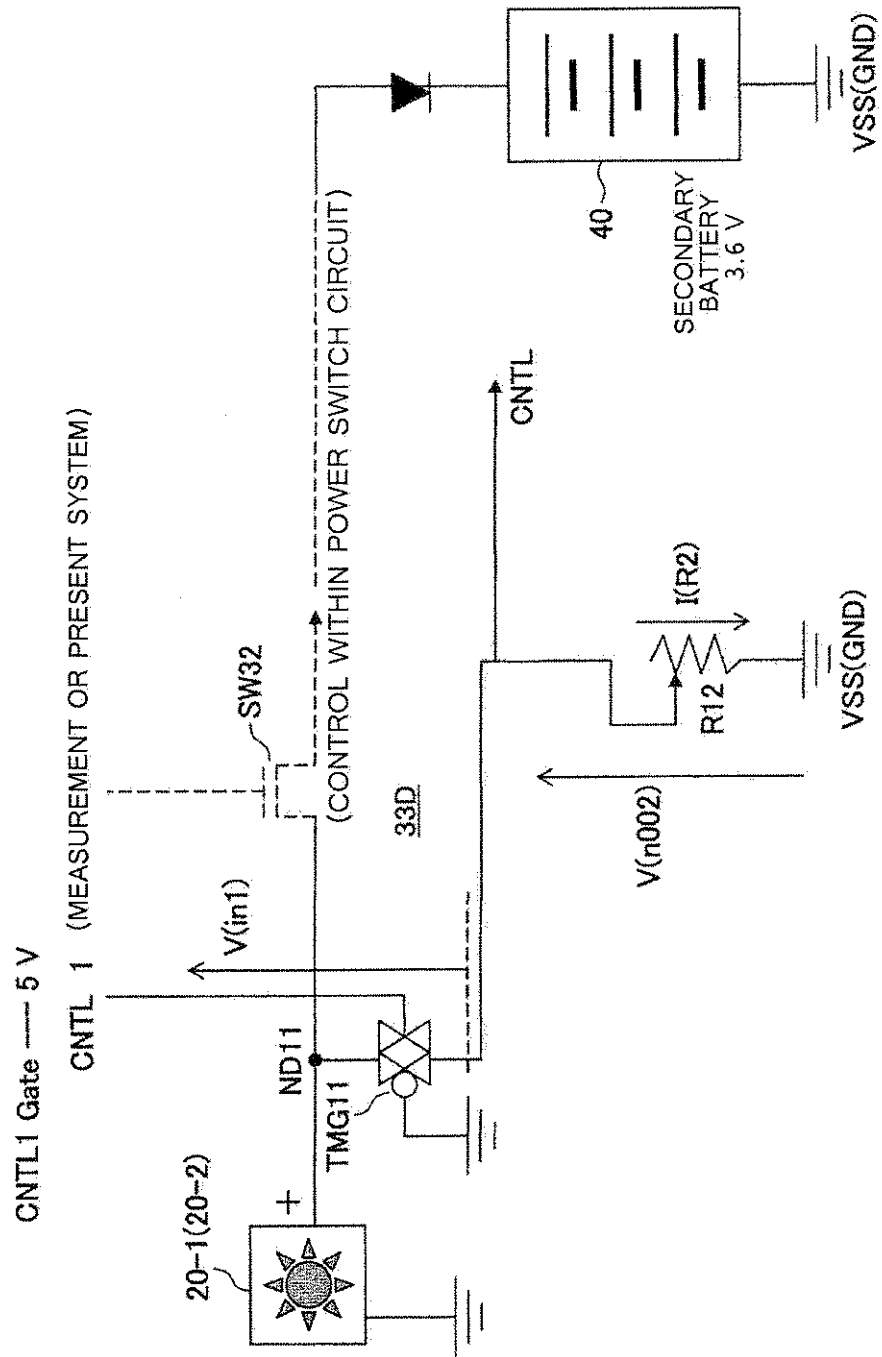


FIG. 44

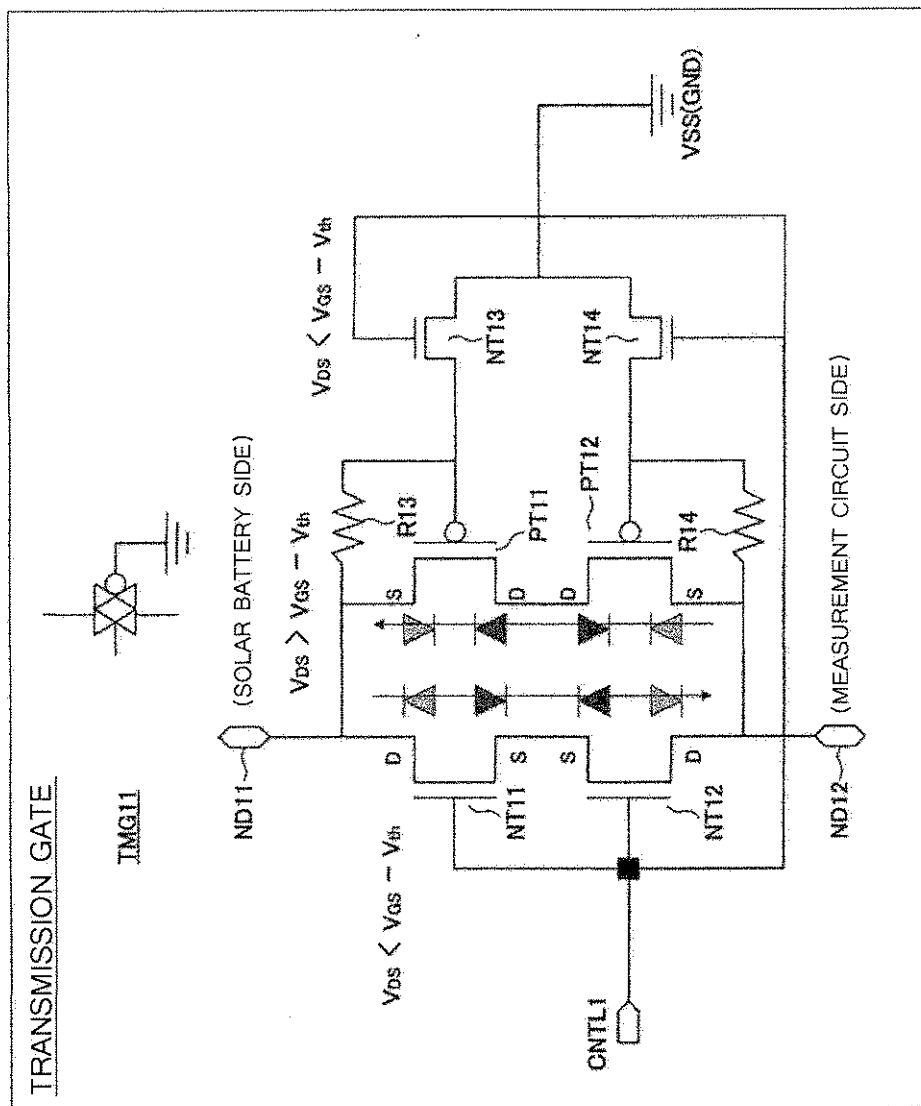


FIG. 45

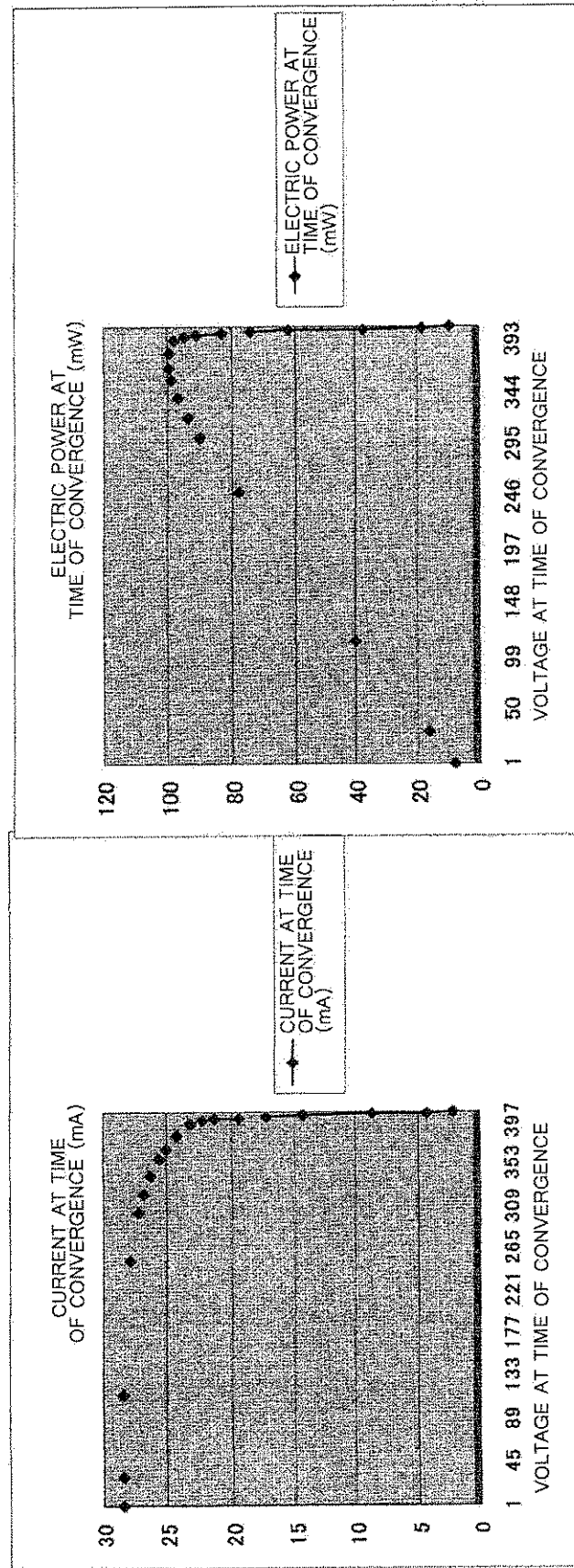


FIG. 46

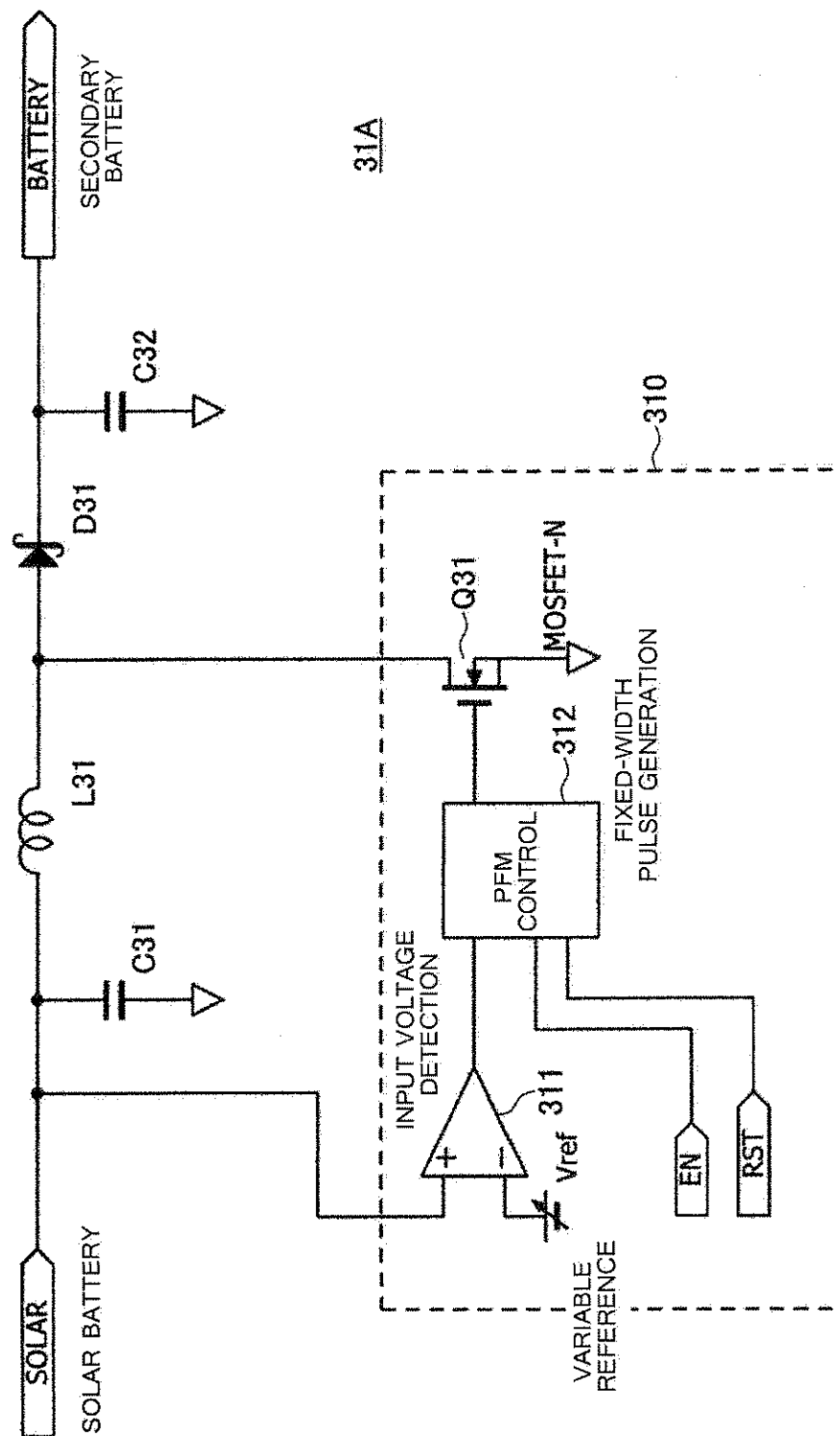


FIG. 47

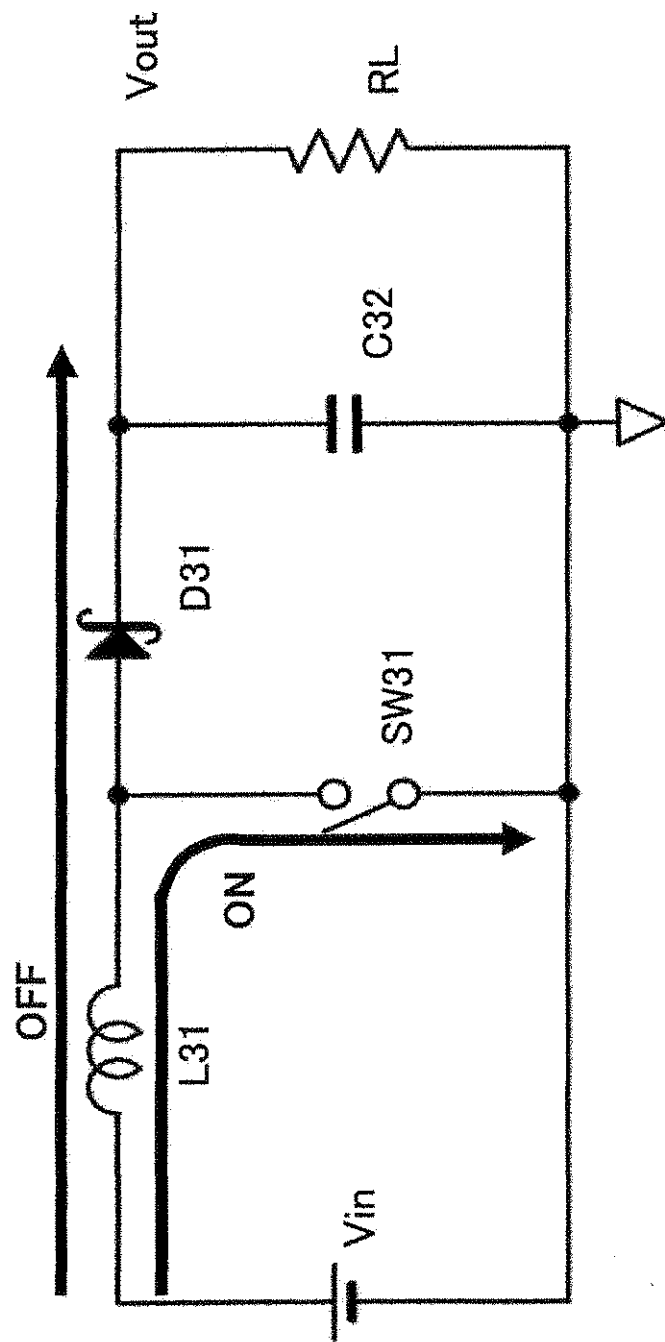


FIG. 48

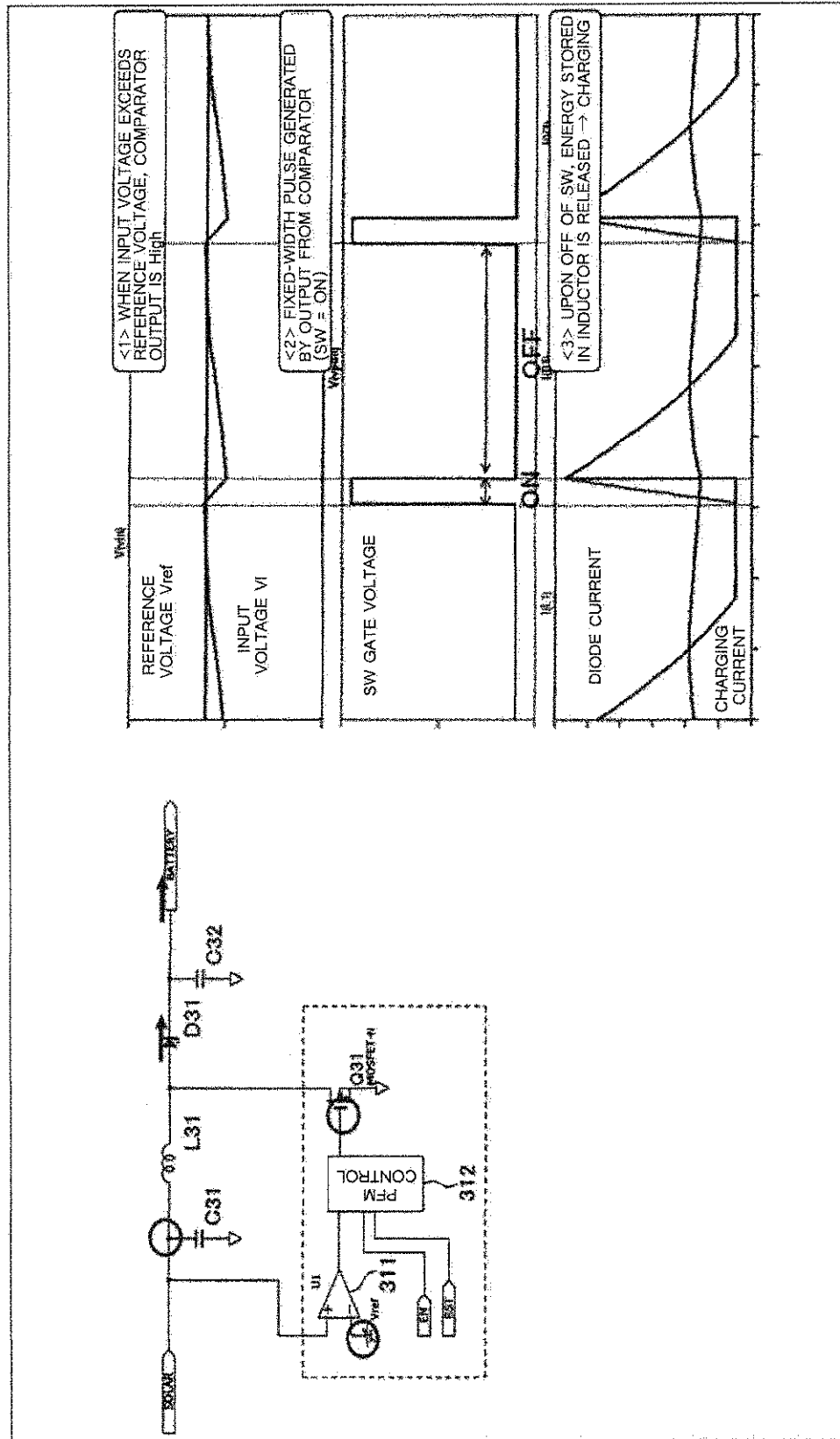


FIG. 50

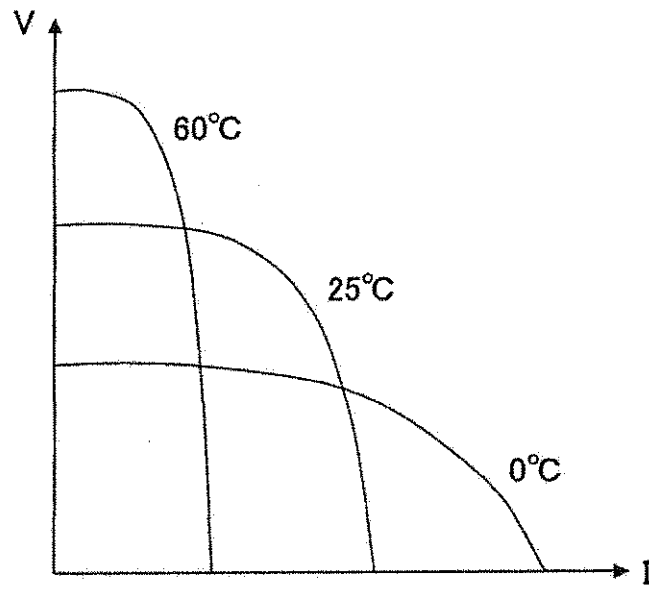


FIG. 51

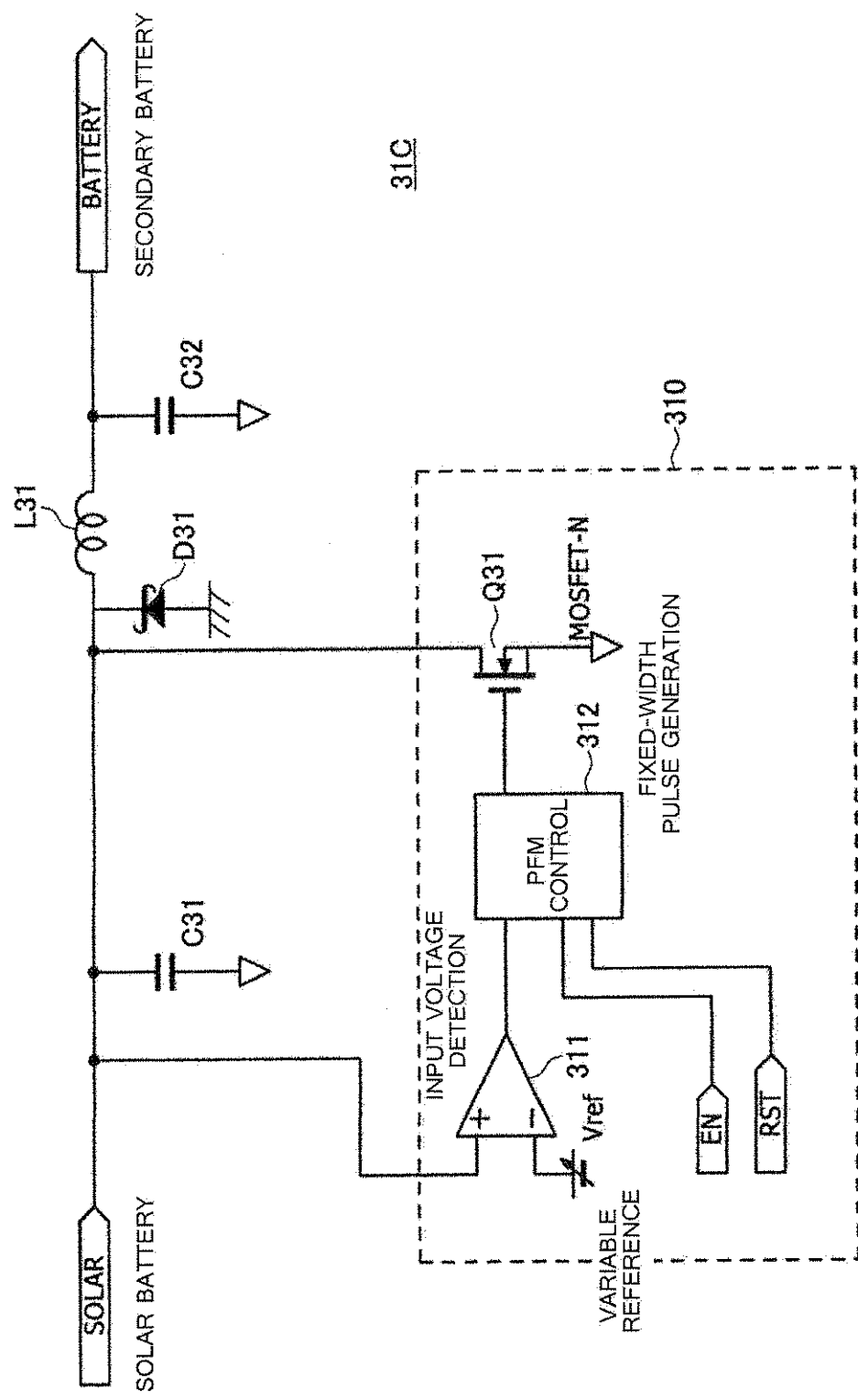


FIG. 52

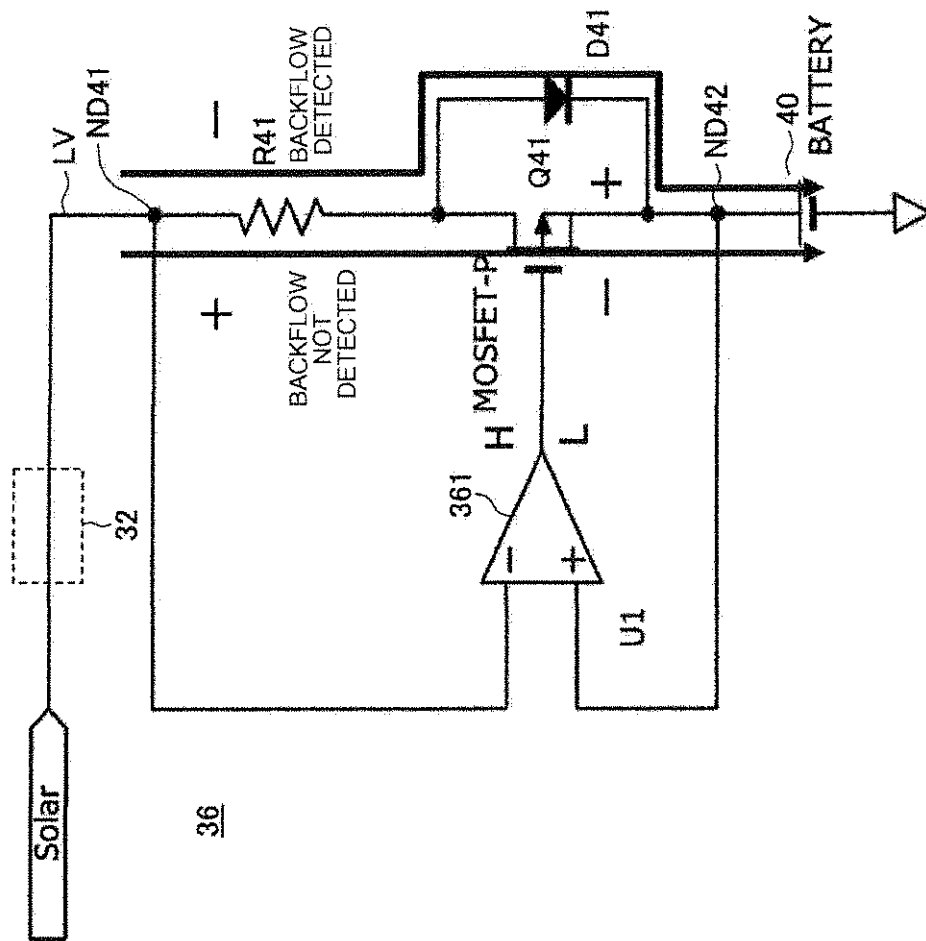


FIG. 53

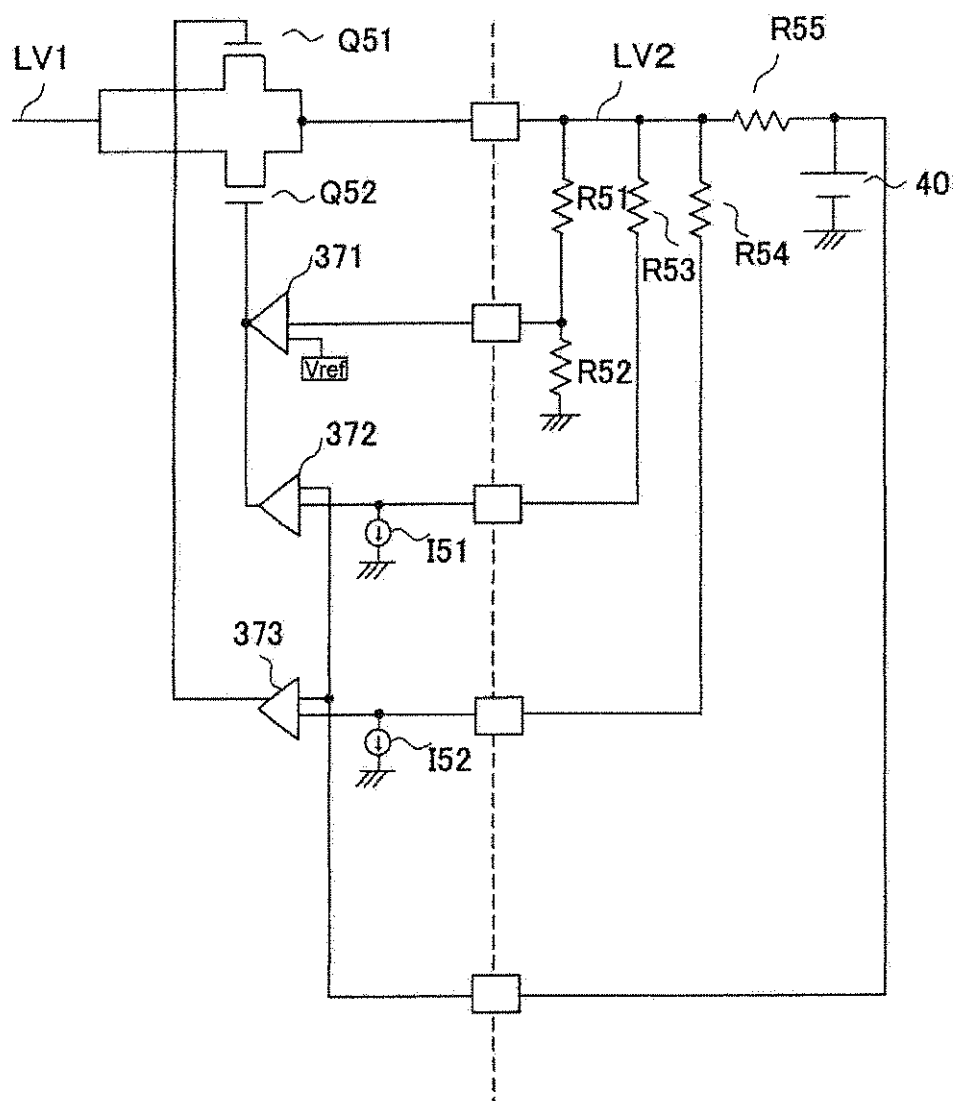


FIG. 54

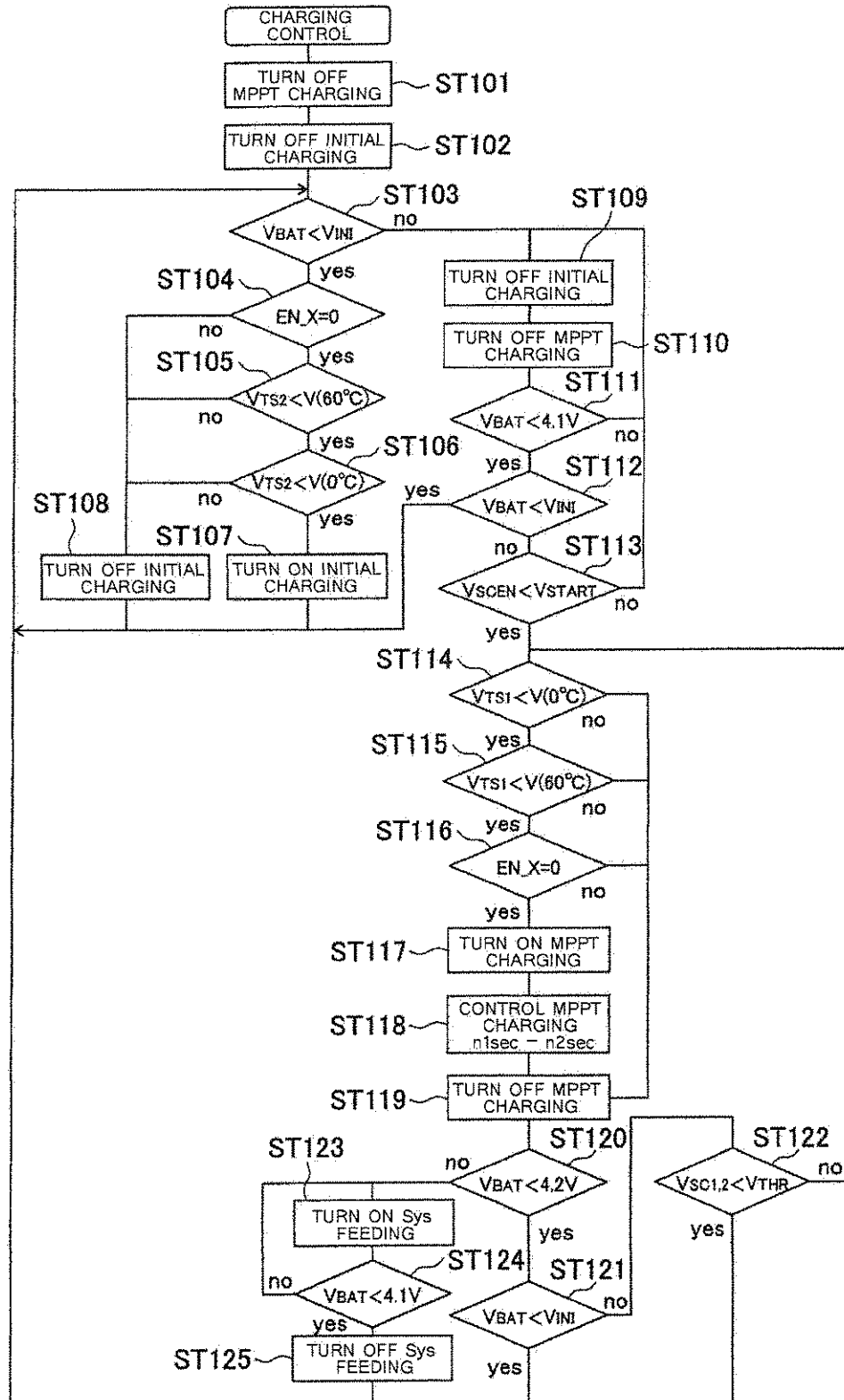


FIG. 55

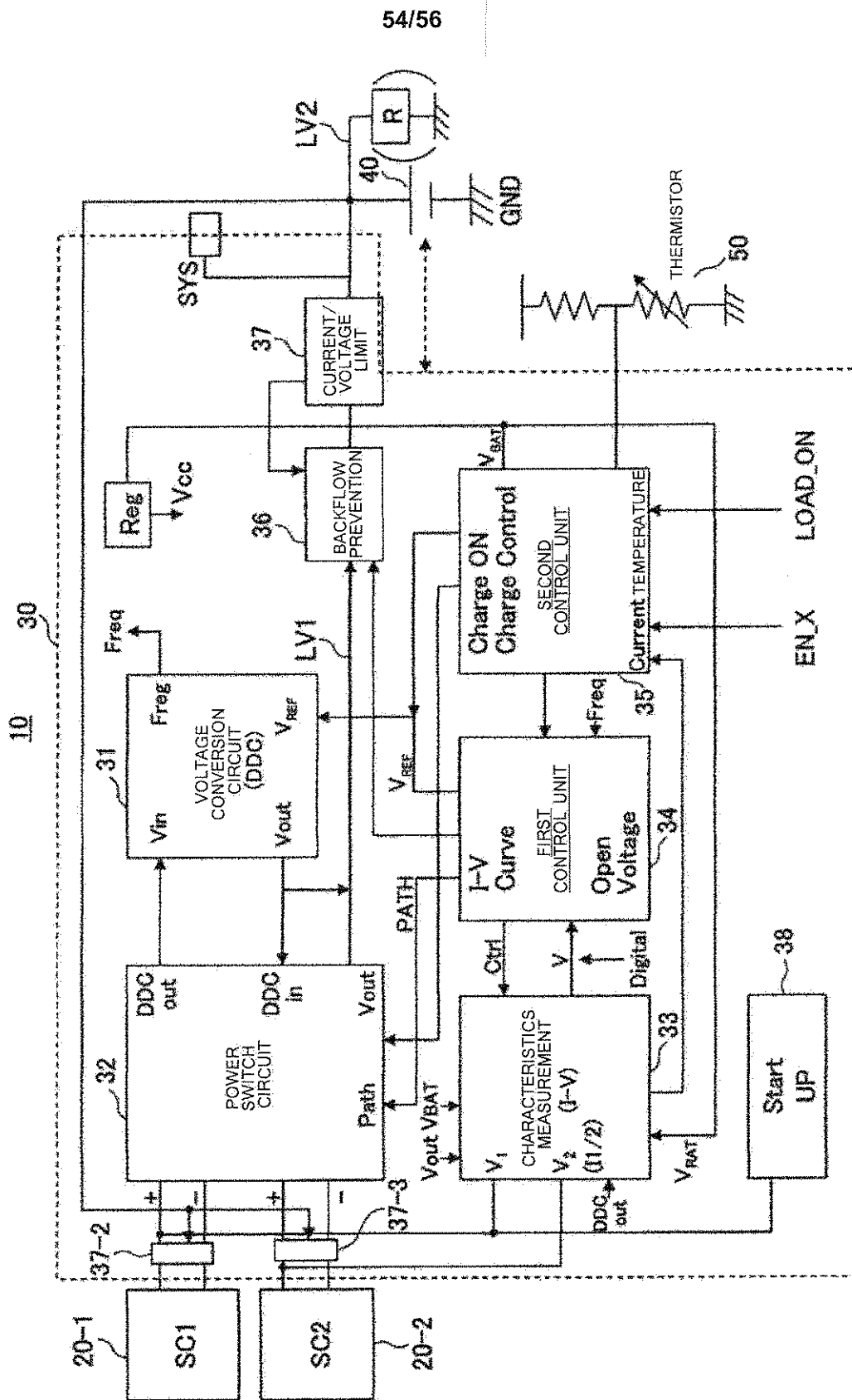


FIG. 56

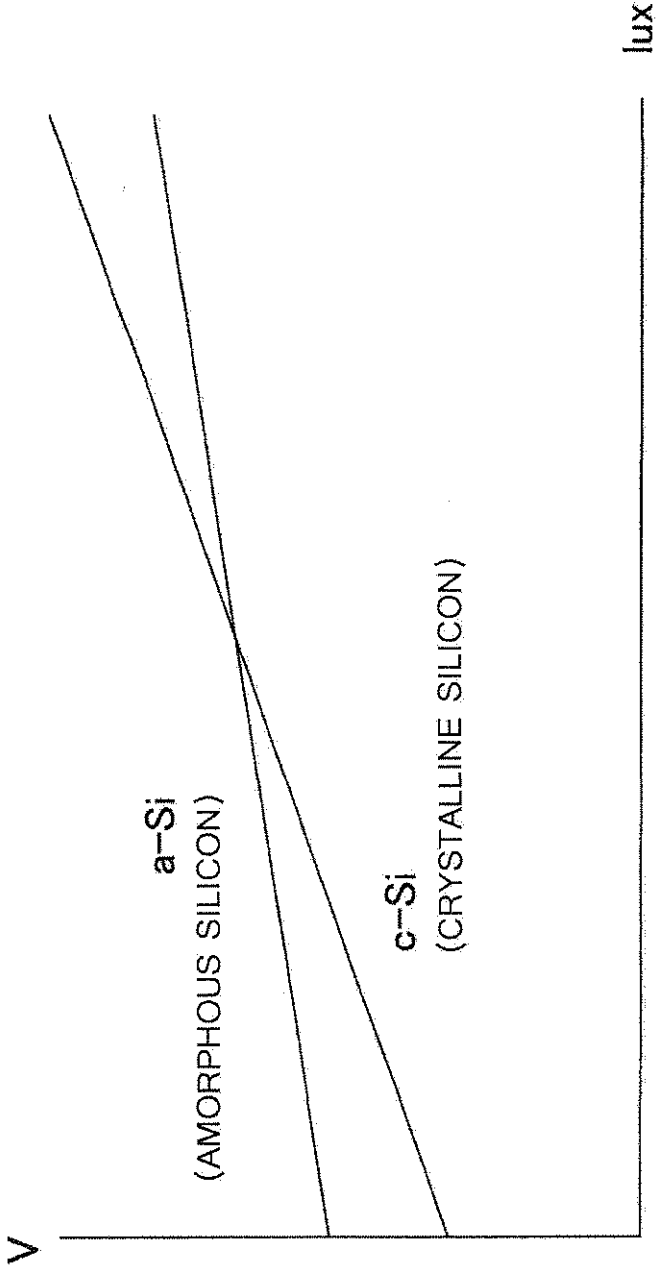


FIG. 57

