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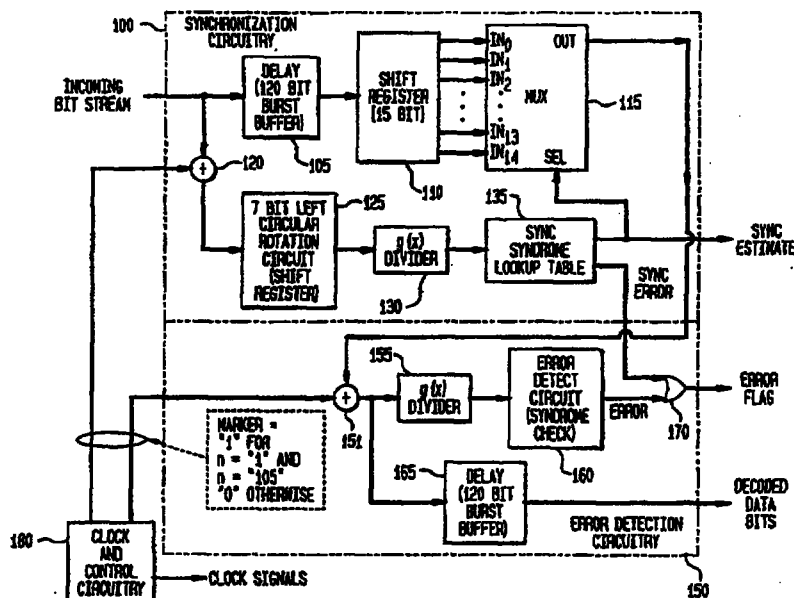
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(71) Applicant: BELL COMMUNICATIONS RESEARCH, INC. [US/US]; 445 South Street, Morristown, NJ 07960-6438 (US). <i>see PF23 & 11/10/99</i>			
(72) Inventors: CHANG, Li-Fung; 11 Burgundy Drive, Holmdel, NJ 07733 (US). ZIEGLER, Robert, A.; 25 Glenmary Avenue, Red Bank, NJ 07701 (US).			
(74) Agents: YEADON, Loria, B. et al.; International Coordinator, Room 1G112R, 445 South Street, Morristown, NJ 07960-6438 (US).			

(54) Title: AN IMPROVED TECHNIQUE FOR JOINTLY PERFORMING BIT SYNCHRONIZATION AND ERROR DETECTION IN A TDM/TDMA SYSTEM



(57) Abstract

An improved technique for simultaneously performing bit synchronization (100) and error detection (150) of received digital data bursts in a time division multiplexed/time division multiple access (TDM/TDMA) system, such as that used in conjunction with low power portable digital telephony. With the improved technique both bit synchronization (100) and error detection (150) are performed simultaneously to thereby reduce latency in the transceiver. Since round-trip delay is reduced, echo suppression techniques may not be needed.