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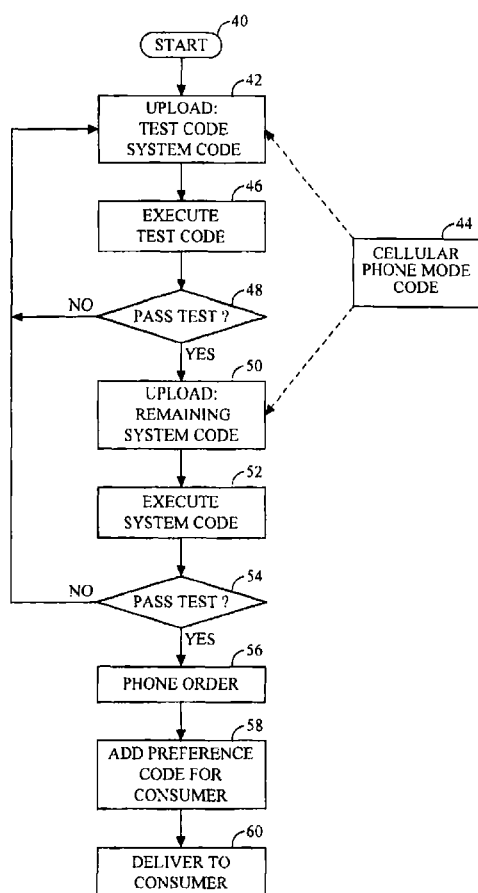
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(54) Title: SYSTEM AND METHOD FOR PRE-PROGRAMMING AN ELECTRONIC DEVICE'S MEMORY



(57) Abstract: A system and method for parallel programming an electronic device's memory during manufacturing. In one embodiment, the electronic device is programmed in parallel with test code and a portion of the system code (42) prior to board level testing. The test code is then used (46) during board level testing of the electronic device. Once board level testing is complete, the electronic device is programmed with additional system code (50) to complement the existing system code, whereby system level testing is then performed (52).



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