FIG. 1A

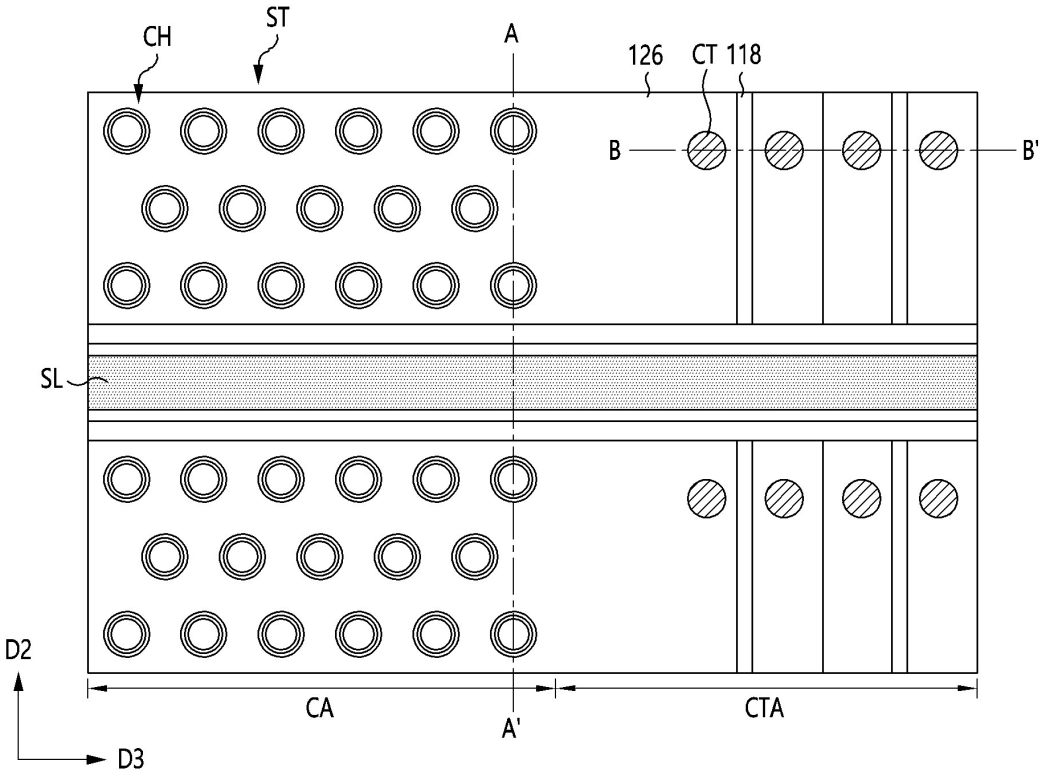


FIG. 1B

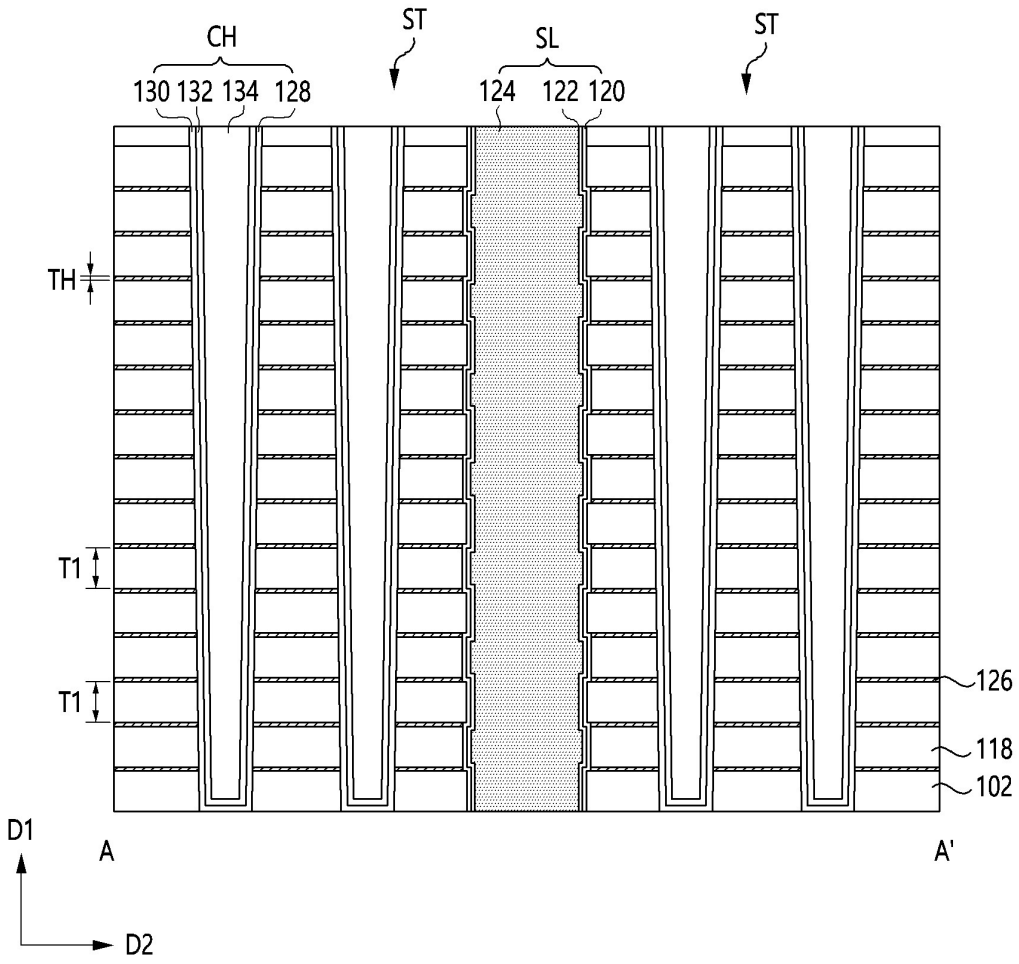

CH
ST
SL
ST
130 132 134 128
124 122 120
TH
T1
T1
126
118
102
D1
A
A'
D2

FIG. 1C

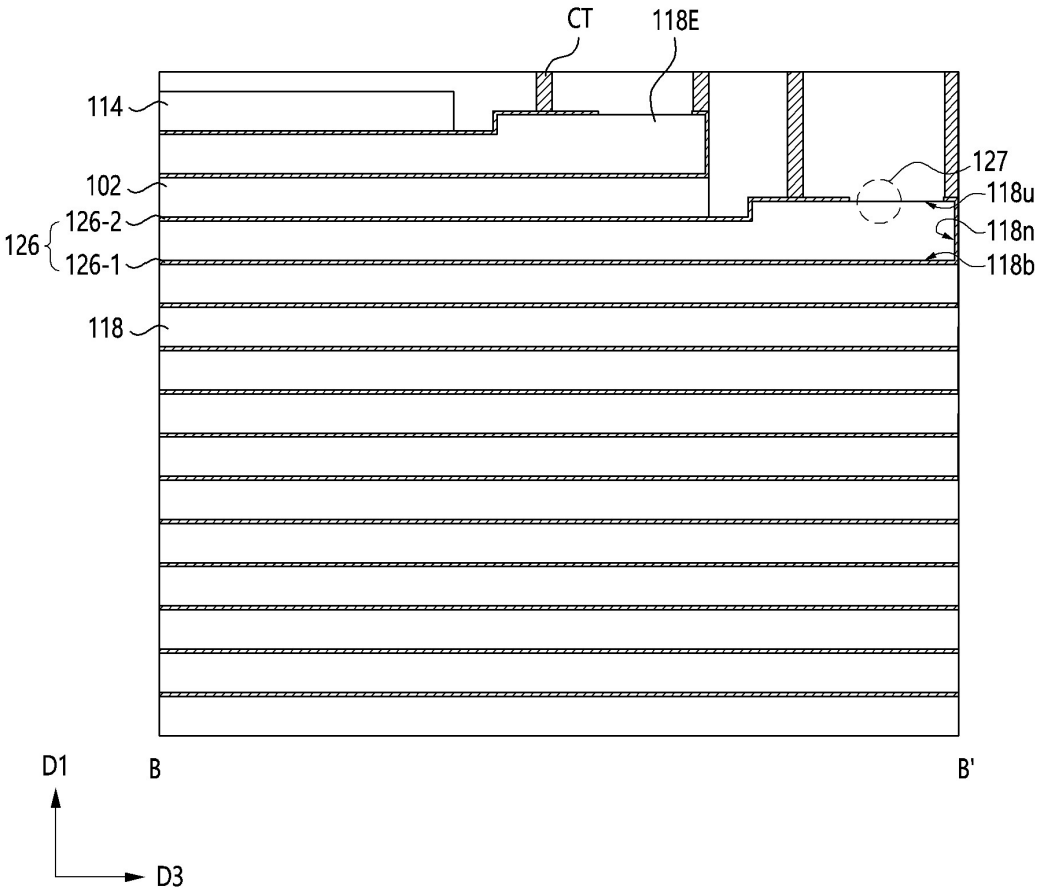


FIG. 2A

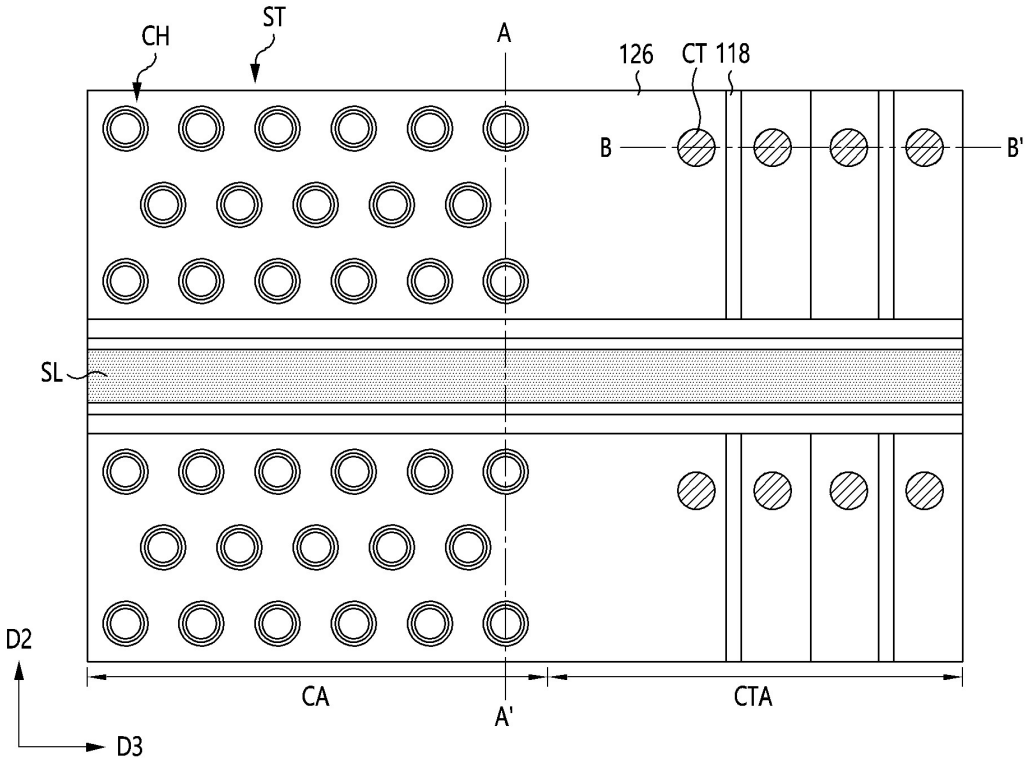


FIG. 2B

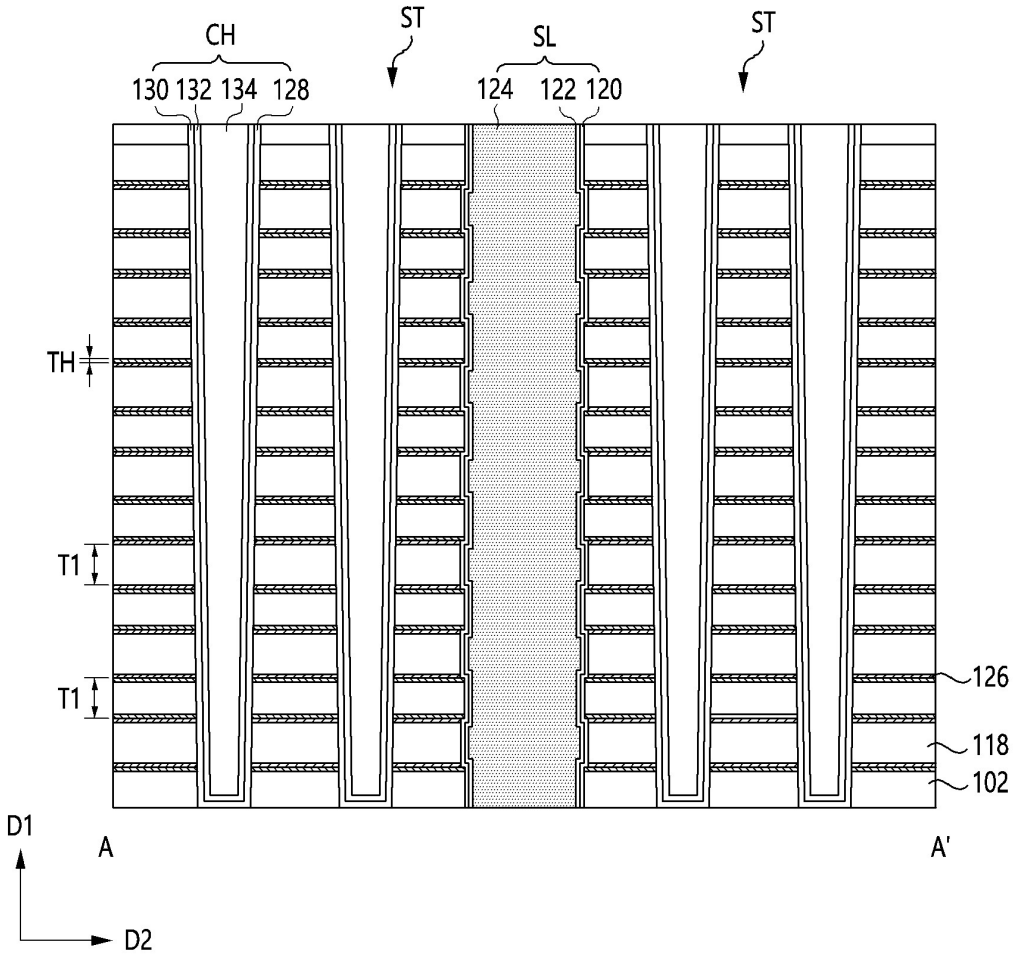

CH
ST
SL
ST
130 132 134 128
124 122 120
TH
T1
T1
126
118
102
D1
A
A'
D2

FIG. 2C

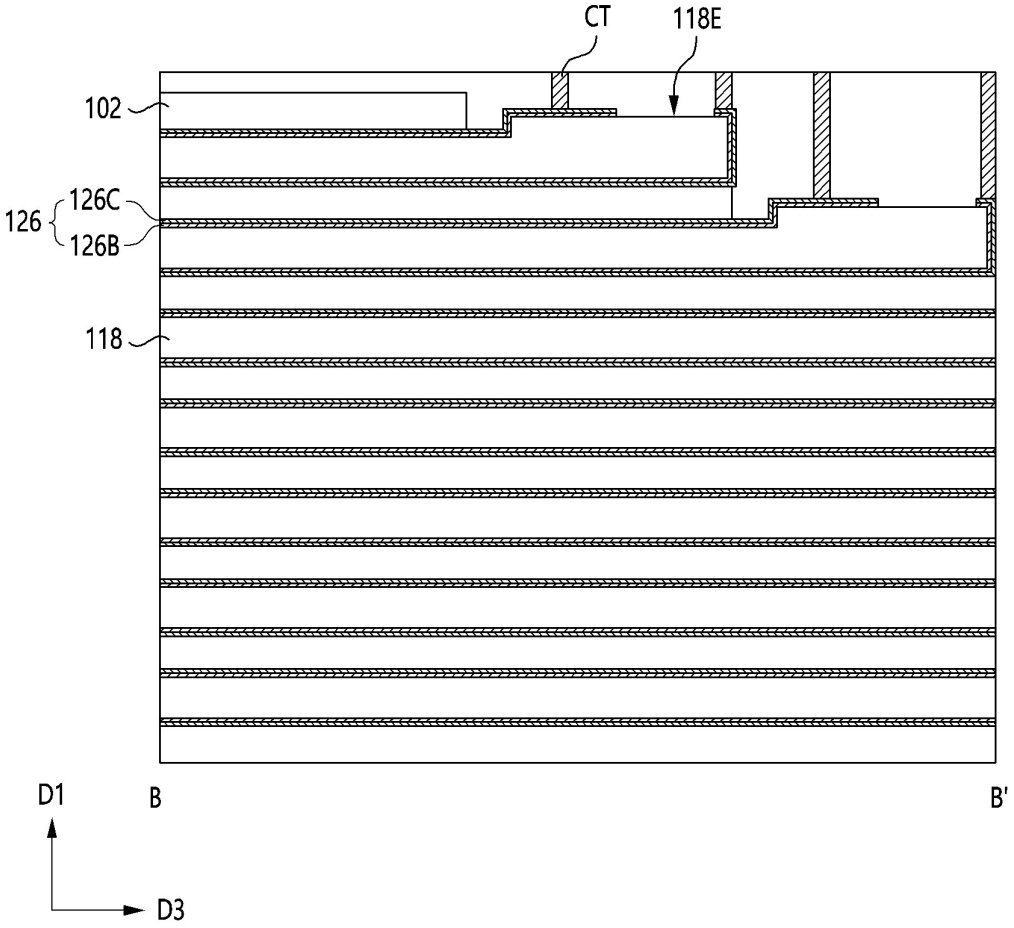


FIG. 3

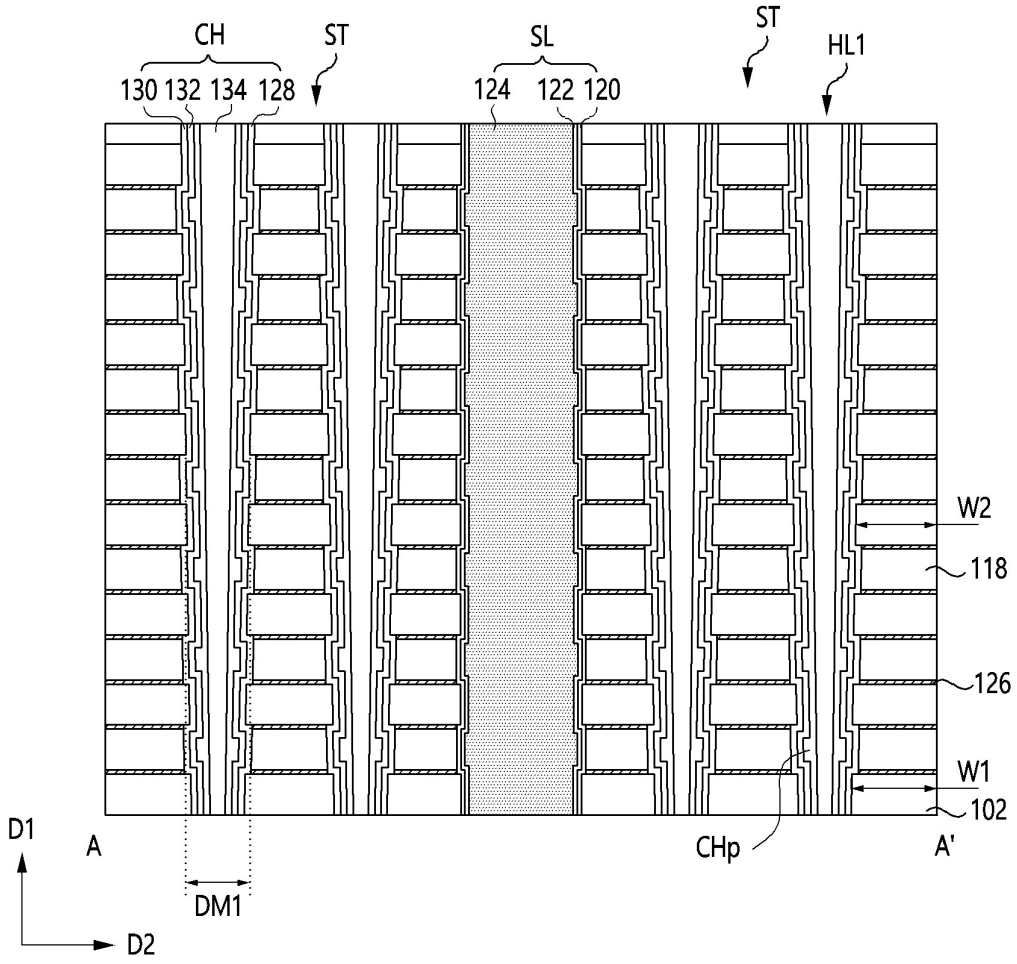

CH
ST
SL
ST
HL1
130 132 134 128
124 122 120
W2
118
126
W1
102
CHp
A
A'
DM1
D1
D2

FIG. 4A

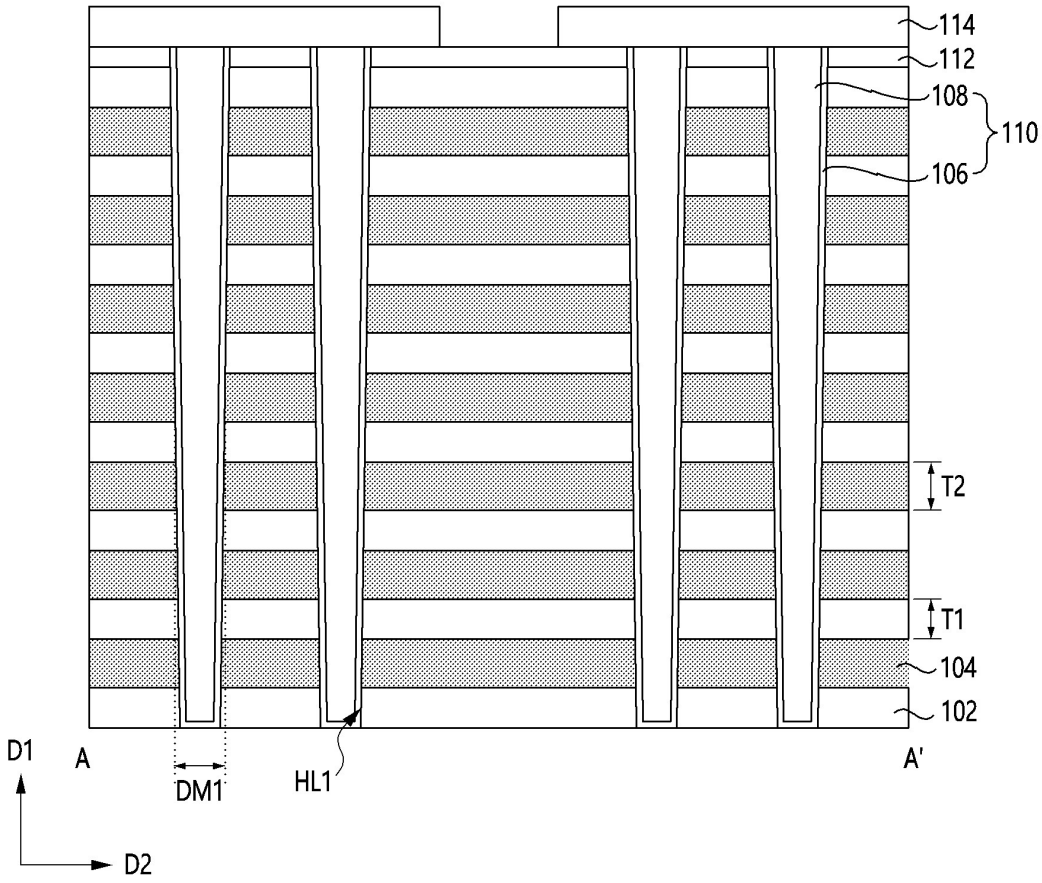


FIG. 4B

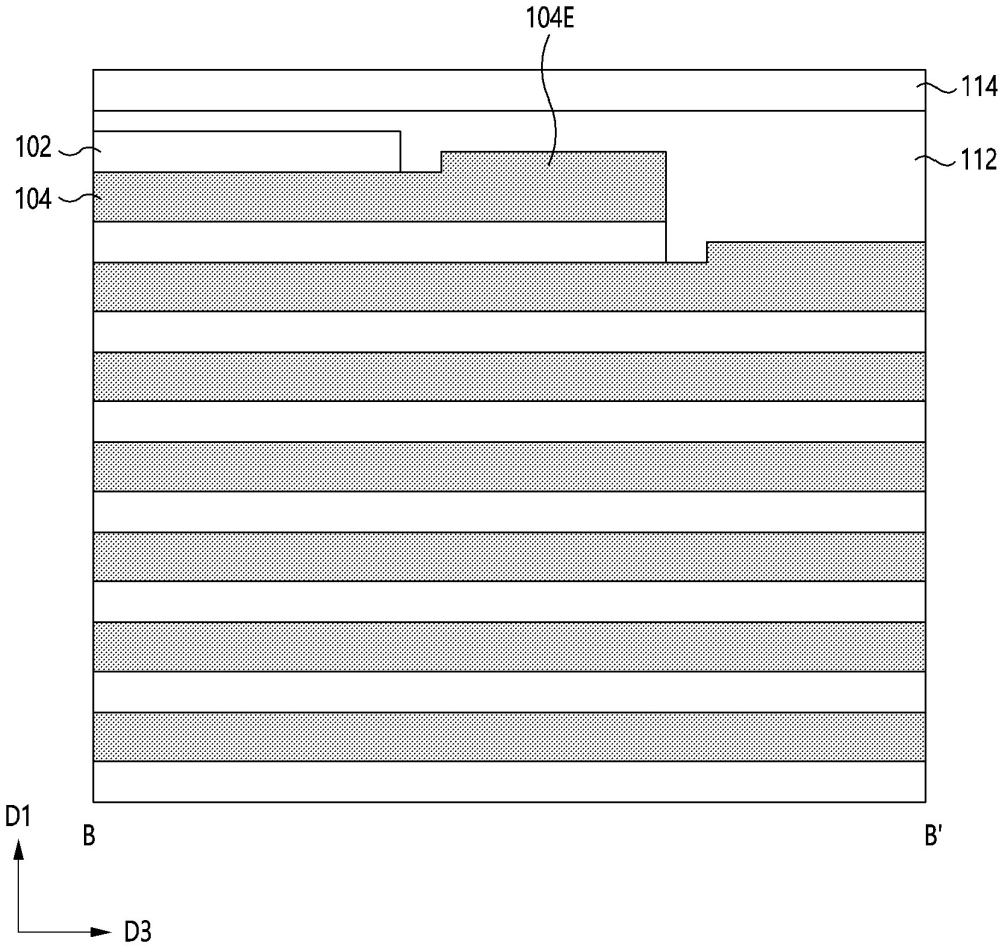


FIG. 5

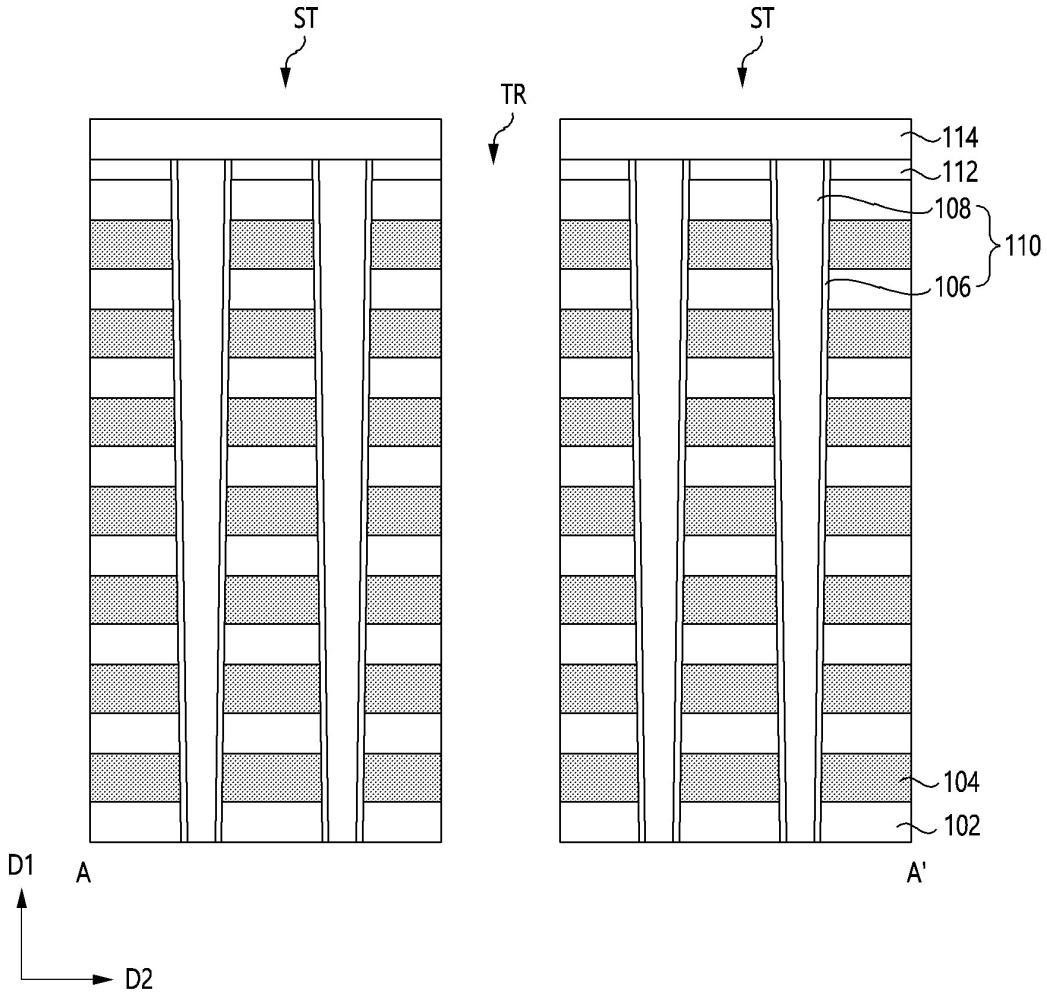


FIG. 6A

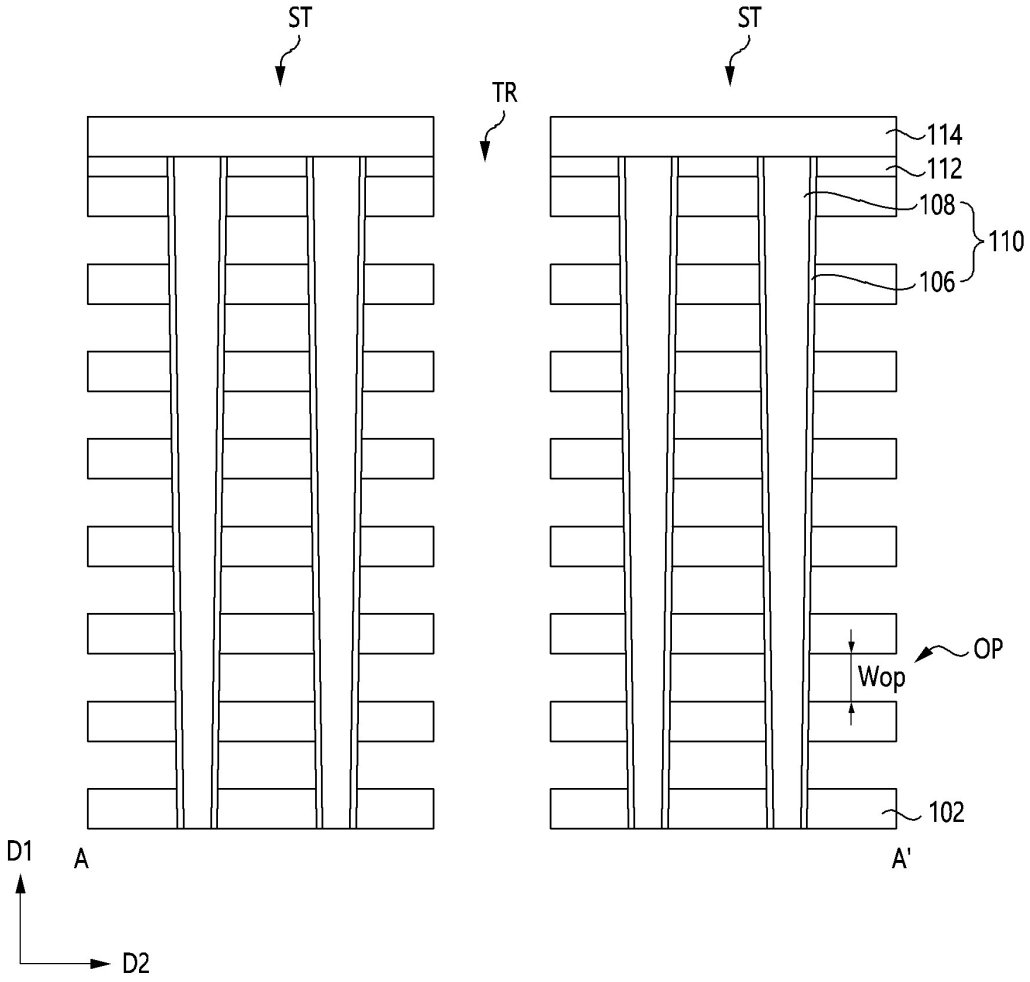


FIG. 6B

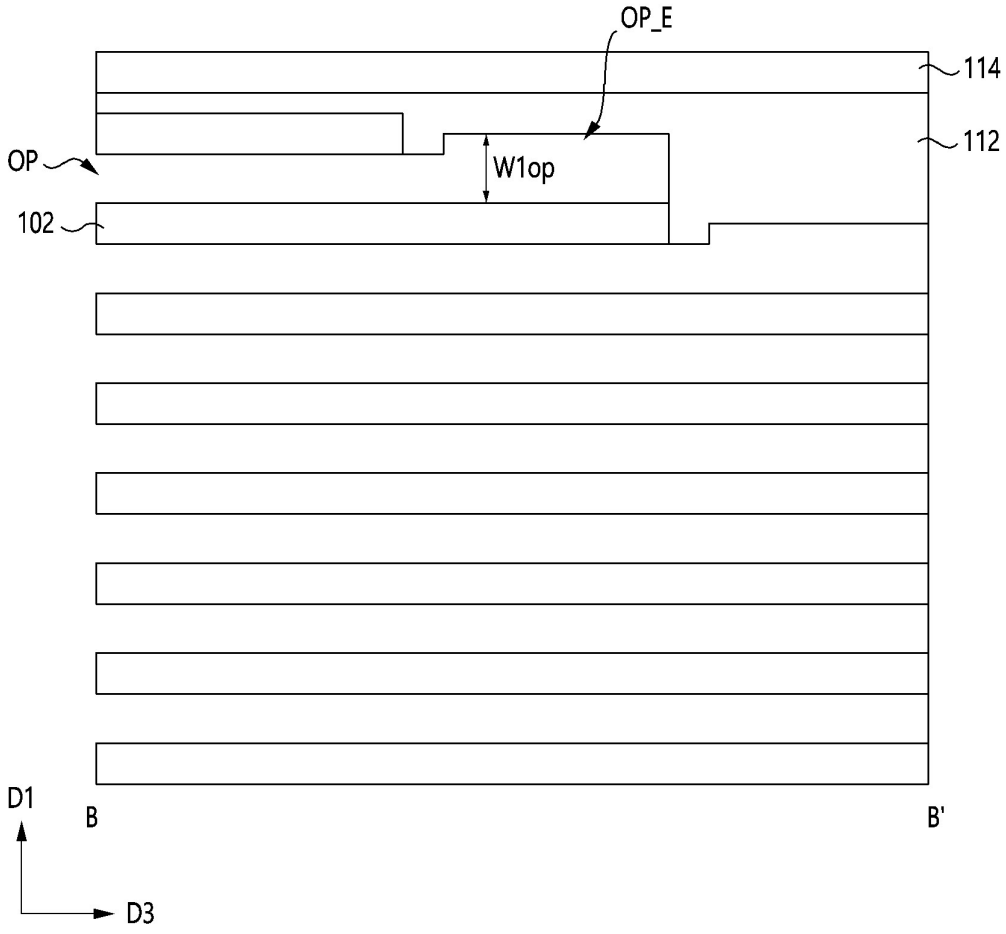


FIG. 7A

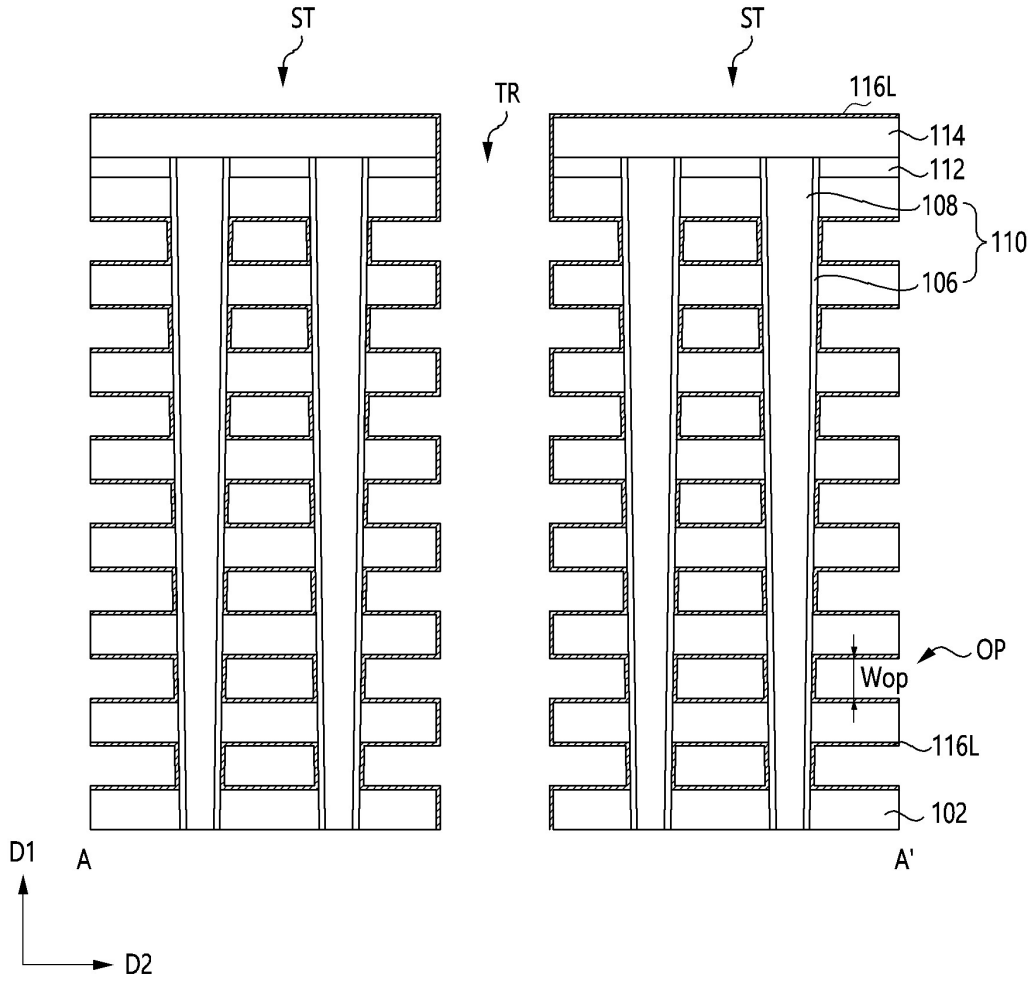


FIG. 7B

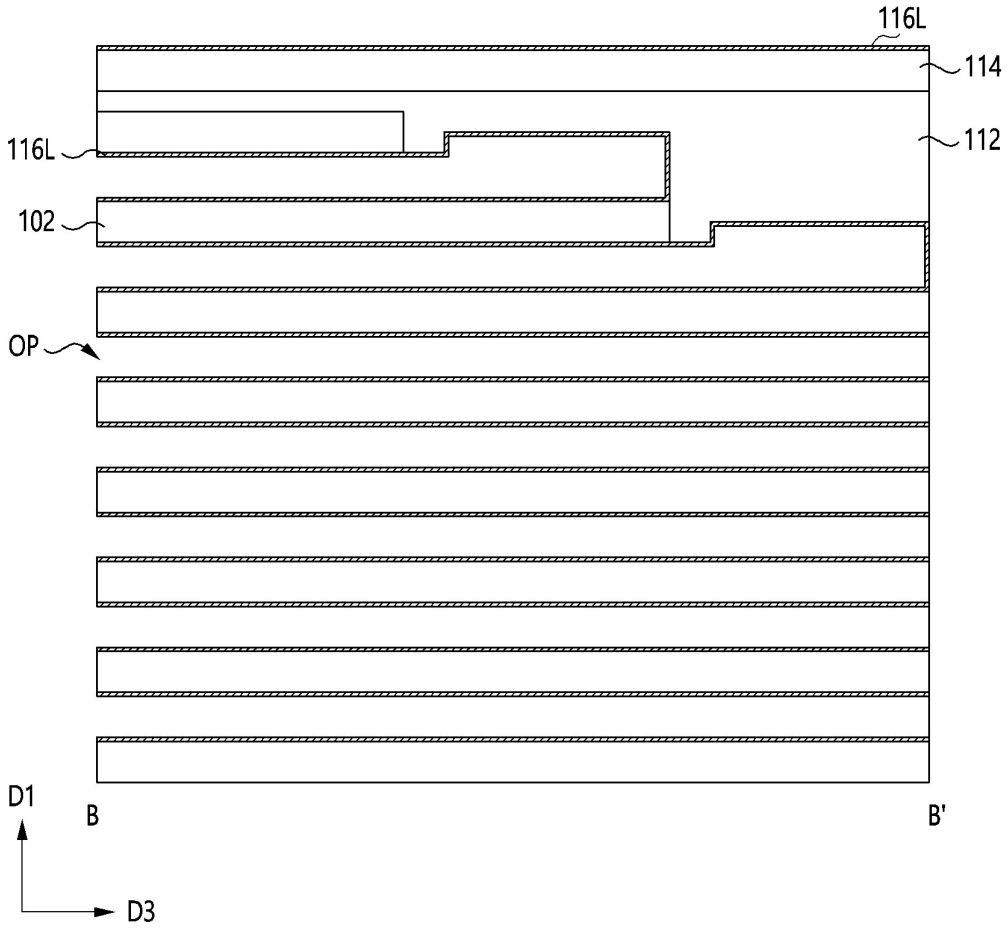


FIG. 8A

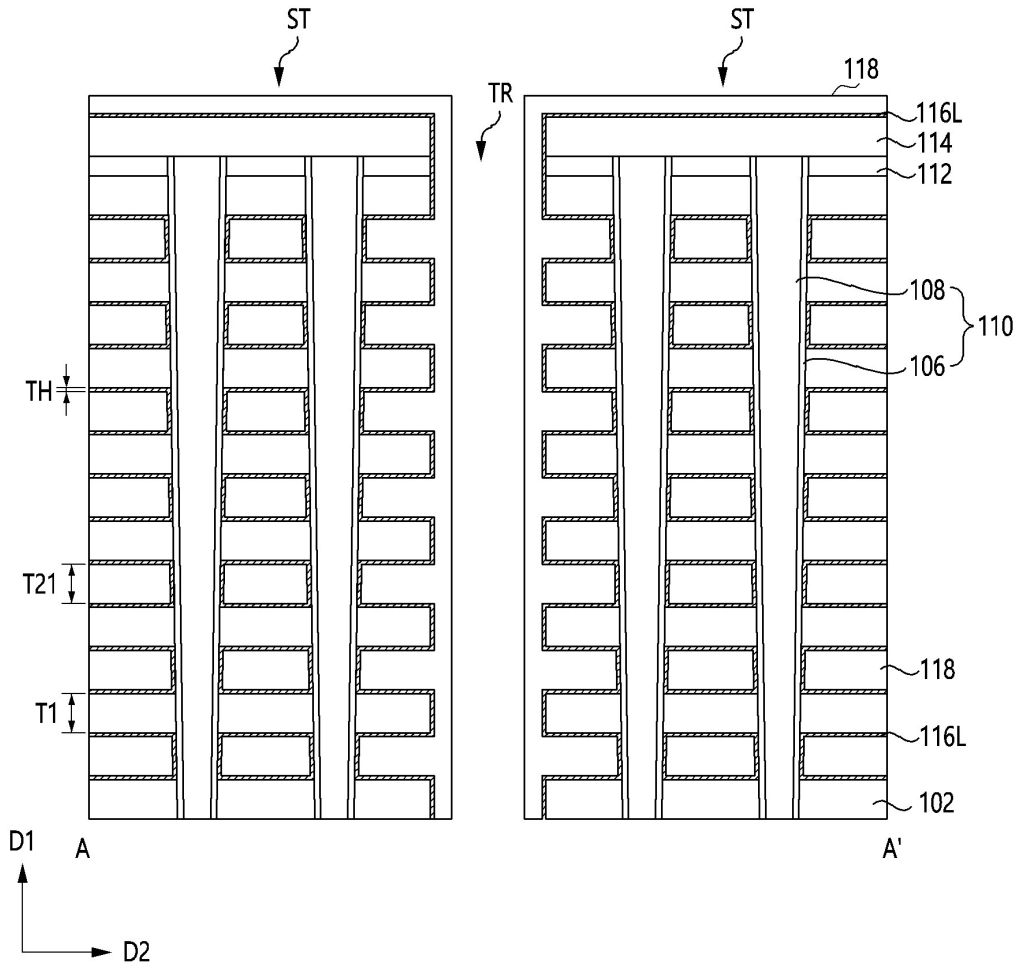

ST
ST
TR
118
116L
114
112
108
110
106
TH
T21
T1
118
116L
102
D1
A
A'
D2

FIG. 8B

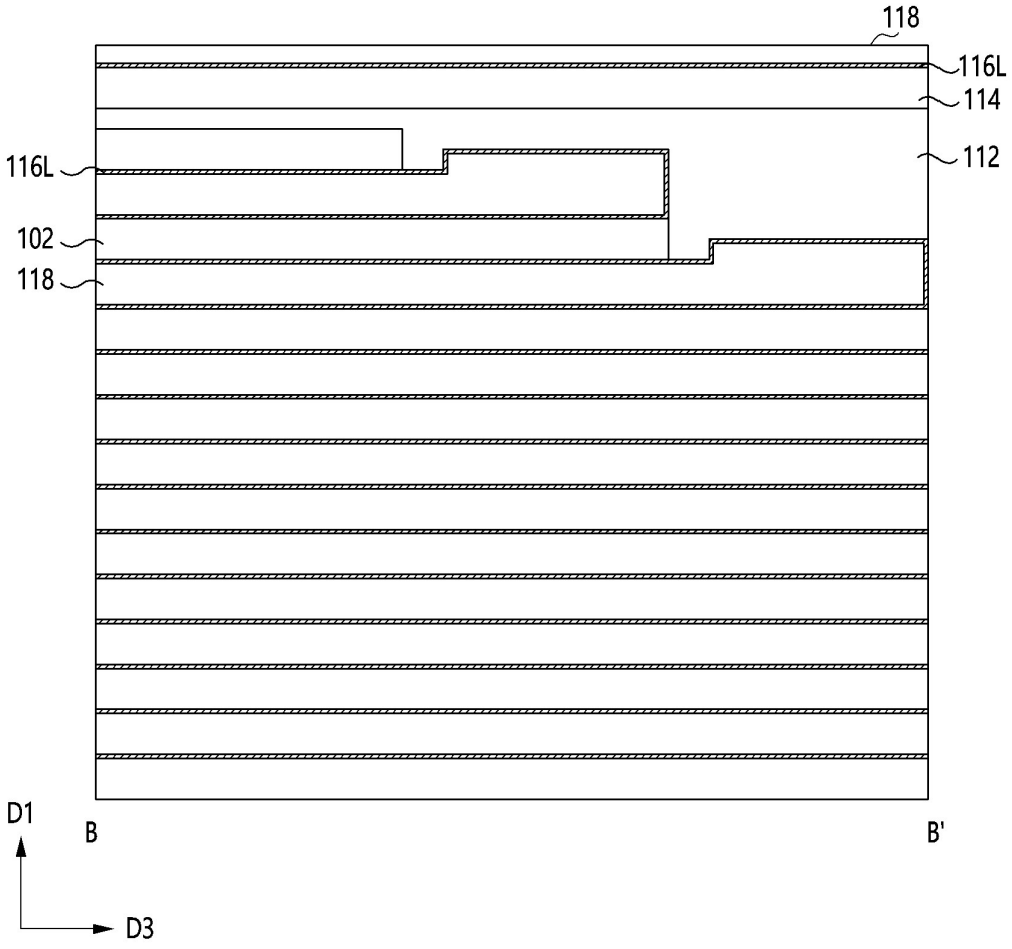


FIG. 9

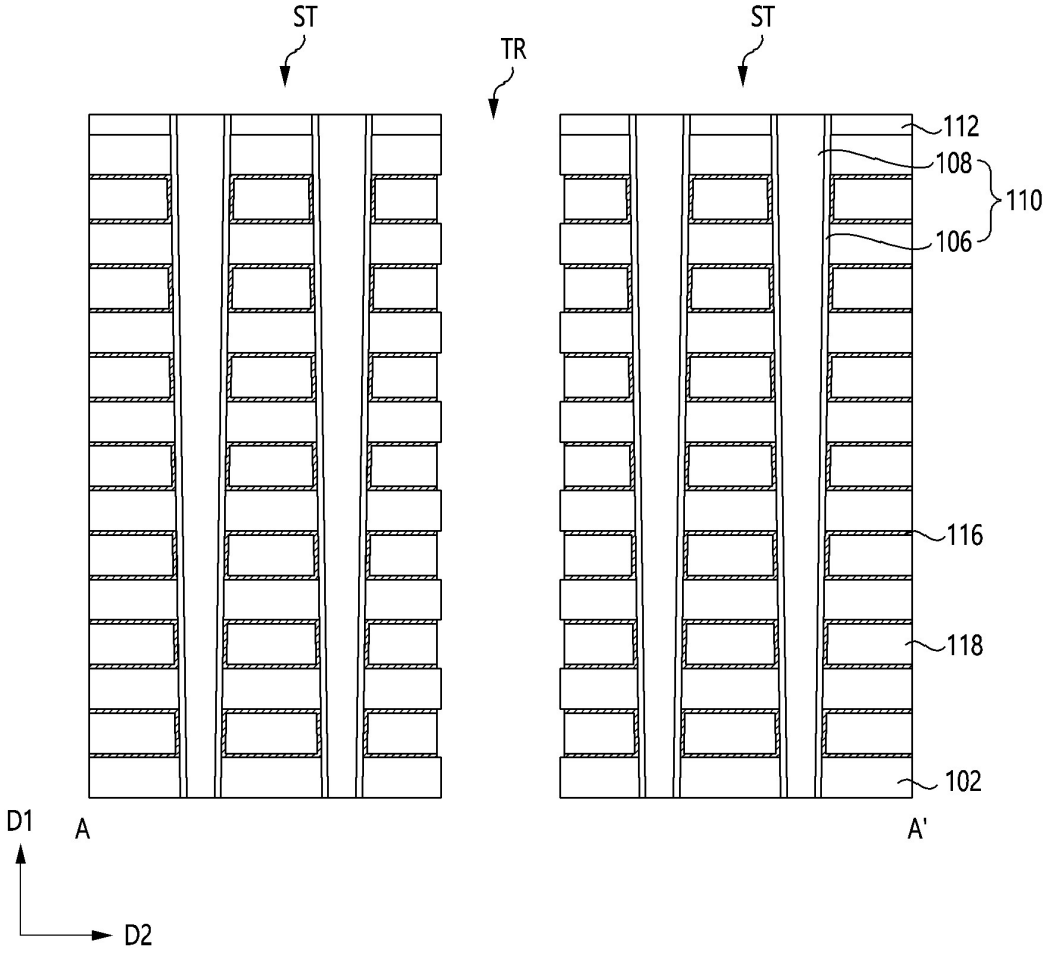


FIG. 10

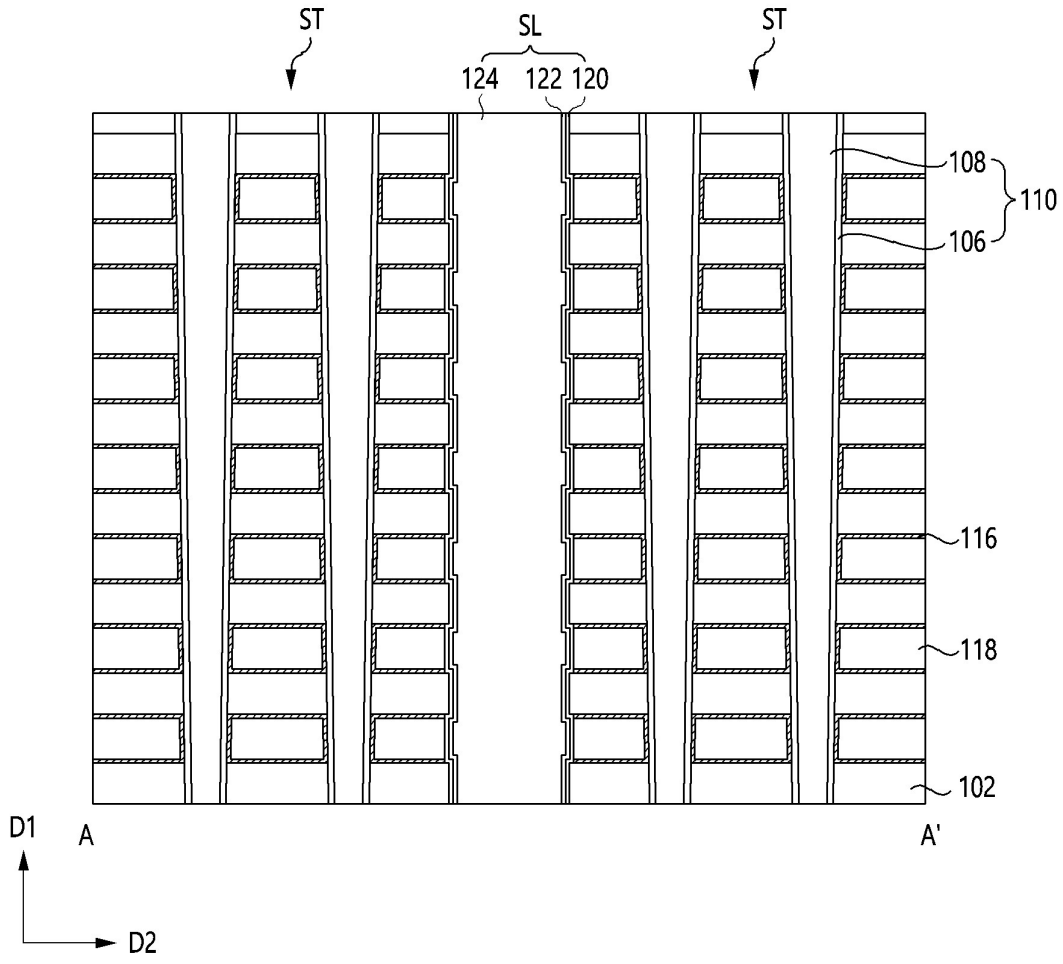


FIG. 11

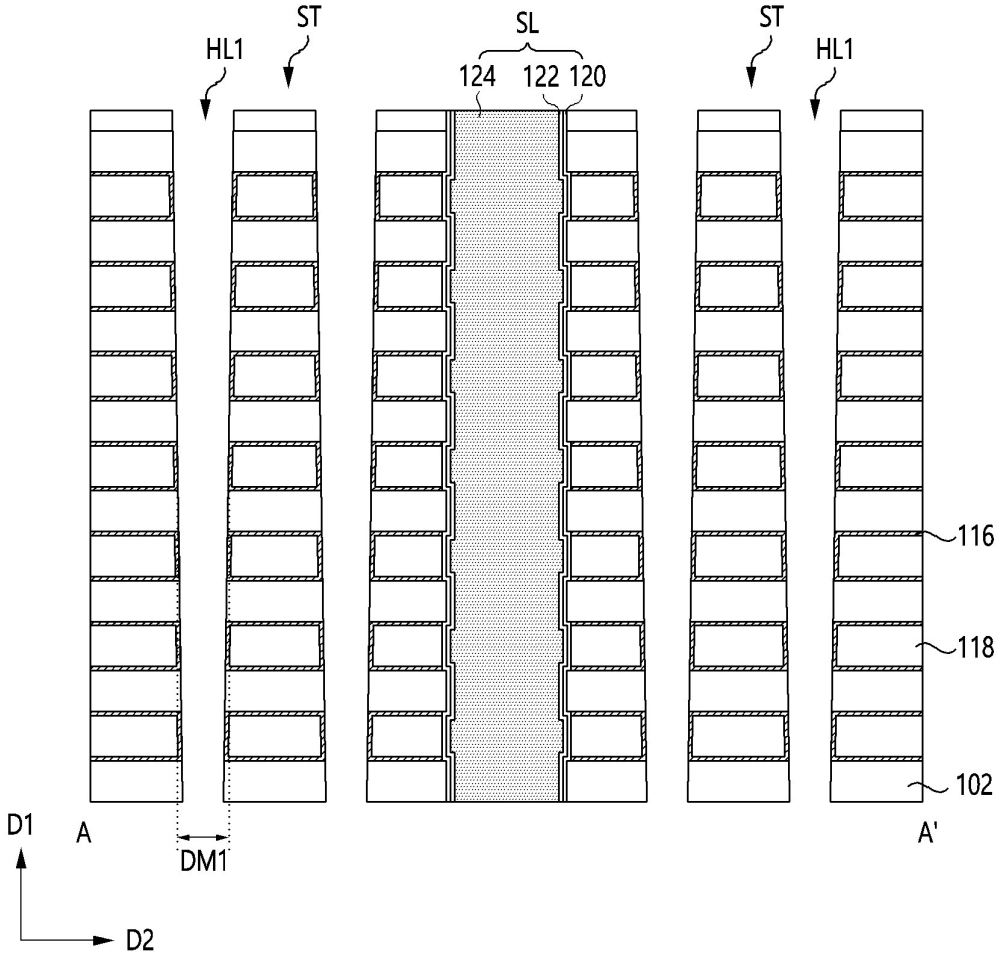


FIG. 12A

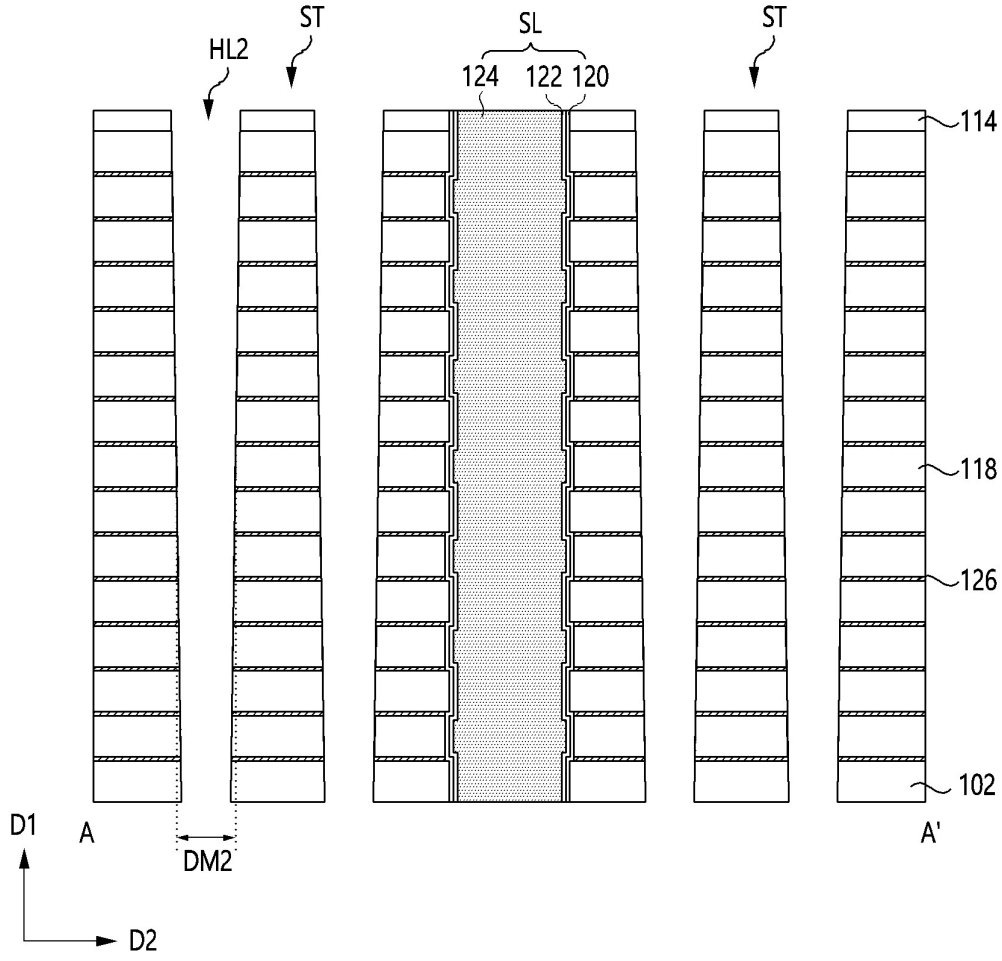


FIG. 12B

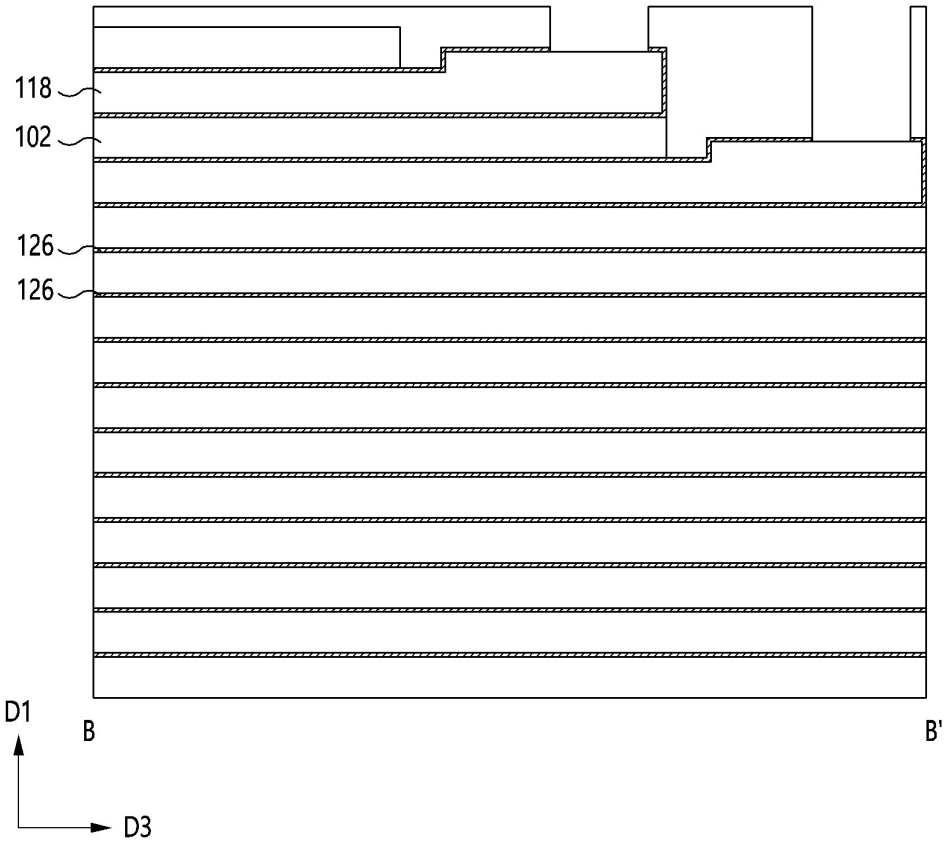


FIG. 13

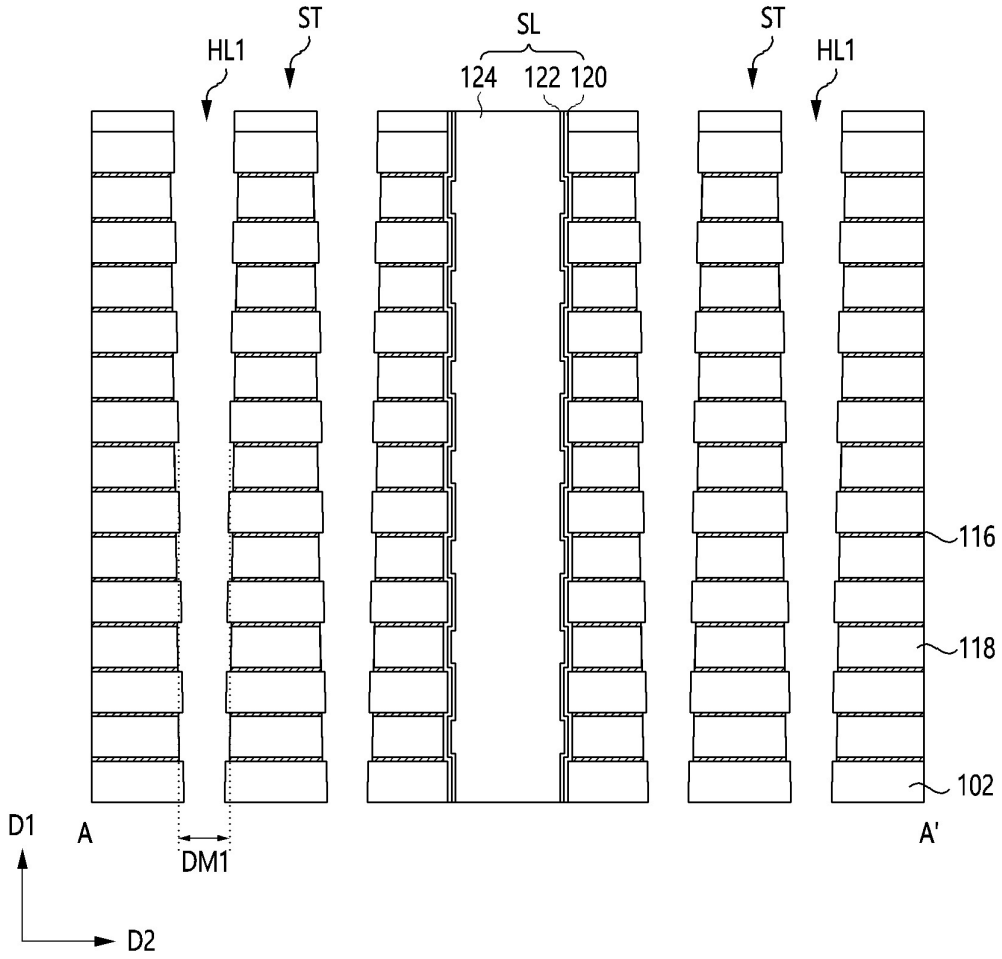


FIG. 14

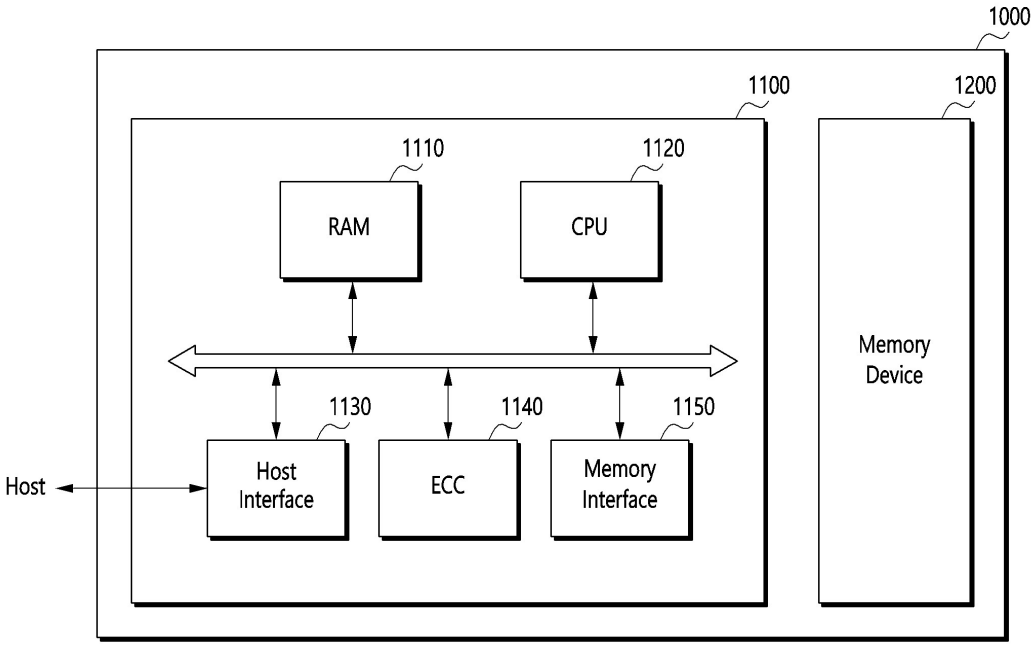

1000
1100
1200
1110
1120
RAM
CPU
Memory
Device
1130
1140
1150
Host
Host
Interface
ECC
Memory
Interface

FIG. 15

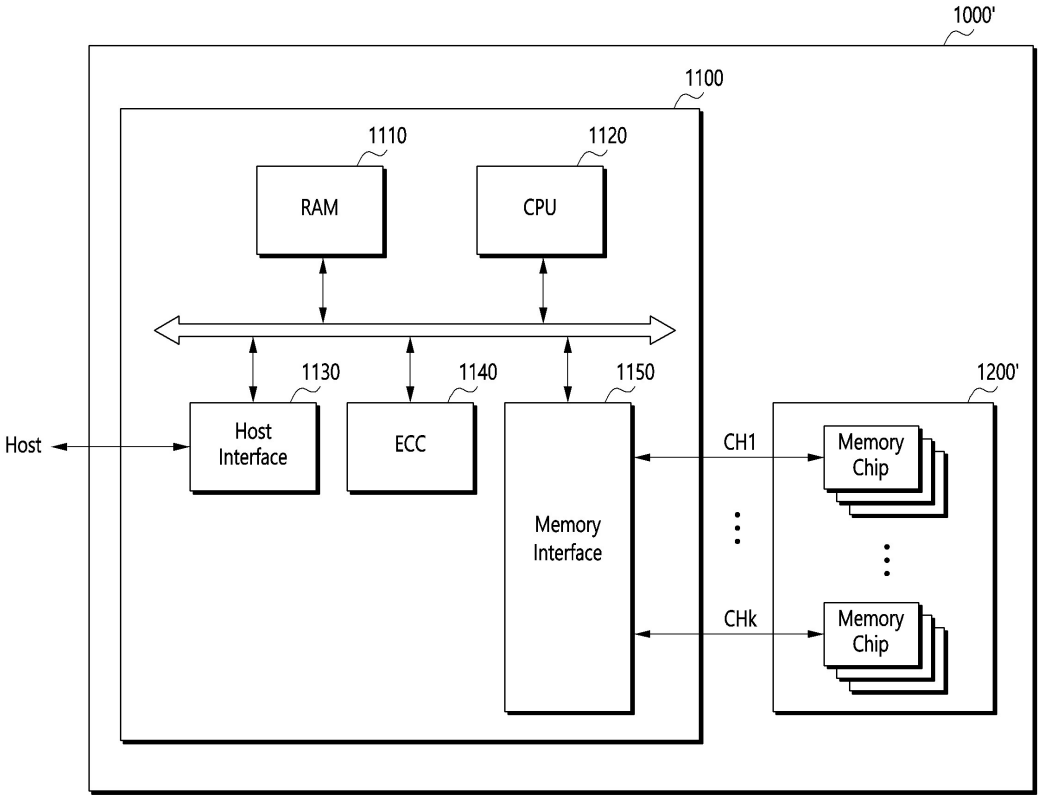

1000'
1100
1110
1120
RAM
CPU
1130
1140
1150
1200'
Host
Host Interface
ECC
Memory Interface
CH1
CHk
Memory Chip
Memory Chip

FIG. 16

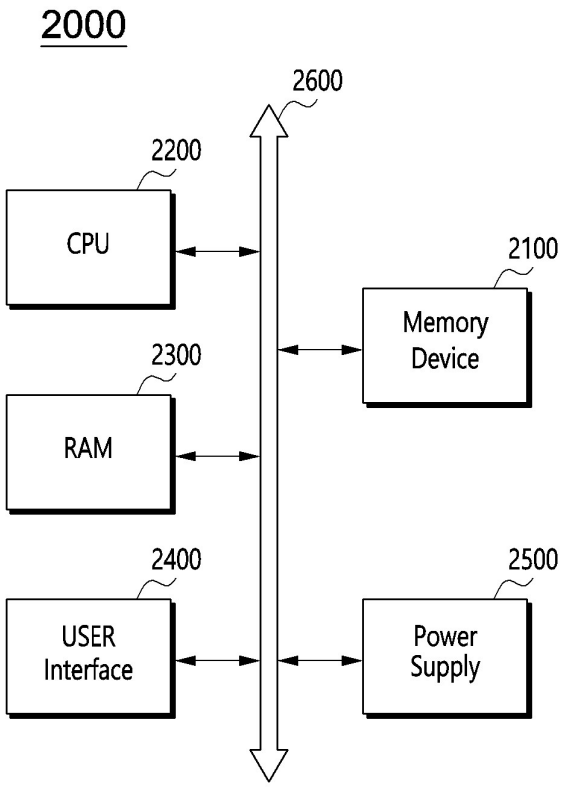
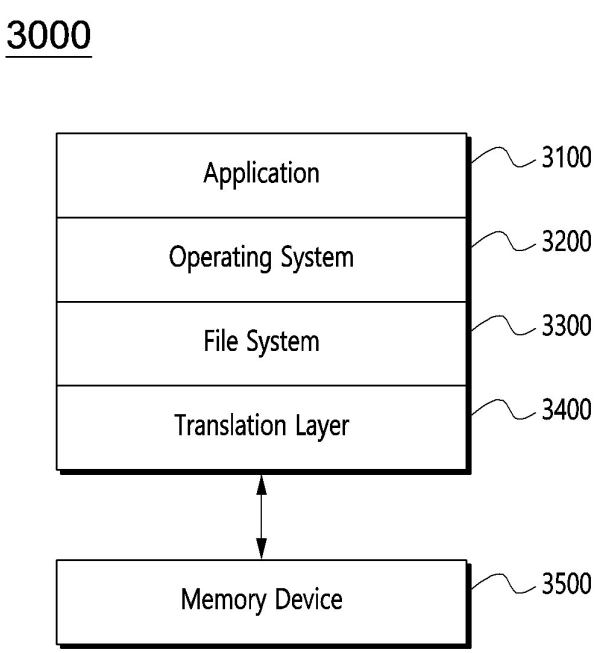

2000
2600
2200
CPU
2100
Memory Device
2300
RAM
2400
USER Interface
2500
Power Supply

FIG. 17


3000
Application
3100
Operating System
3200
File System
3300
Translation Layer
3400
Memory Device
3500