FIG. 1

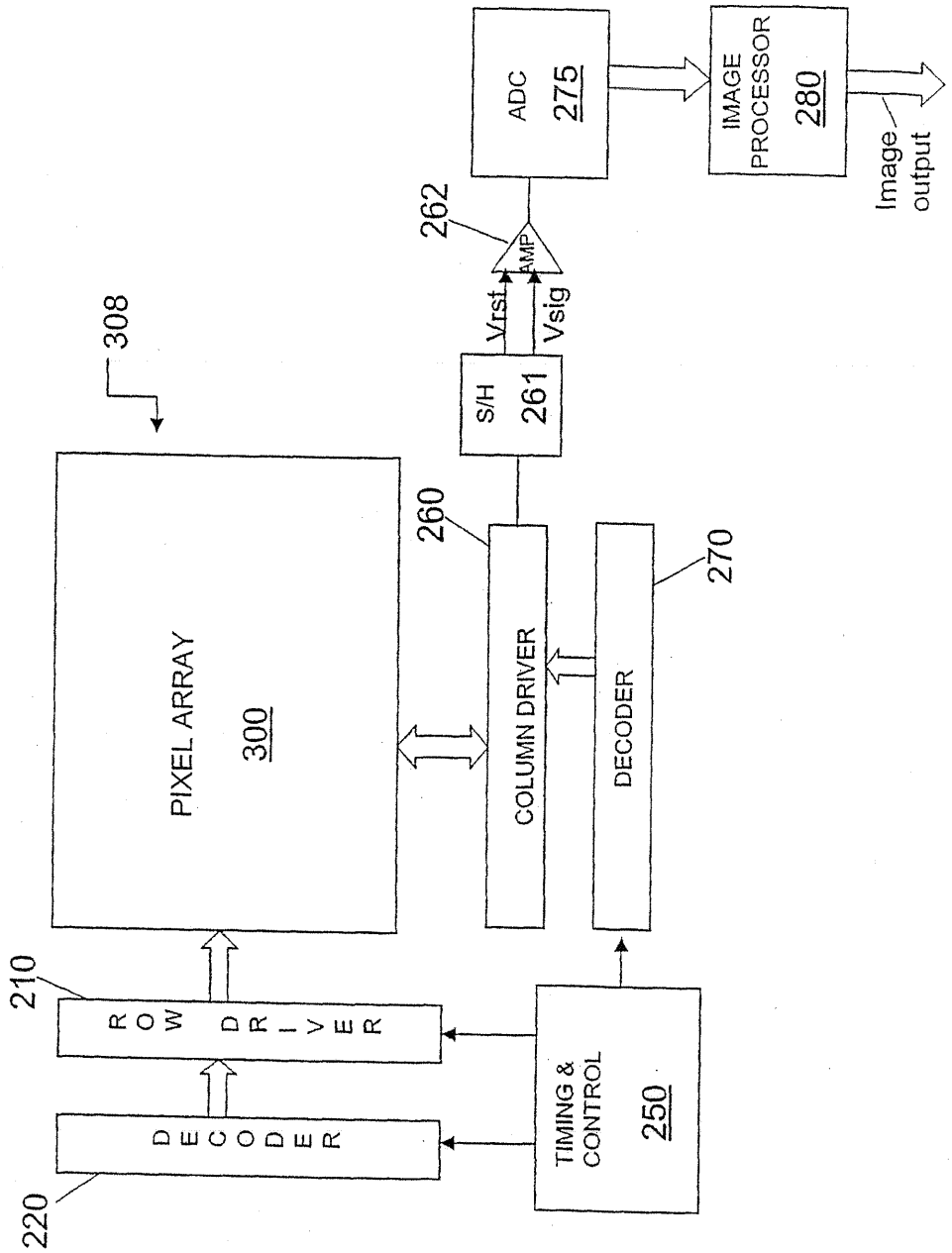

ADC
275
IMAGE PROCESSOR
280
Image output
262
AMP
Vrst
Vsig
S/H
261
308
PIXEL ARRAY
300
260
COLUMN DRIVER
DECODER
270
210
ROW DRIVER
DECODER
220
TIMING & CONTROL
250

FIG. 2

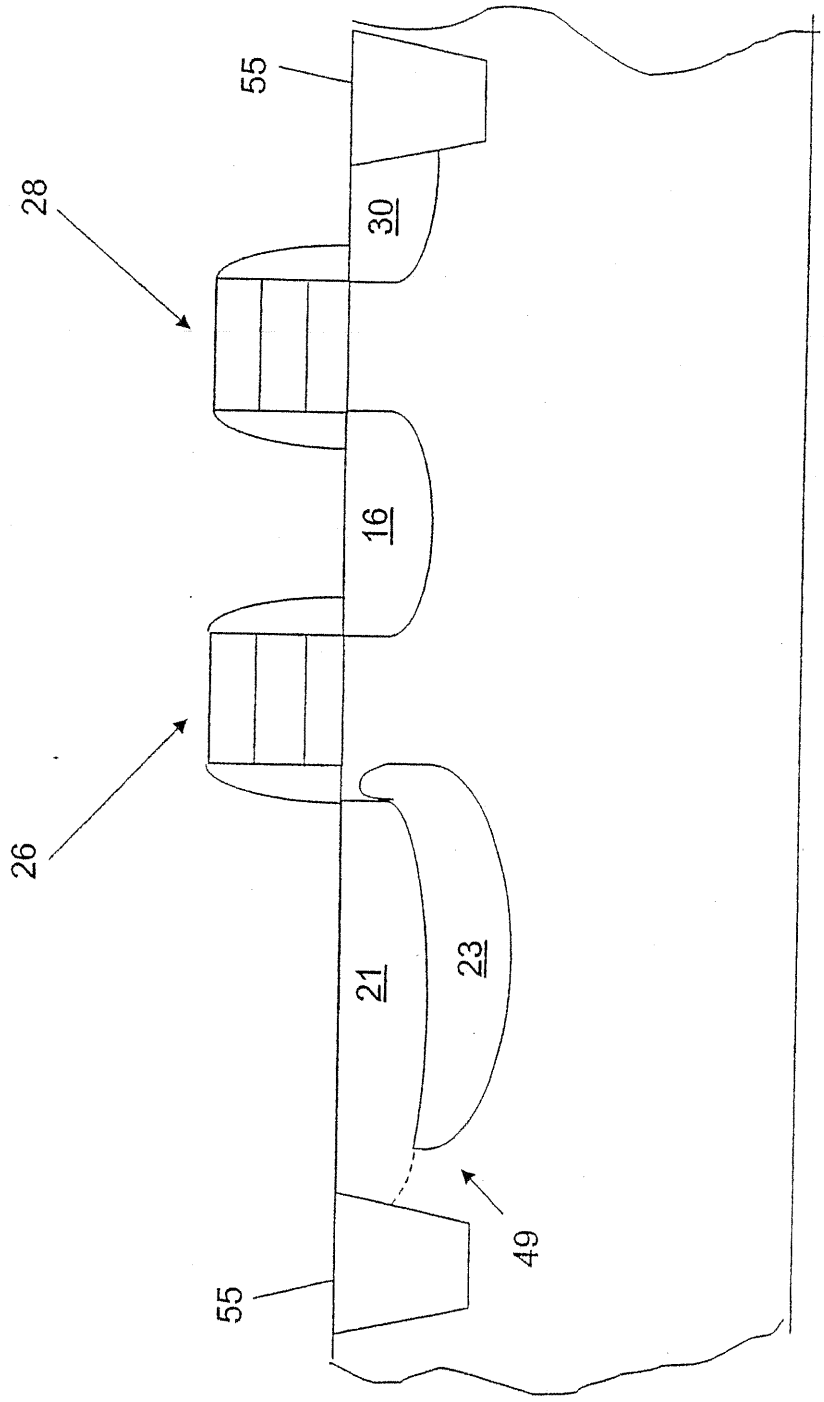


FIG. 3

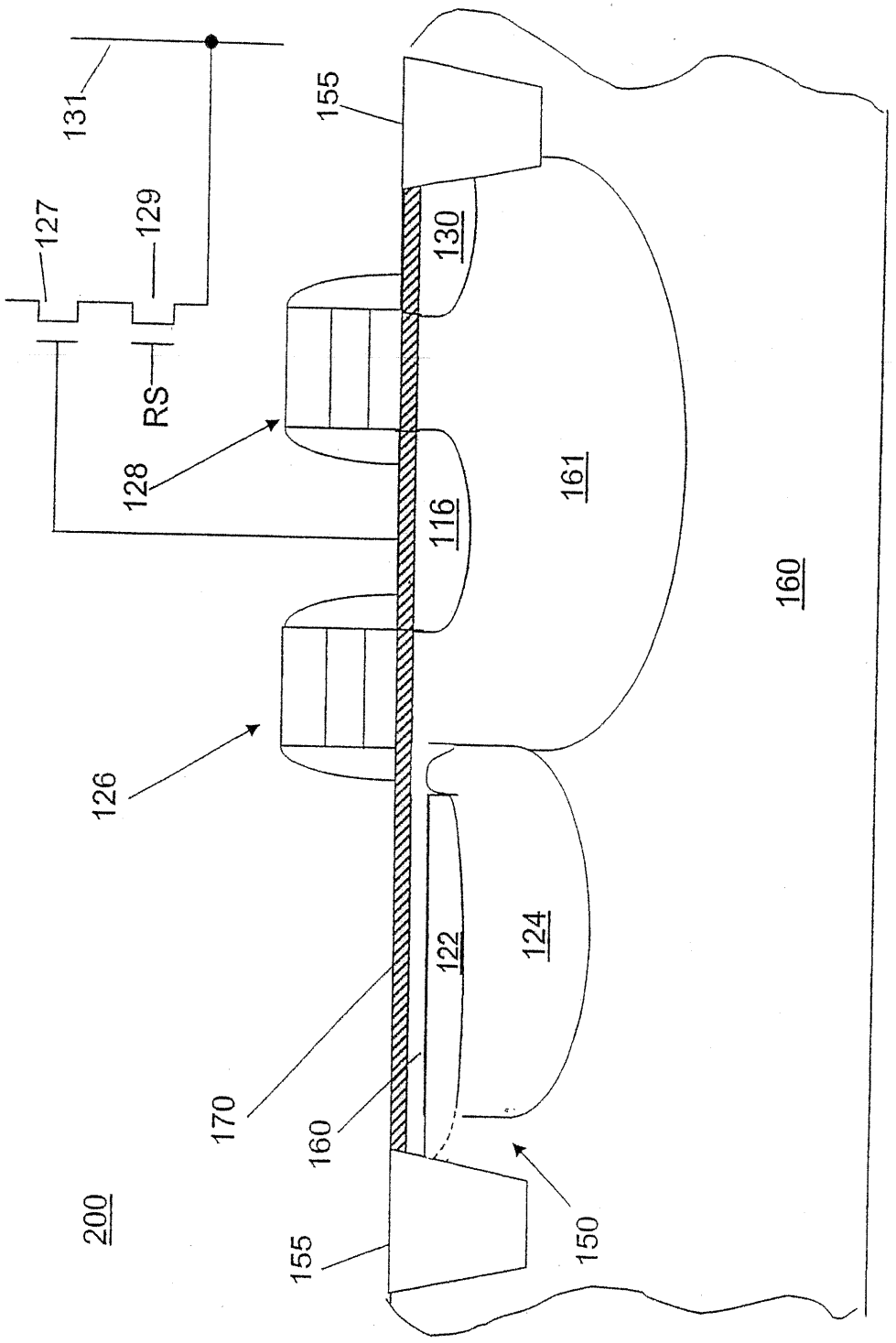

131
155
127
129
130
RS
128
161
116
160
126
124
122
170
160
200
155
150

FIG. 4B

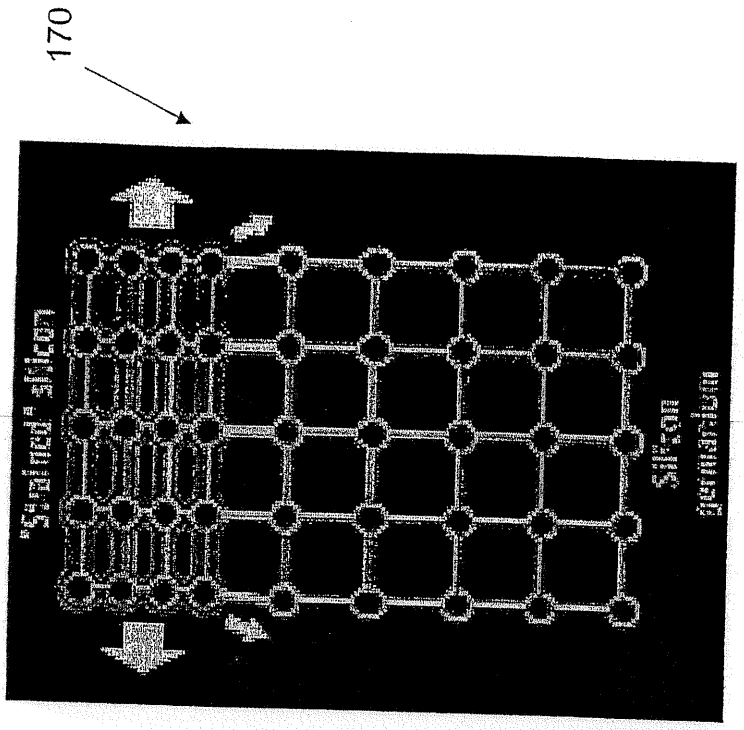

170
"Strained" silicon
Silicon germanium

FIG. 4A

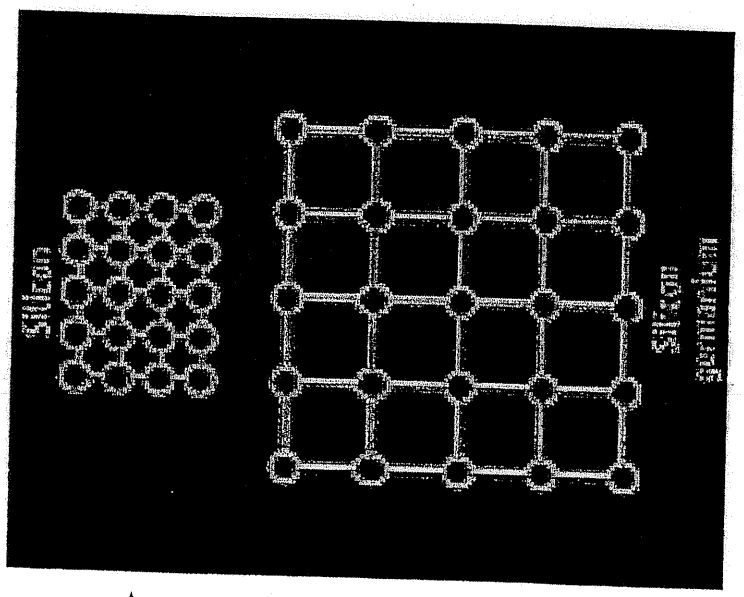

Silicon
Silicon germanium
174
172

FIG. 5

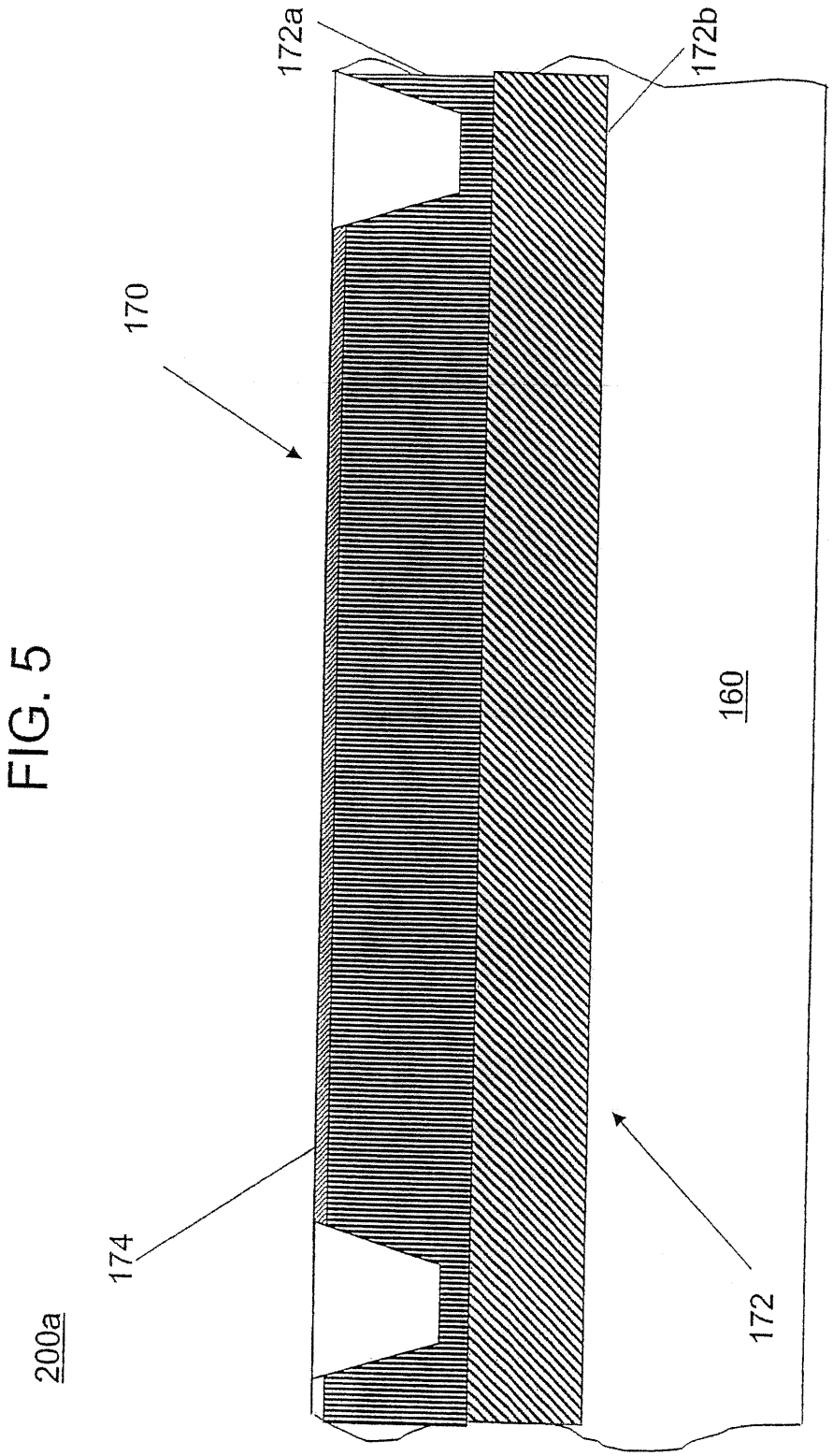


FIG. 6

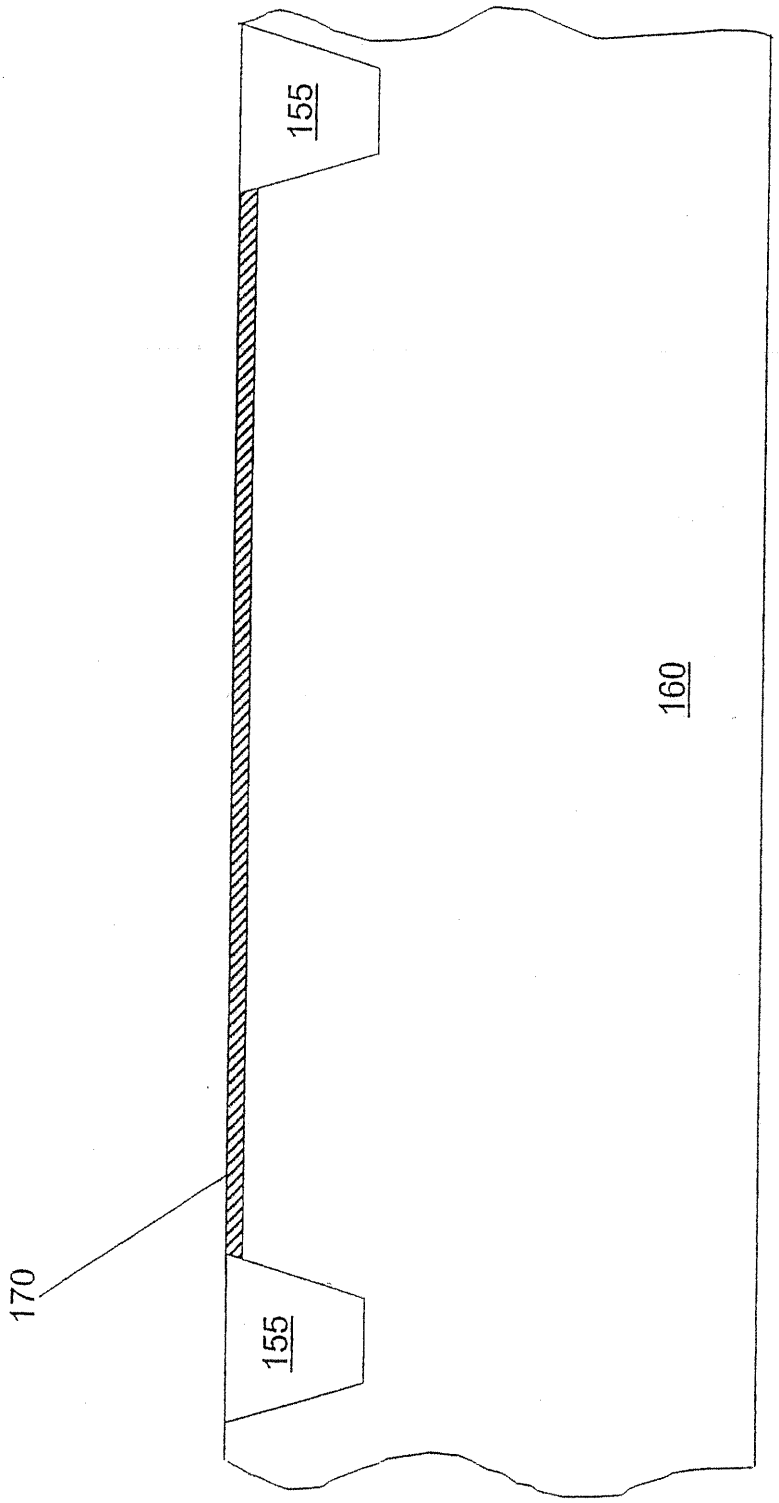


FIG. 7

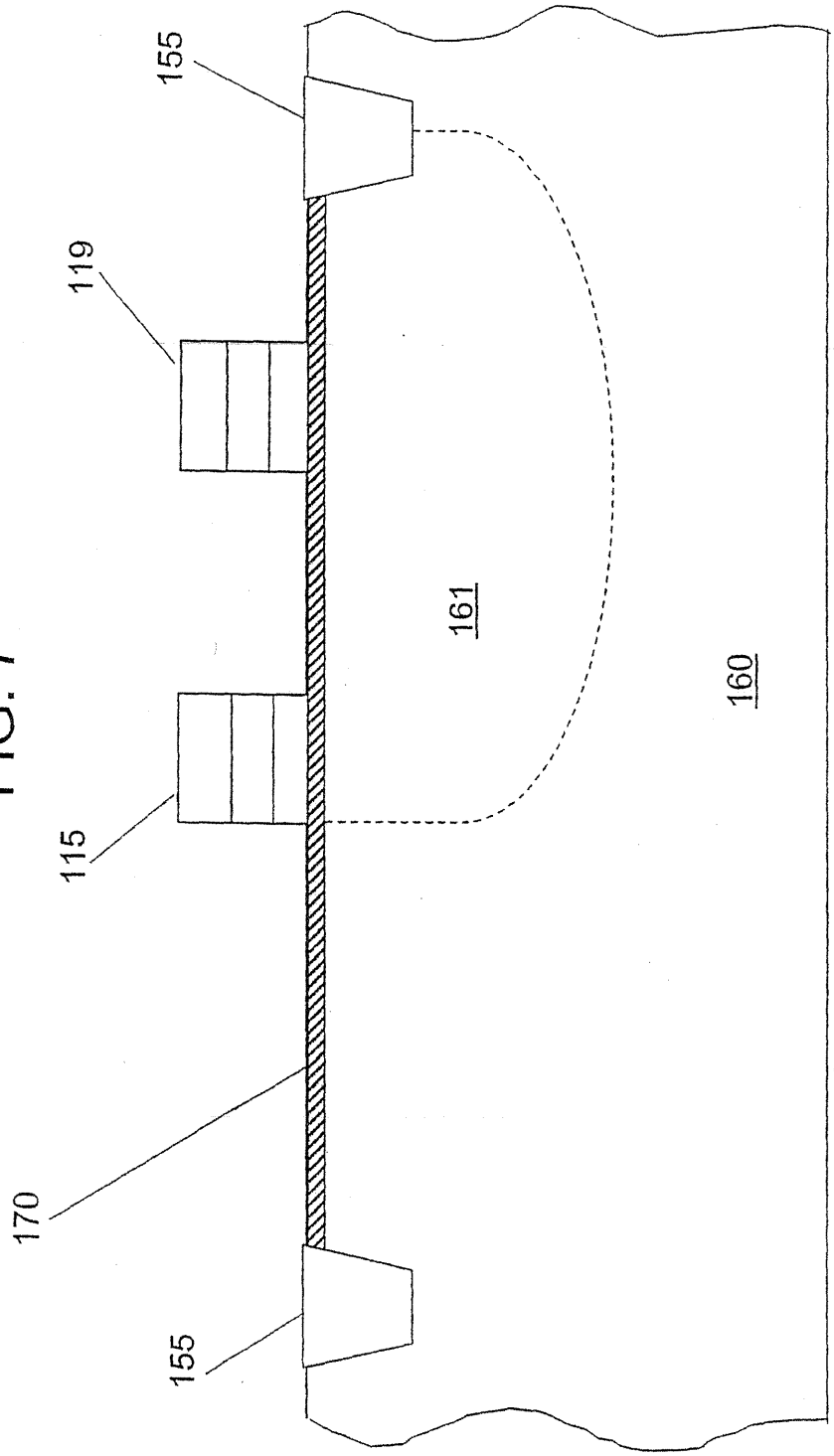


FIG. 8

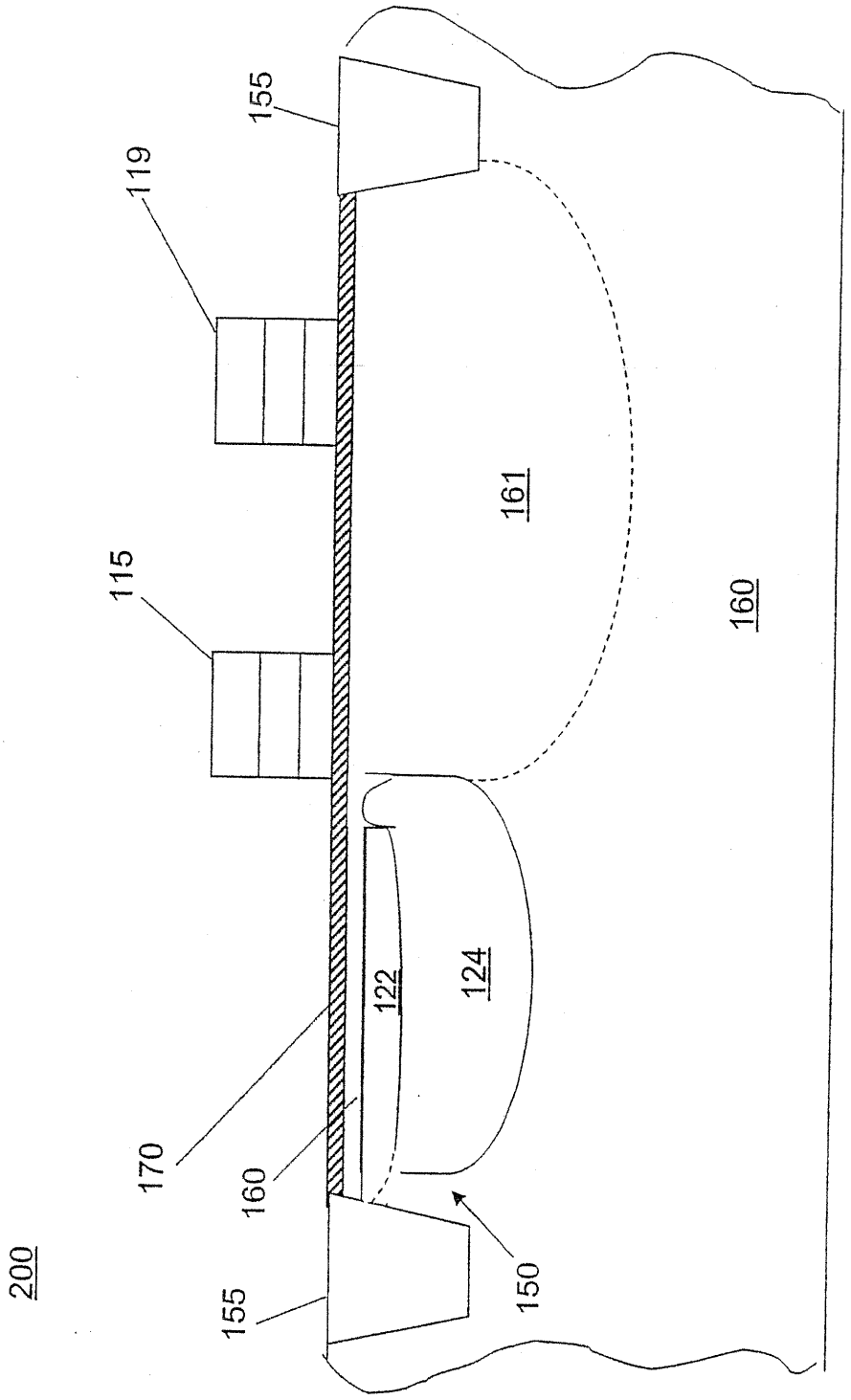
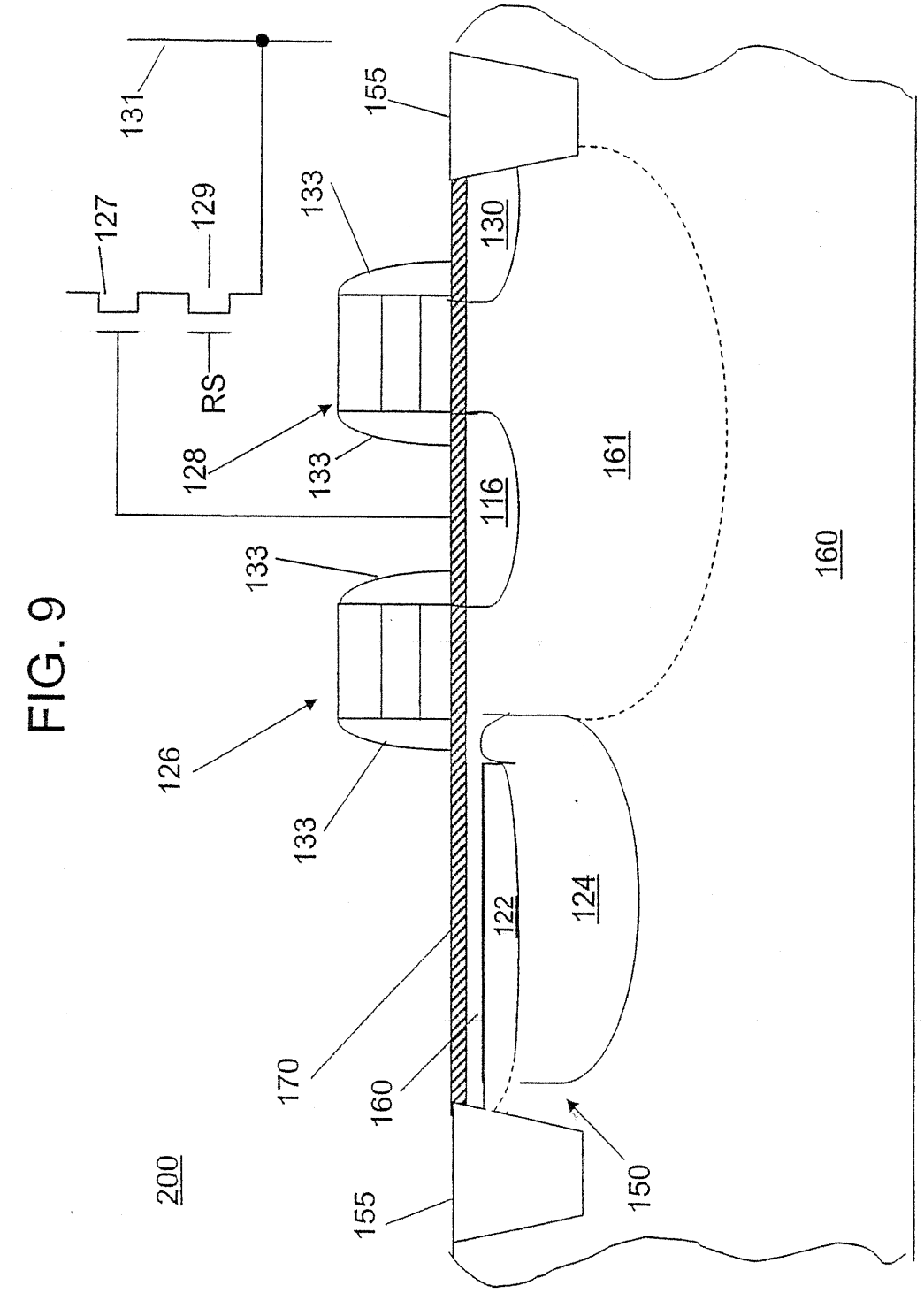


FIG. 9


131
127
129
RS
128
133
155
130
161
116
160
126
124
122
170
150
200

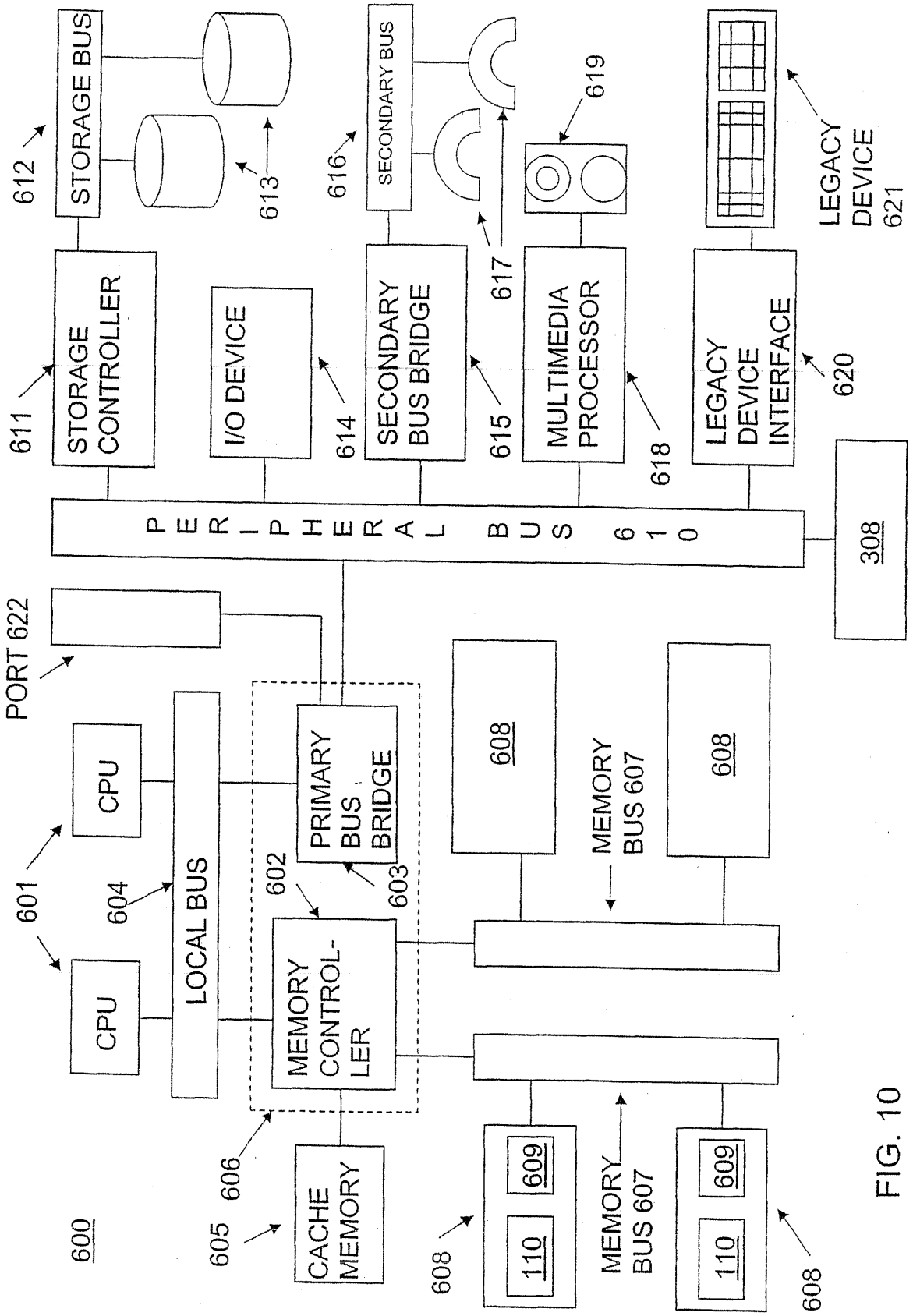

600
601
CPU
CPU
604
LOCAL BUS
602
603
PRIMARY BUS BRIDGE
MEMORY CONTROL-LER
606
605
CACHE MEMORY
608
608
608
609
110
609
110
MEMORY BUS 607
MEMORY BUS 607
PORT 622
PERIPHERAL BUS 610
308
611
STORAGE CONTROLLER
612
STORAGE BUS
613
I/O DEVICE
614
SECONDARY BUS BRIDGE
615
616
SECONDARY BUS
617
MULTIMEDIA PROCESSOR
618
619
LEGACY DEVICE INTERFACE
620
LEGACY DEVICE 621

FIG. 10