

Inst0	src1	src2		rd1	rd2	
Inst1	src1	src2		rd1	rd2	
Inst2	src1	src2		rd1	rd2	
Inst3	src1	src2		rd1	rd2	

FIG 1

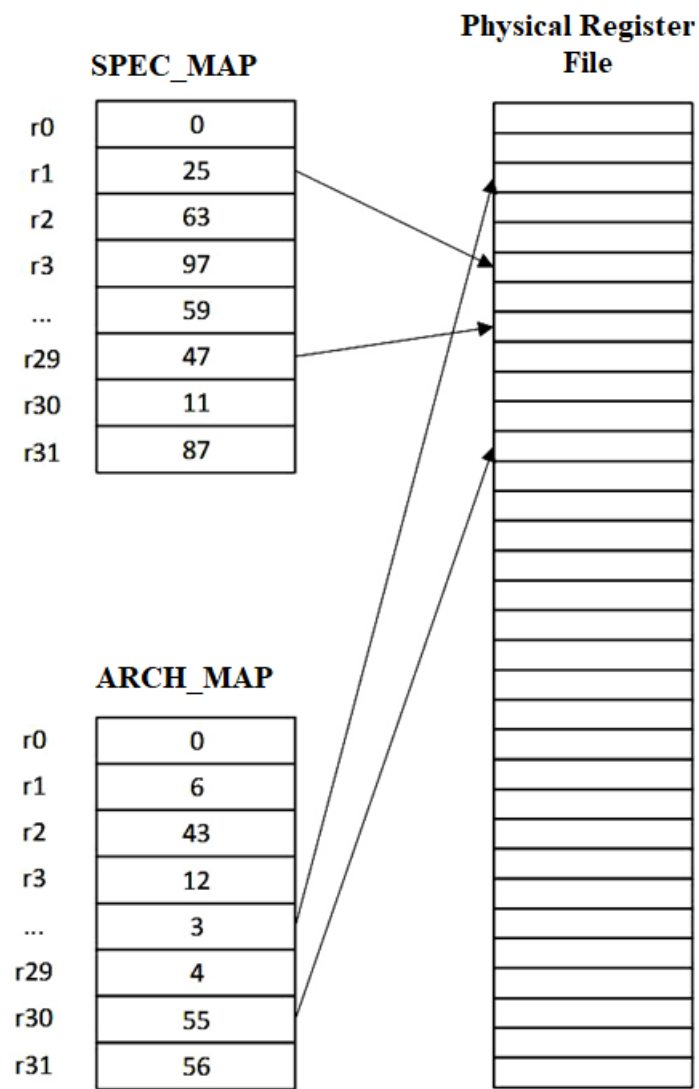


FIG 2

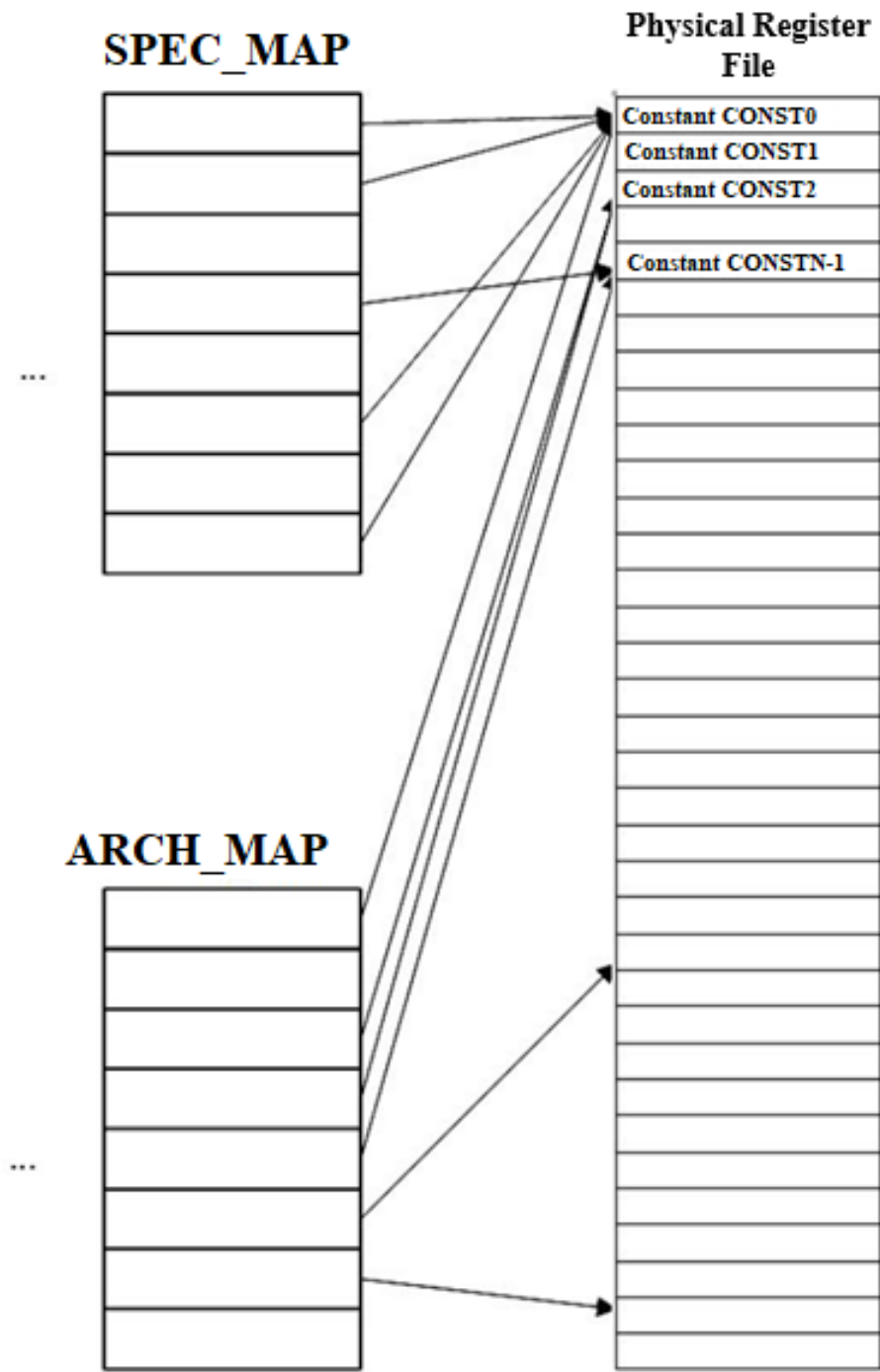


FIG 3

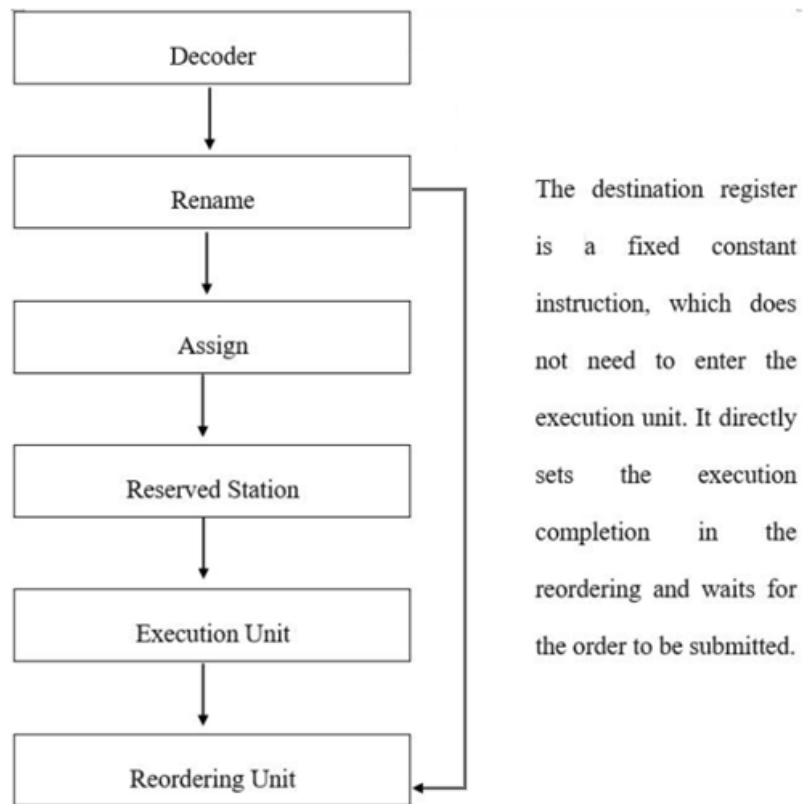


FIG 4

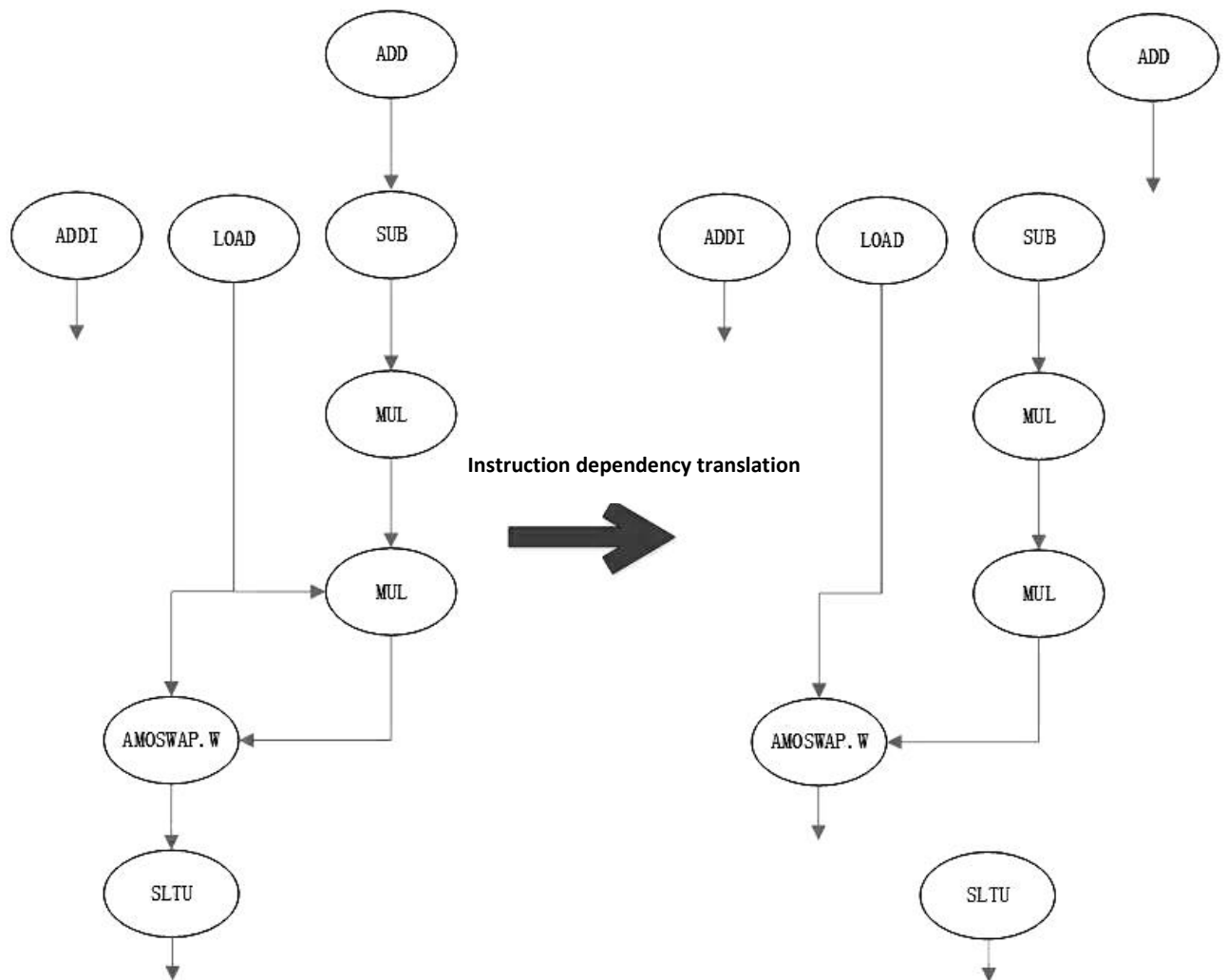


FIG 5

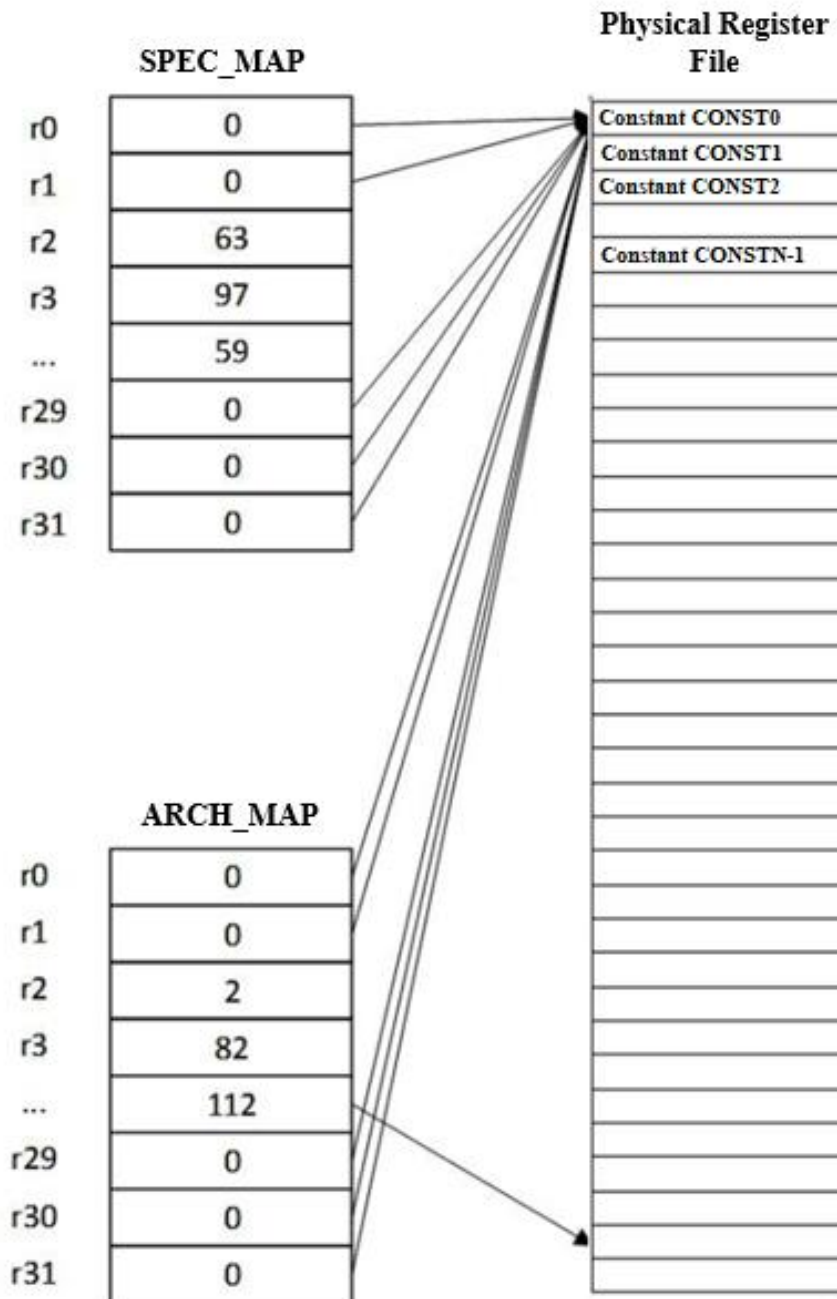


FIG 6