

(Abstract)

(Title) ELECTRO-OPTICAL PANEL AND ELECTRONIC DEVICE

[Object] To provide an electro-optical panel which is capable of effectively exhibiting the features of the construction in which two transistor elements are used per pixel, and an electronic device using the electro-optical panel.

[Solving Means] A pixel includes a P-channel TFT 116p in which the gate electrode is connected to a scanning line 112a, and an N-channel TFT 116n in which the gate electrode is connected to a scanning line 112b. When the P-channel TFT 116p and the N-channel TFT 116n are turned on, the voltage of an image signal supplied to a data line 114 is written into a holding capacitor HC and a holding capacitor LC. The holding capacitor HC is connected to the drain electrode of the N-channel TFT 116n. Also, the holding capacitor HC is connected to the drain electrode of the P-channel TFT 116p via a wiring line L. Since the wiring line L is formed of a low-resistance material, the time constant when the image signal is written into the holding capacitor HC via the P-channel TFT 116p can be decreased.

[Selected Figure]

Fig. 4