The structure is, for example, an additively manufactured circuit carrier (e.g. a printed circuit board). According to embodiments, the printing process may be interrupted to add e.g. electronic components, and is continued precisely at the same location. Embodiments also allow a quality assurance process to verify the precision of the continued printing.

BRIEF DESCRIPTION OF THE FIGURES

The embodiments of the present invention will be better understood by reference to the following detailed description and by the accompanying drawings of the various embodiments, which, however, should not be construed as limiting the disclosure to the specific embodiments, but are for explanation and understanding only.

Fig. 1 shows a schematic view of an additively manufactured structure according to an embodiment of the present invention.

Figs. 2A-2C show further embodiments for opaque structures and for 2-dimensional position determination.

Fig. 3 shows a more detailed illustration of a vernier grid according to further embodiments.

Fig. 4 shows a possible arrangement of the vernier grids in the substructures.

Fig. 5 shows an embodiment for an electrical readout of the position accuracy.

Fig. 6 shows a schematic flow diagram for a process according to embodiments.

DETAILED DESCRIPTION

Fig. 1 shows a schematic view of an additively manufactured structure according to one embodiment, the structure comprising a plurality of substructures 10, 20, at least one electronic component 15, and a vernier-based position marker 30. The substructures 10, 20 comprise a first substructure 10 on which a second substructure 20 has been additively manufactured such that the electronic