

No-wire PCB with Integrated circuit and method for testing using the same

Abstract

5 A no-wire PCB used in an in-circuit test system for performing an in-circuit testing
on a device under test (DUT) is presented. The no-wire PCB comprises a first
plurality of electrical contact points on a first portion of its upper surface, which are
aligned with a plurality of double-ended probes of a no-wire fixture. The no-wire
PCB also includes at least one IC mounted on the second portion of its upper
10 surface, such that the electrical contacts thereof are connected to a third plurality
of electrical contact points on the second portion of its upper surface. A second
plurality of contact points on the lower surface of the no-wire PCB are aligned with
the contact pins of an in-circuit tester. Under the control of the in-circuit tester, the
IC of the no-wire PCB is used to program and test one or more active components
15 of the DUT.

Figure 1