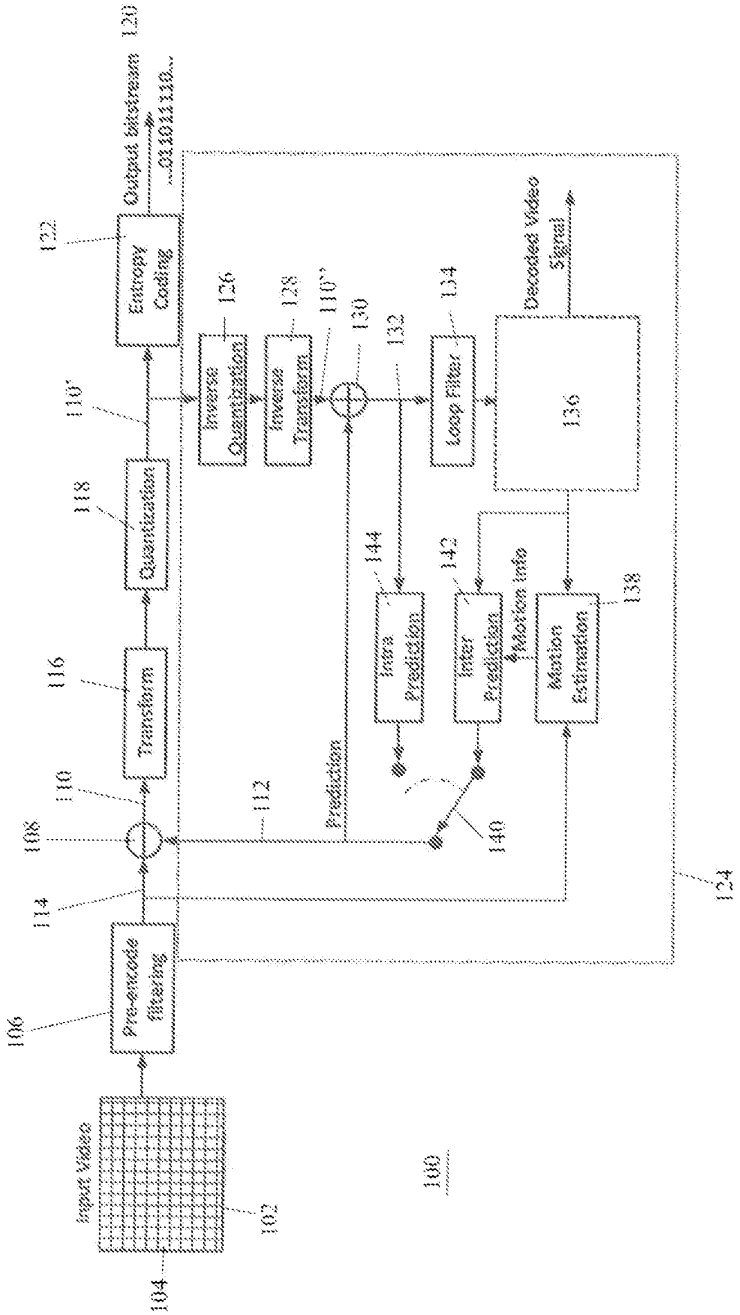
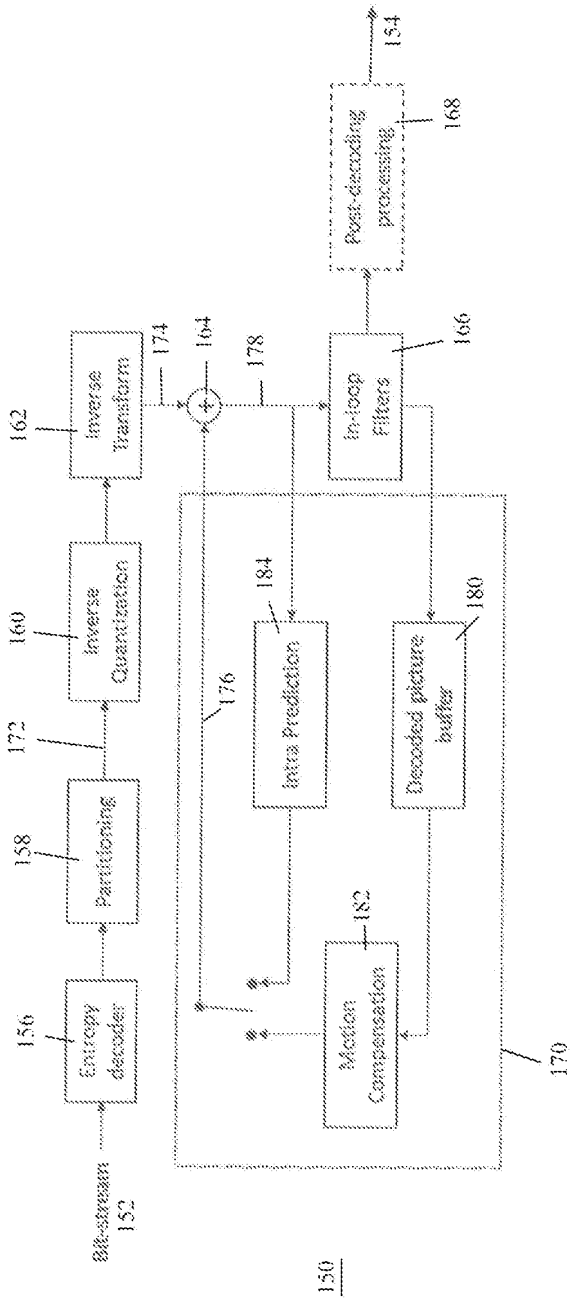
WO 2025/200069 PCT/CN2024/089409

[Fig. 1]



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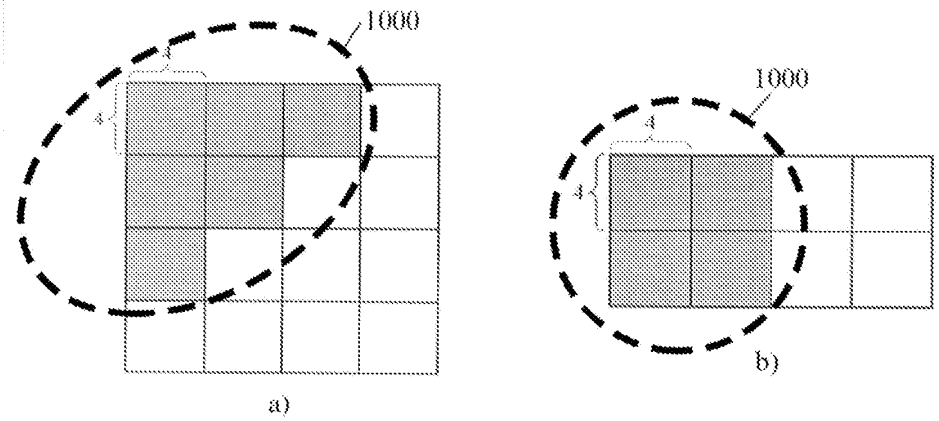
[Fig. 2]



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[Fig. 3]

Rule 26, 15.05.2024

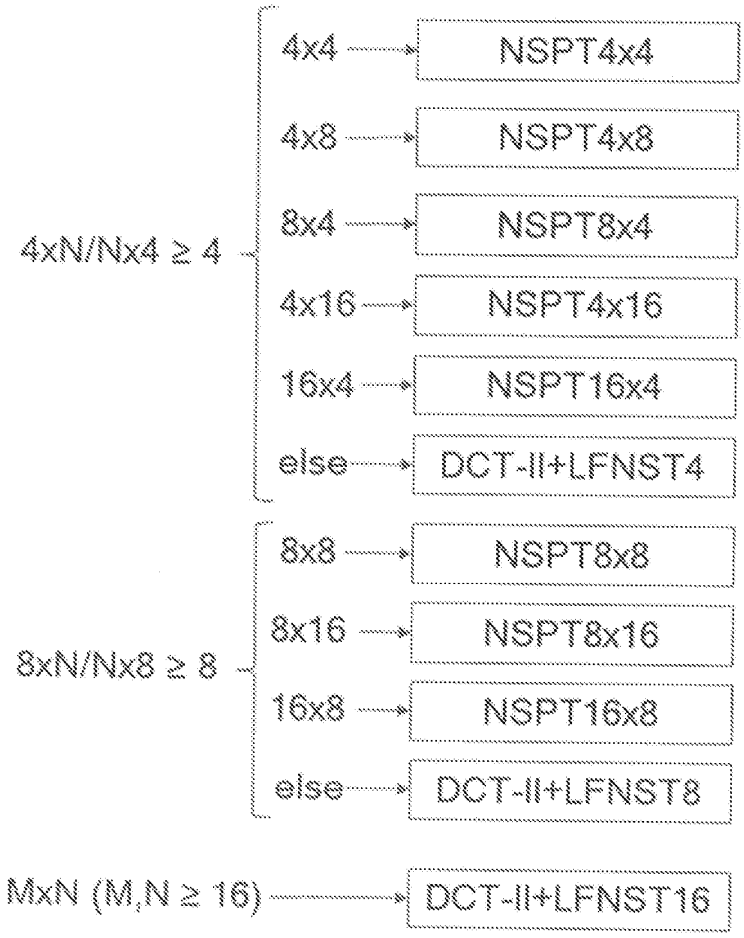


[Fig. 4]

Rule 26, 15.05.2024

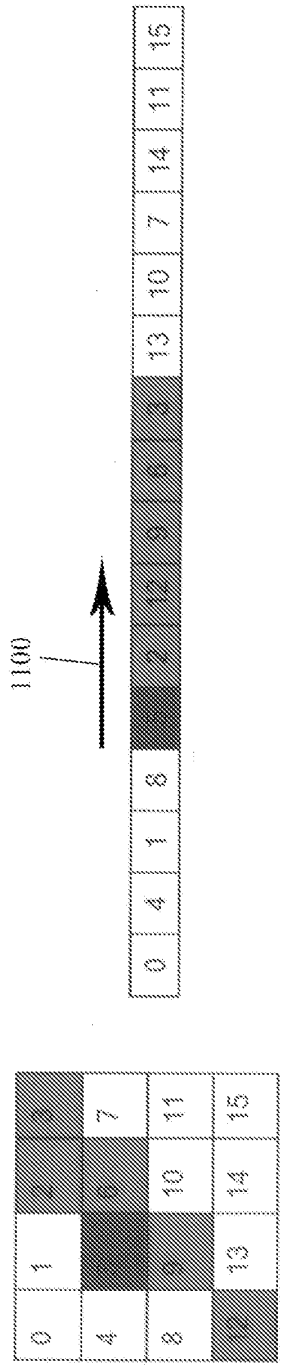
Intra pred. mode	34	33	32	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6
LNNST set index	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2
Intra pred. mode	5	4	3	2	1	0	-1	-2	-3	-4	-5	-6	-7	-8	-9	-10	-11	-12	-13	-14	-15	-16	-17	-18	-19	-20	-21	-22	-23
LFNST set index	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2
Intra pred. mode	-24	-25	-26	-27	-28	-29	-30	-31	-32	-33	-34	-35	-36	-37	-38	-39	-40	-41	-42	-43	-44	-45	-46	-47	-48	-49	-50	-51	-52
LFNST set index	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2

[Fig. 5]



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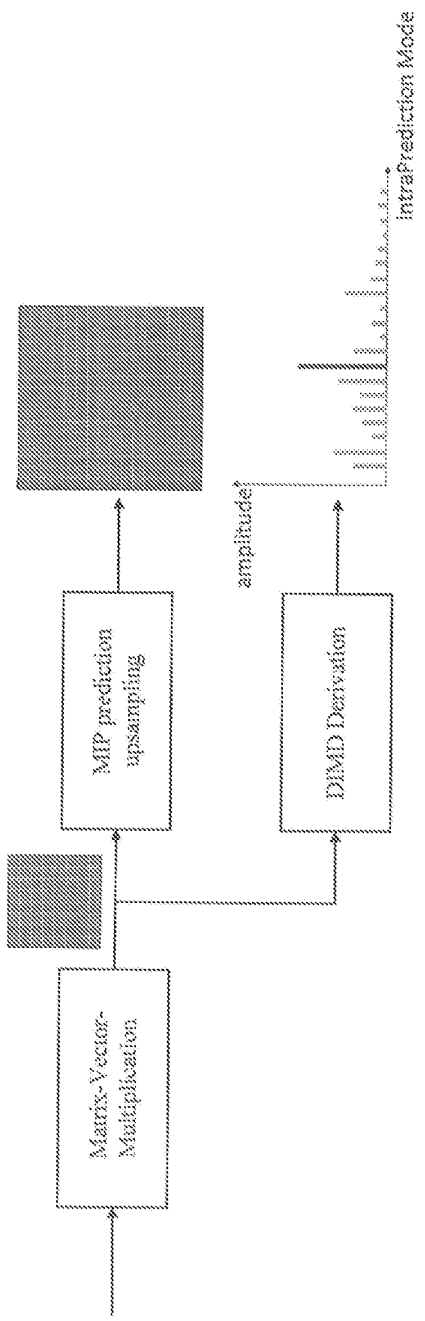
[Fig. 6]



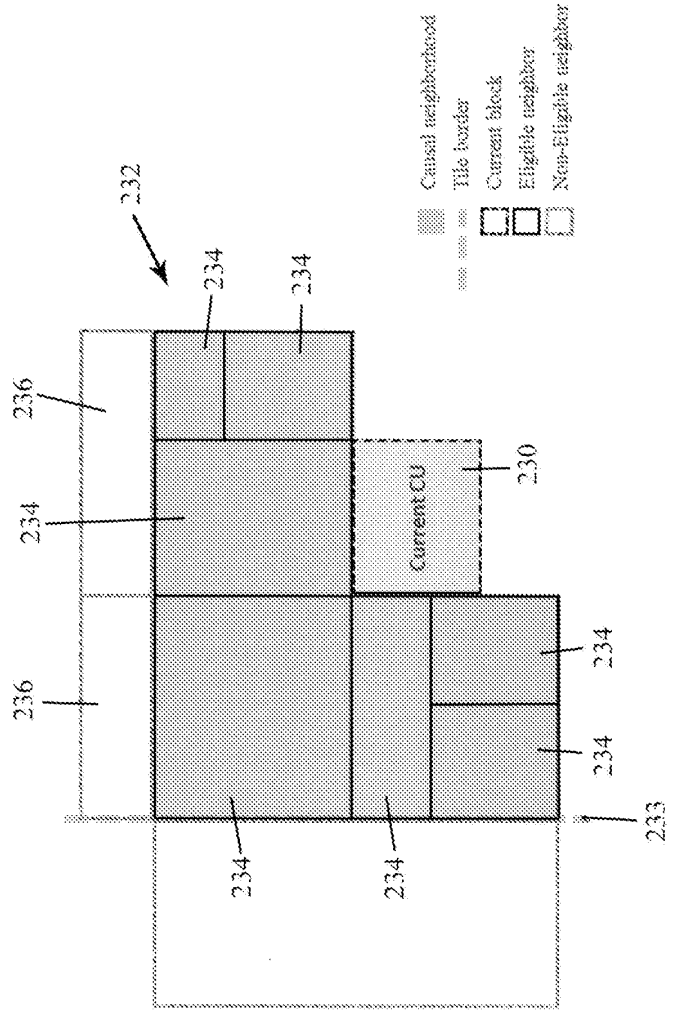
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[Fig. 7]

Rule 26,
15.05.2024

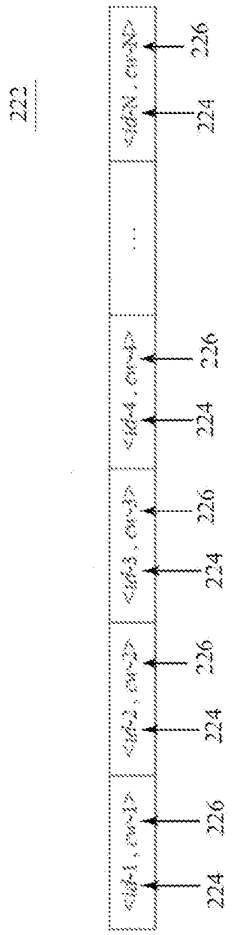


[Fig. 8]



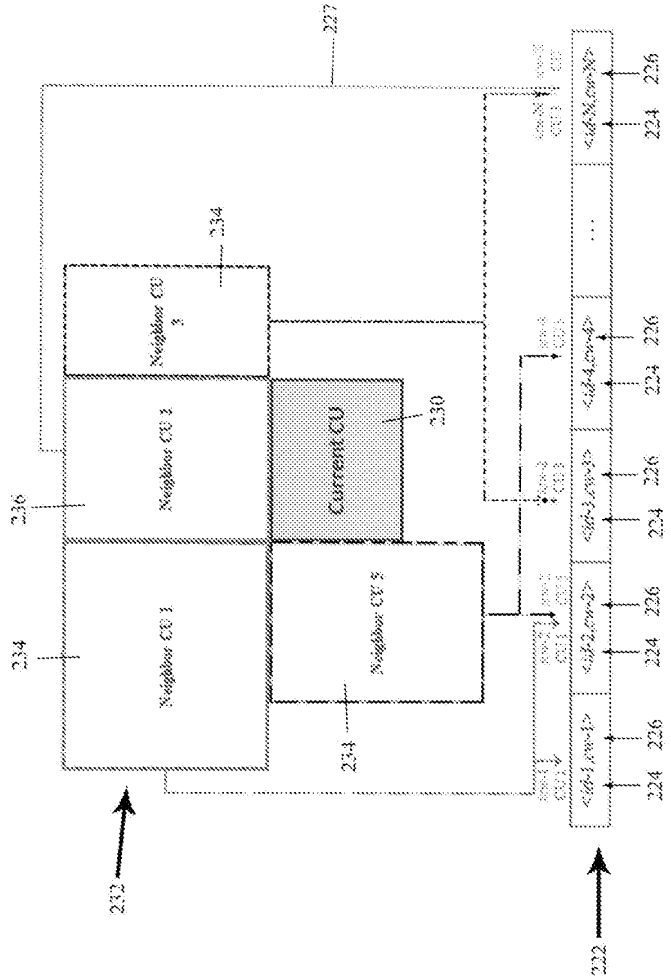
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[Fig. 9]



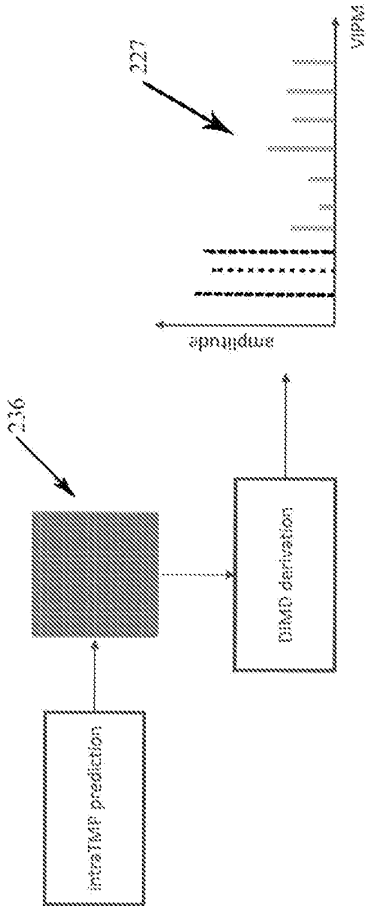
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[Fig. 10]



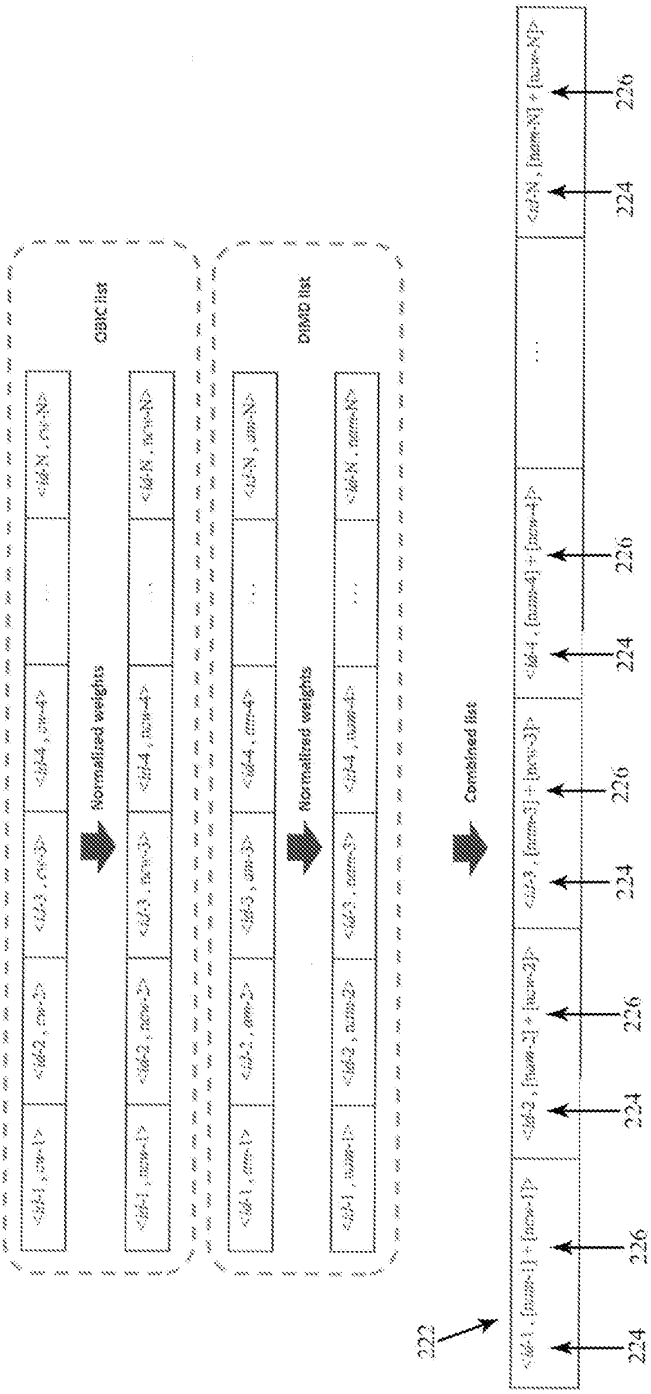
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[Fig. 11]



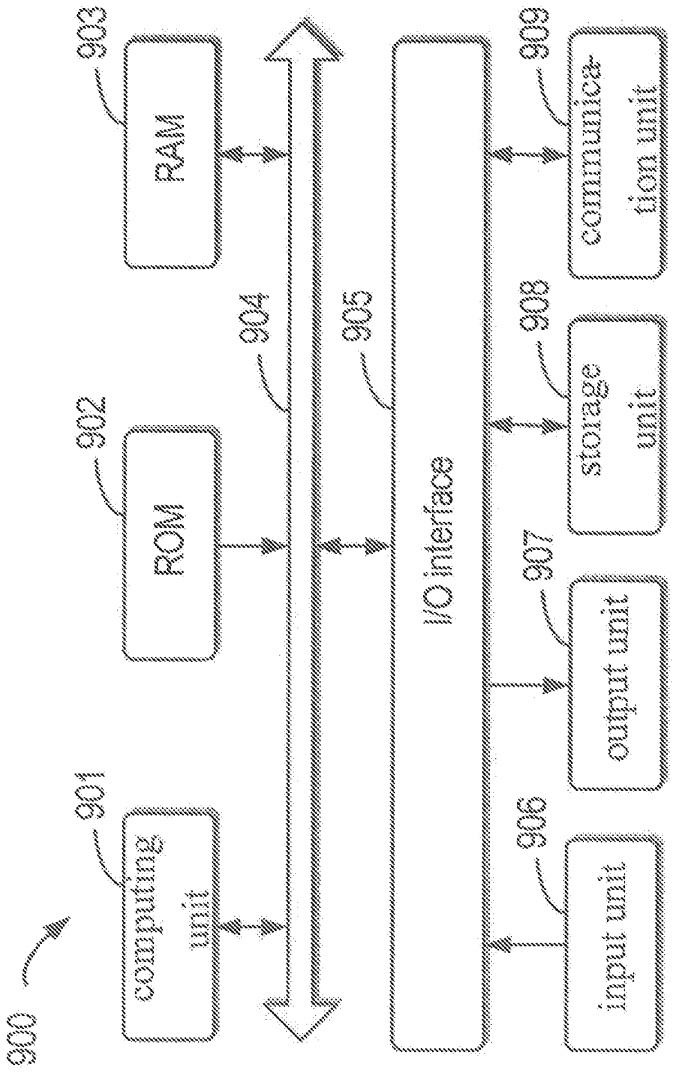
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[Fig. 12]



Rule 26,
15.05.2024

[Fig. 13]


900
901
computing unit
902
ROM
903
RAM
904
905
I/O interface
906
input unit
907
output unit
908
storage unit
909
communication unit