

FIG. 1

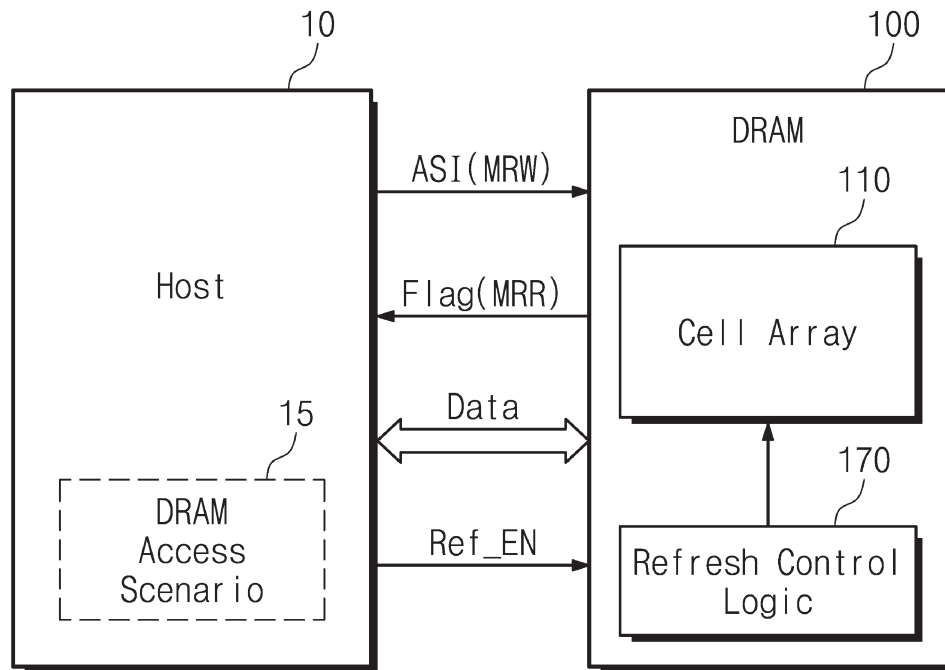


FIG. 2

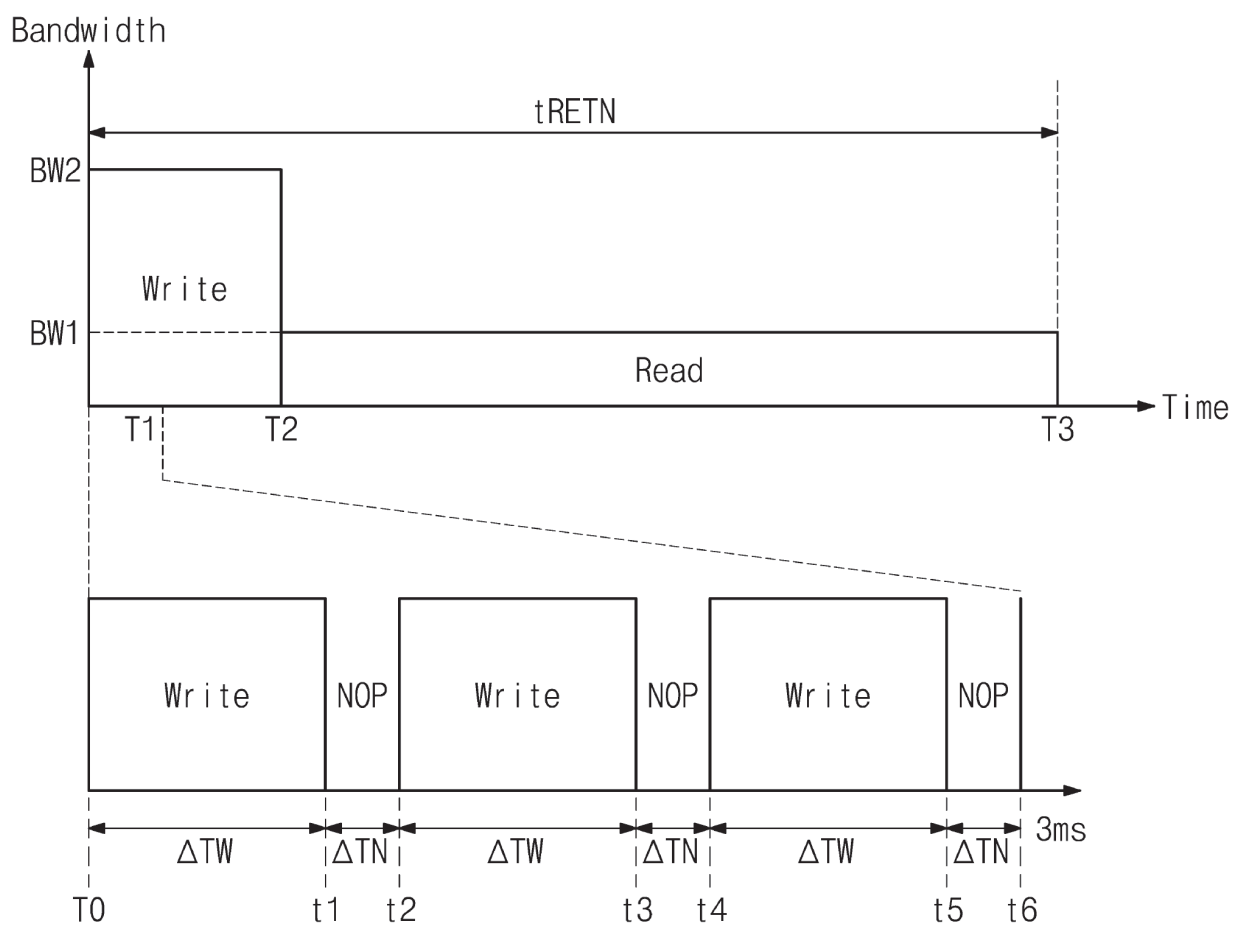


FIG. 3

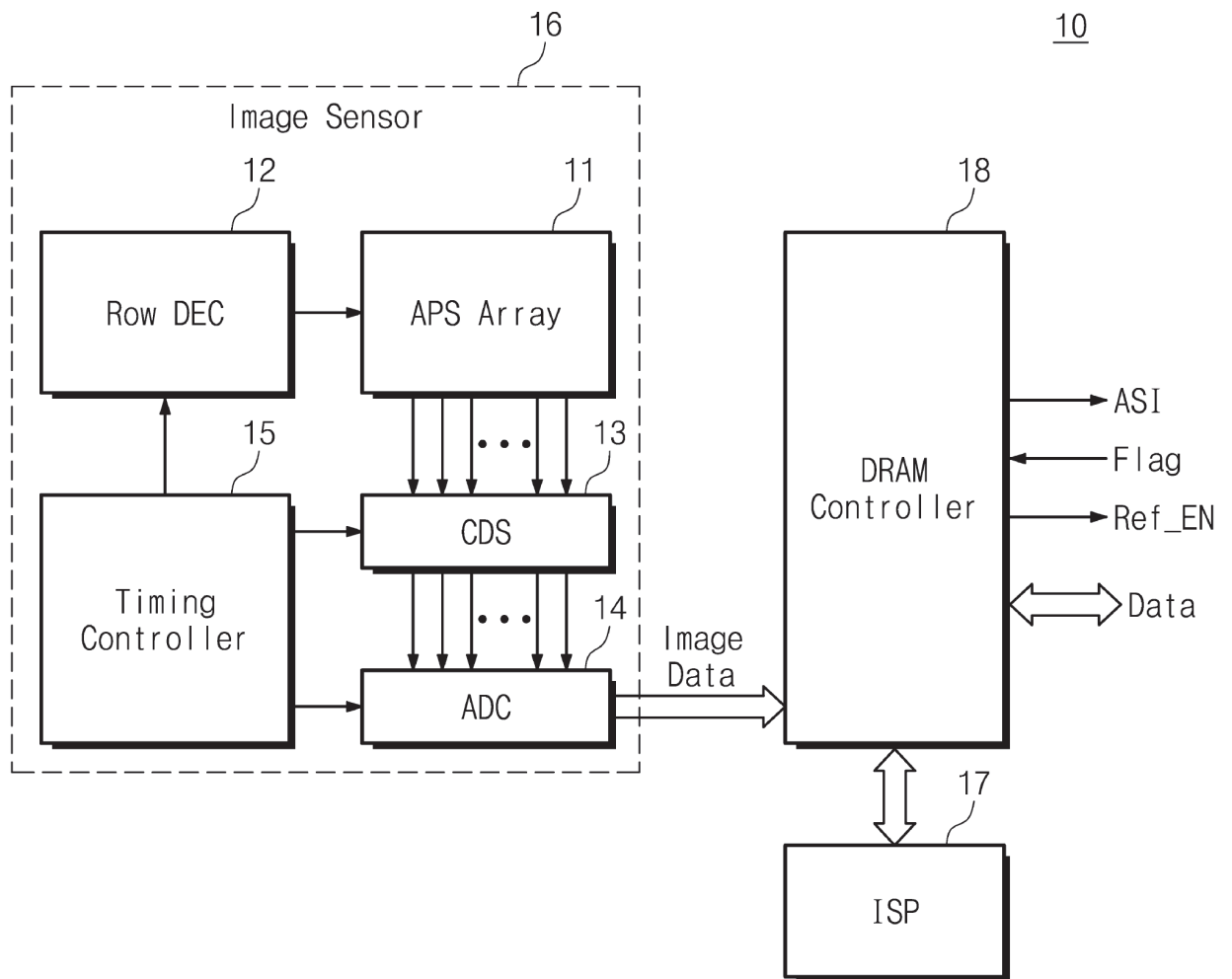


FIG. 4

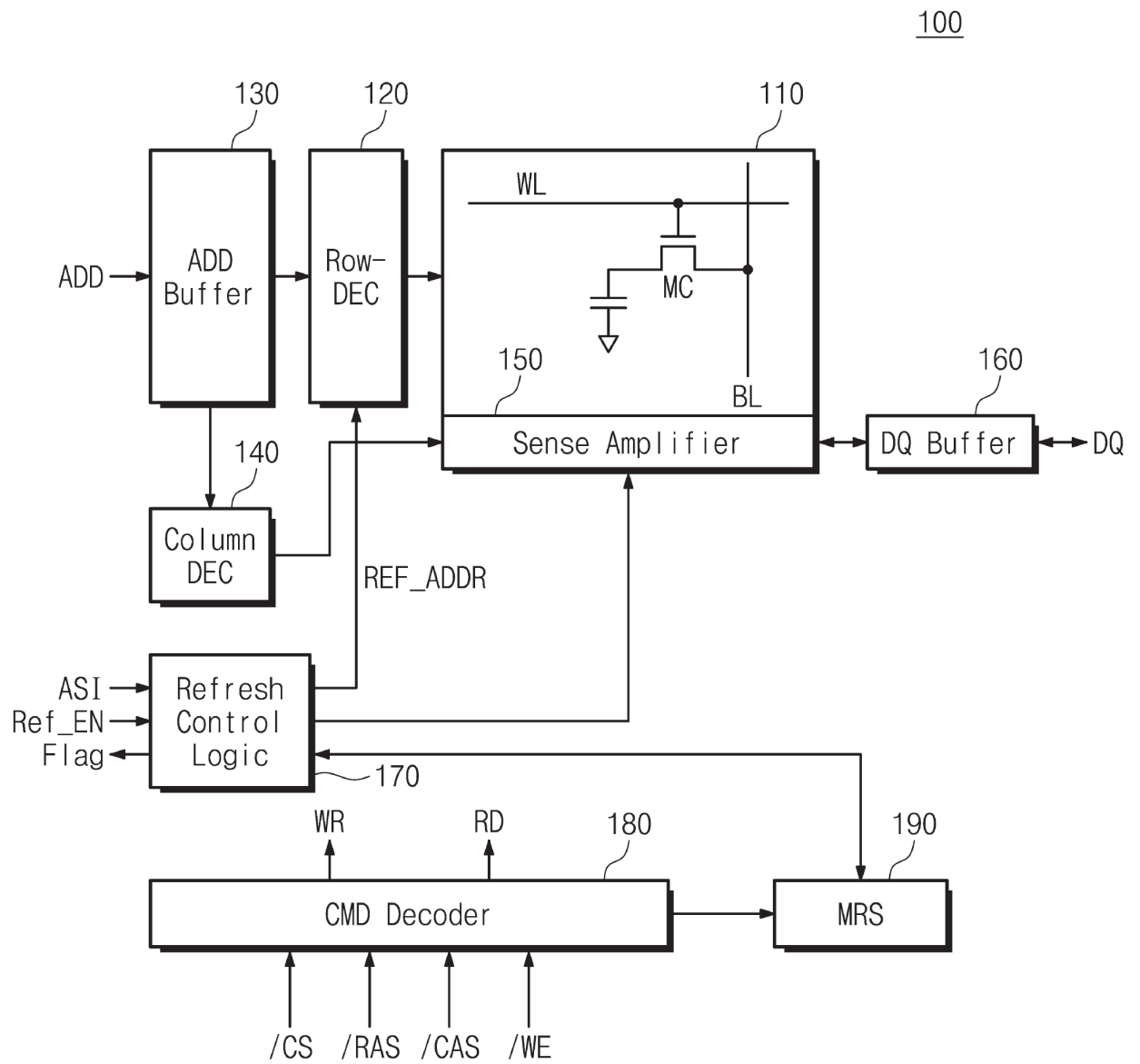


FIG. 5

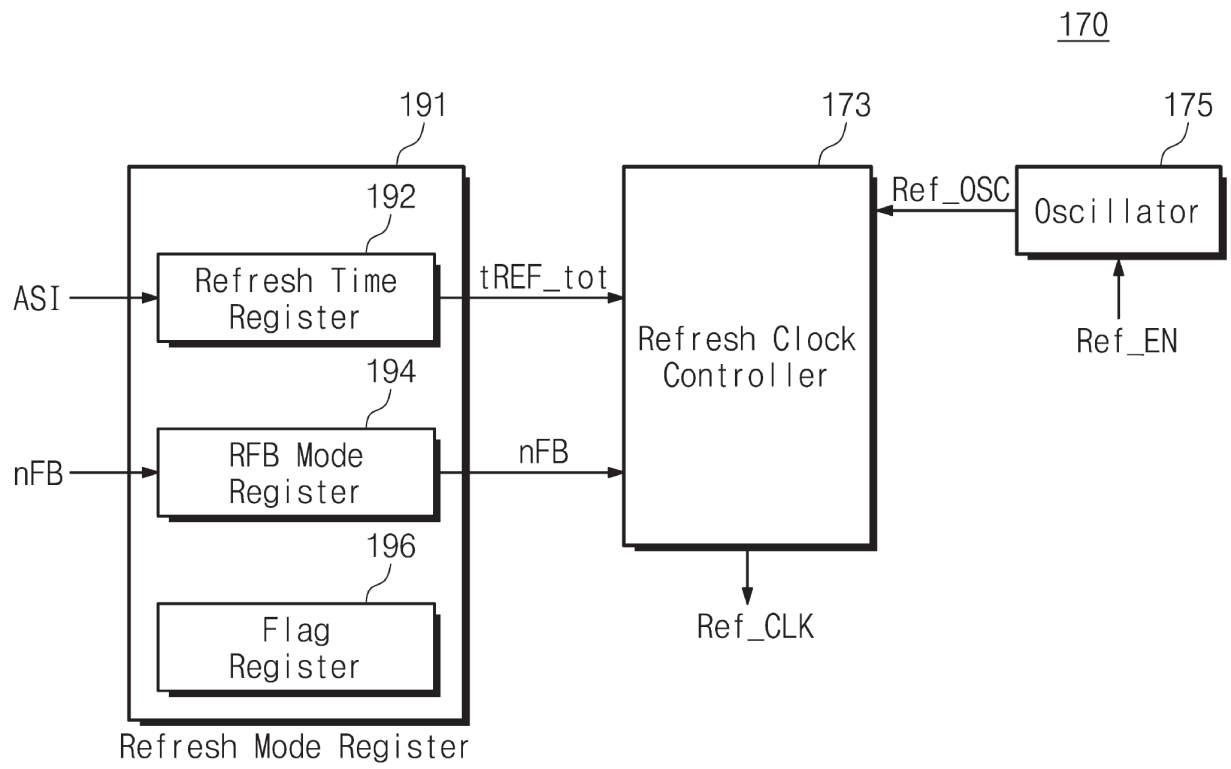


FIG. 6A

Total Refresh Time (tREF_tot)	MRW
$8\text{ms} \leq \text{tREF_tot} < 256\text{ms}$	00
$4\text{ms} \leq \text{tREF_tot} < 8\text{ms}$	01
$2\text{ms} \leq \text{tREF_tot} < 4\text{ms}$	10
$\text{tREF_tot} < 2\text{ms}$	11

FIG. 6B

Acceptable Fail Bit Number	tREF	MRW
All Guarantee	32ms	00
$0 < \text{tot} \leq 10\text{bit}$	128ms	01
$11 < \text{tot} \leq 100\text{bit}$	256ms	10
No Guarantee	256ms~	11

FIG. 7

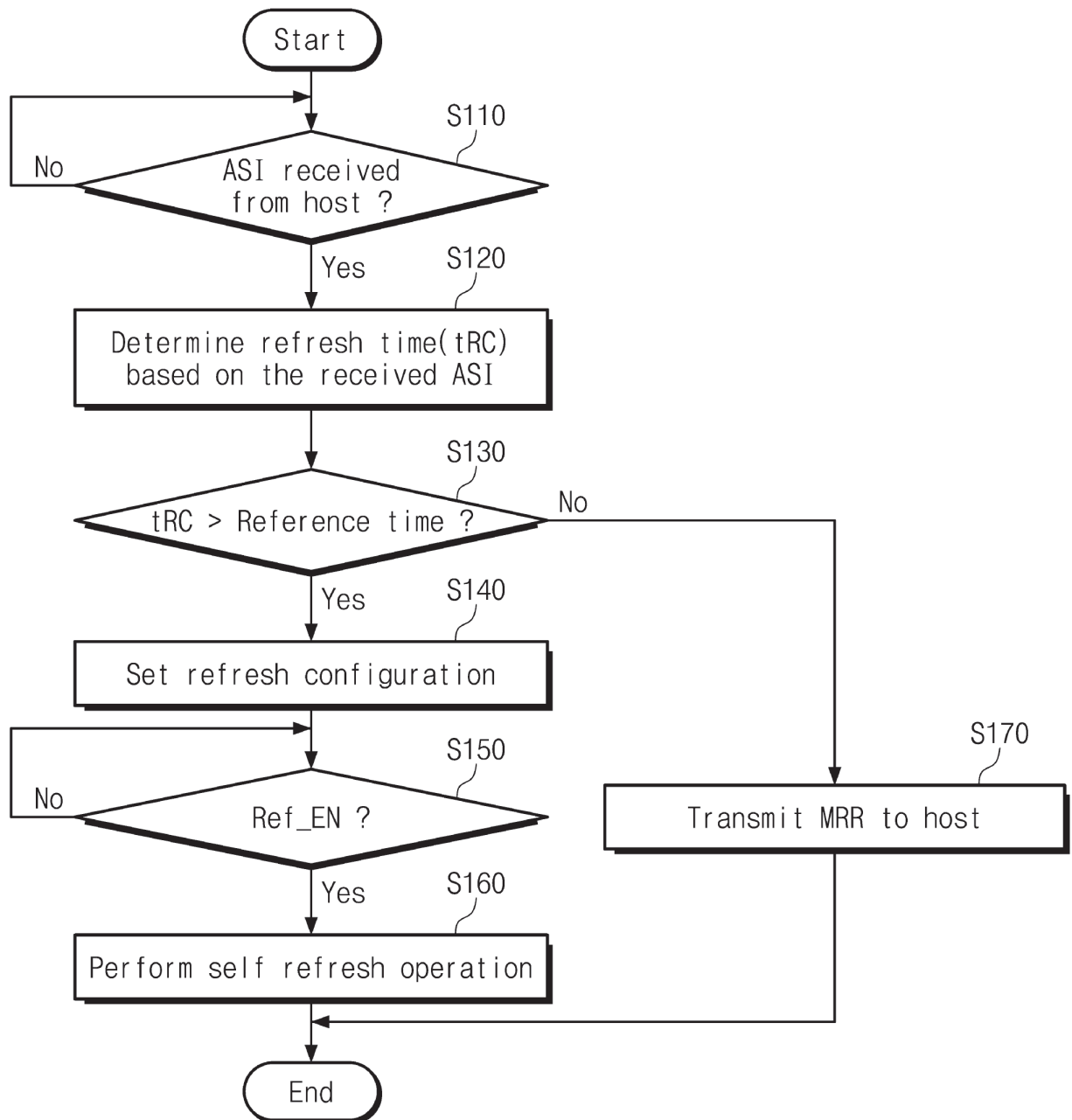


FIG. 8

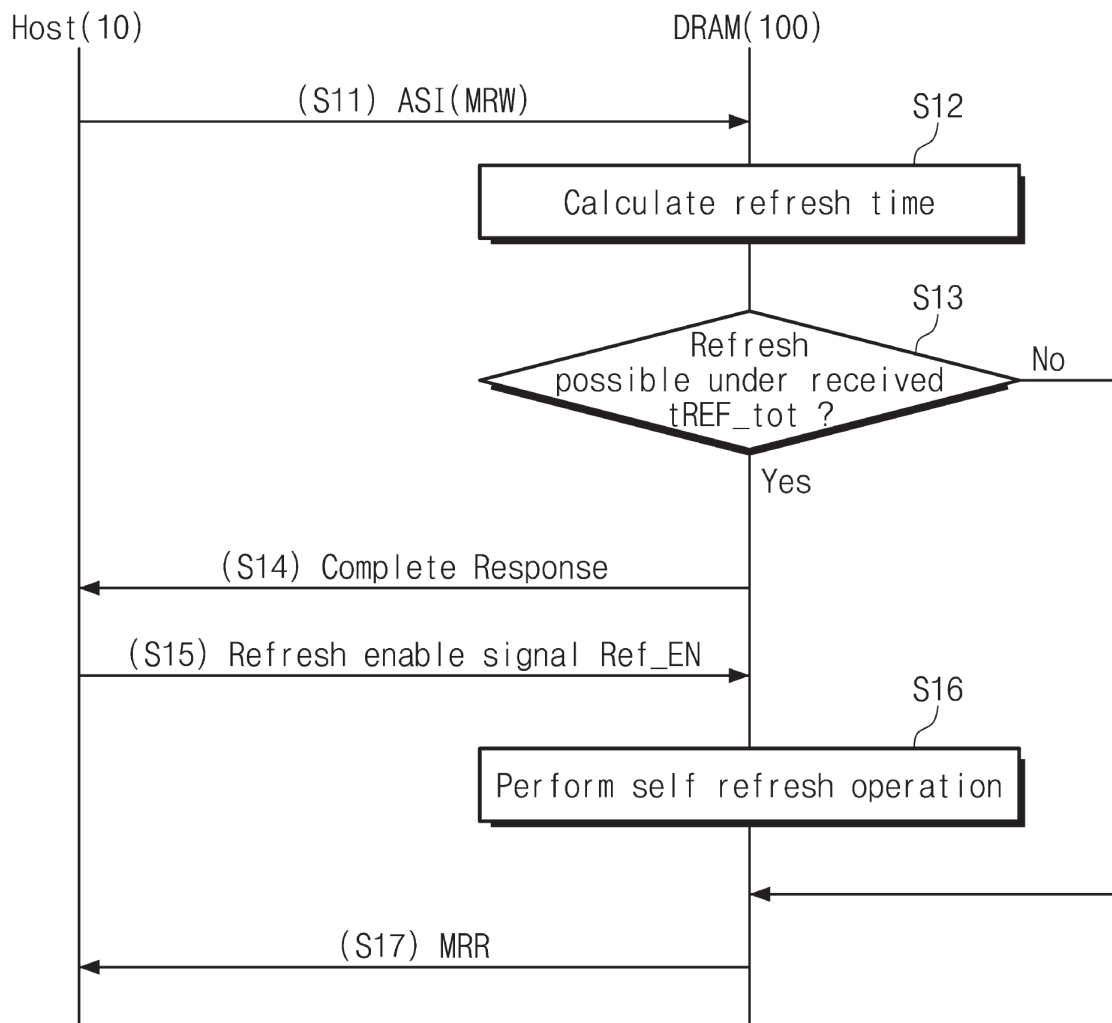


FIG. 9

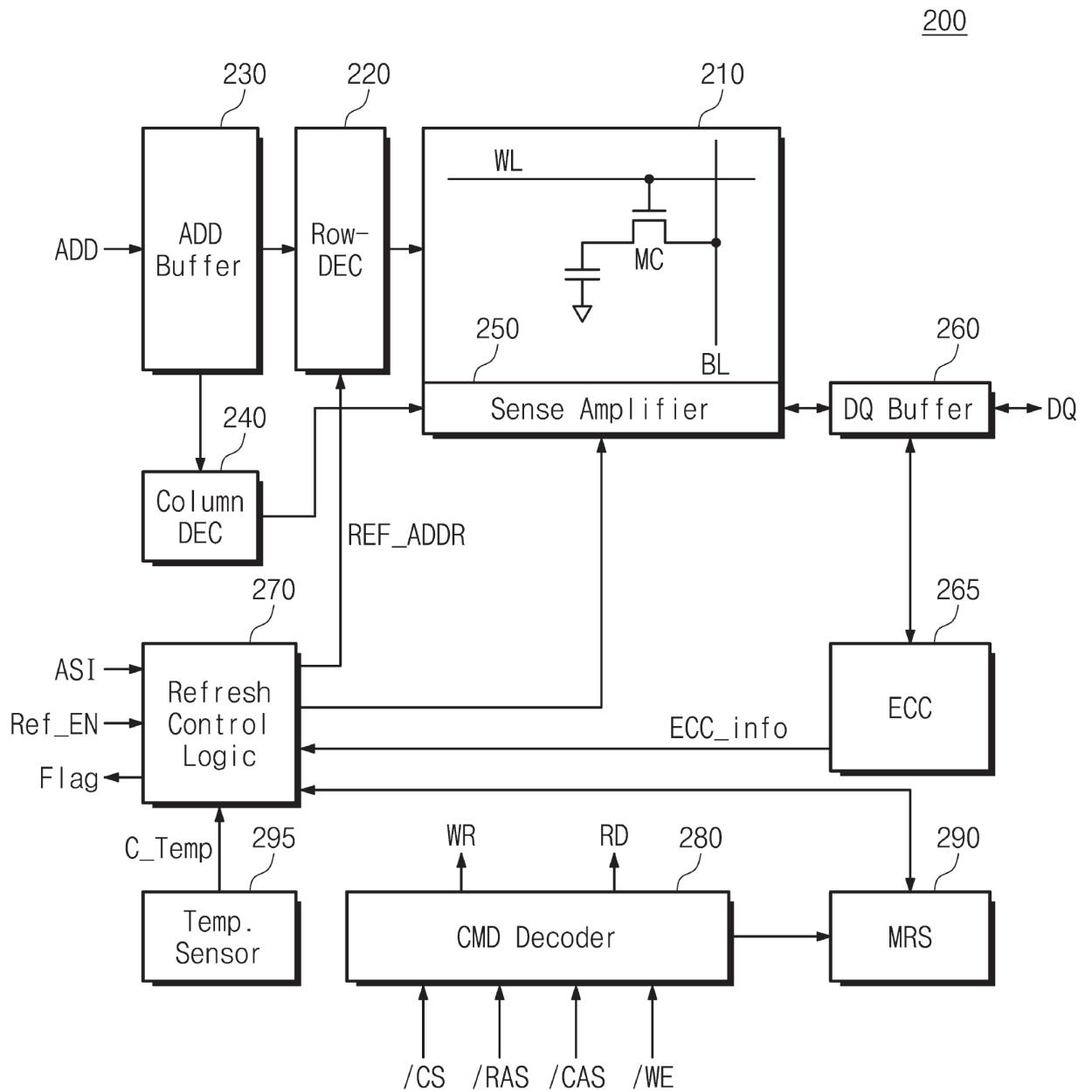


FIG. 10

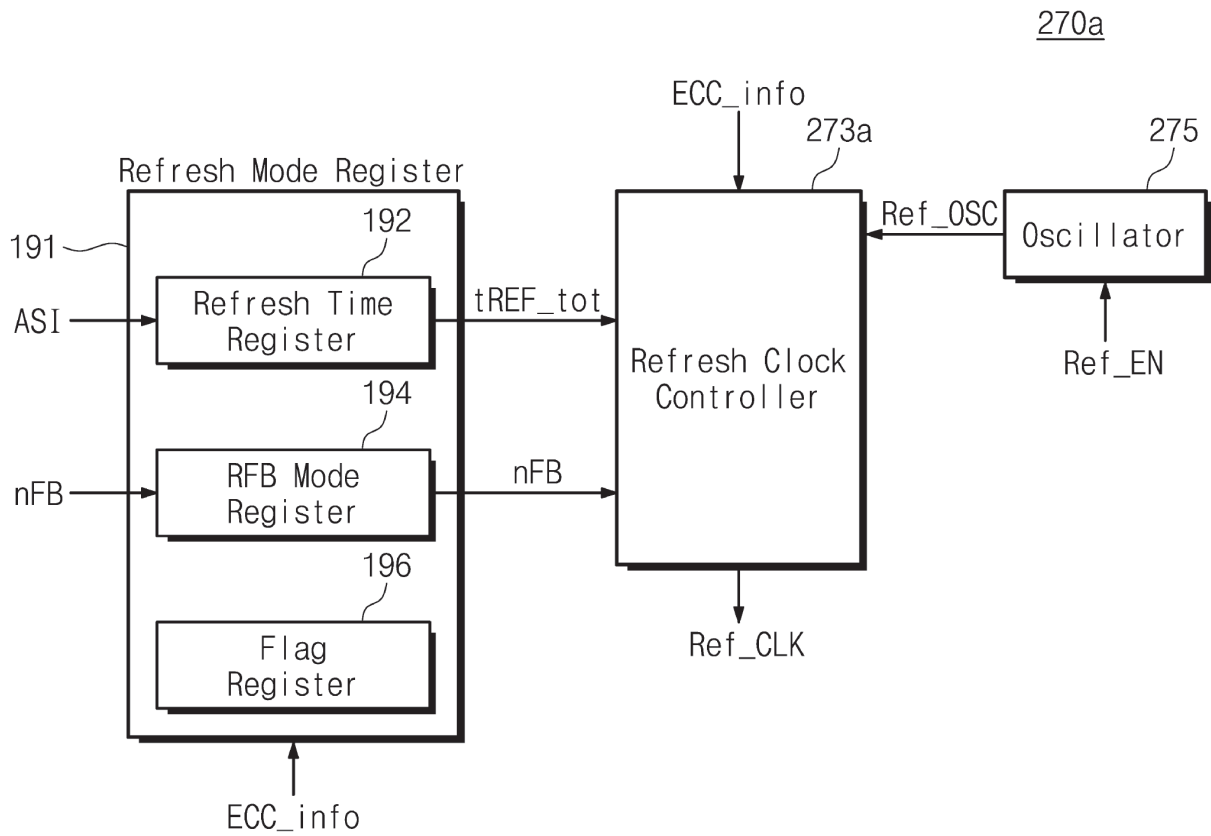


FIG. 11

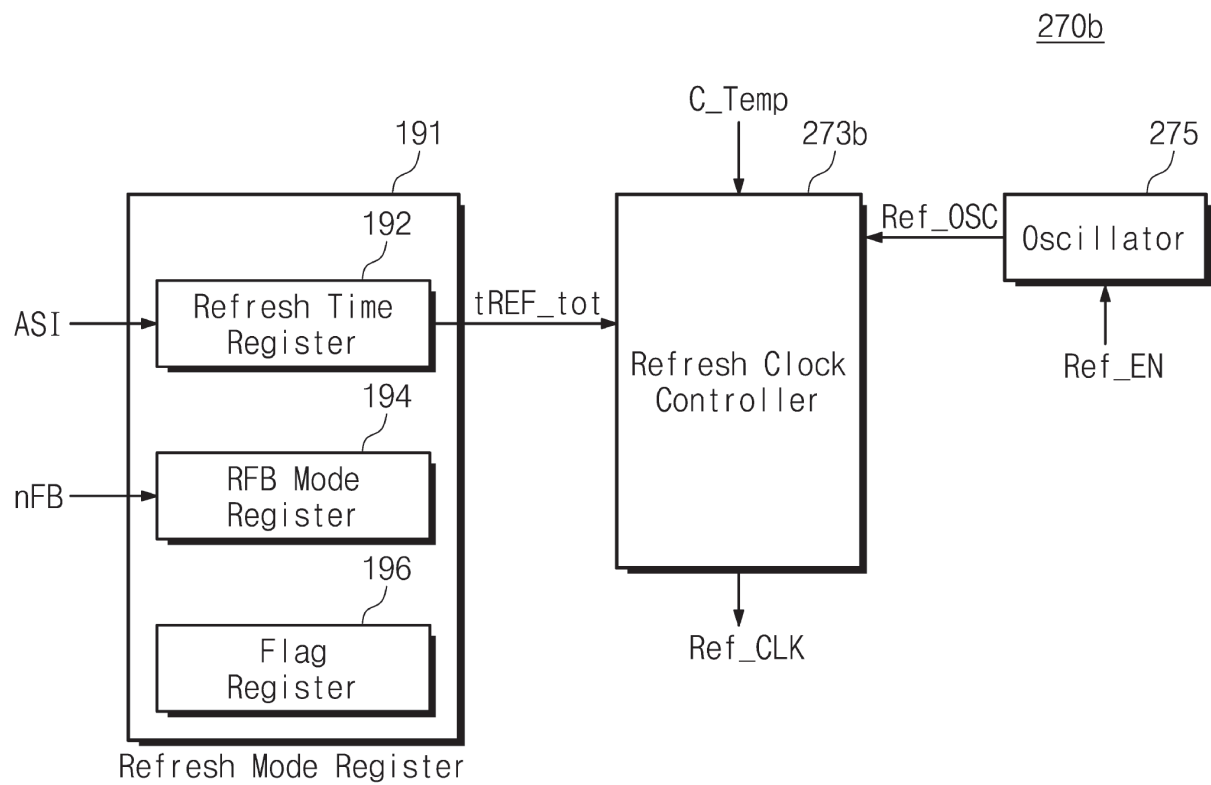


FIG. 12

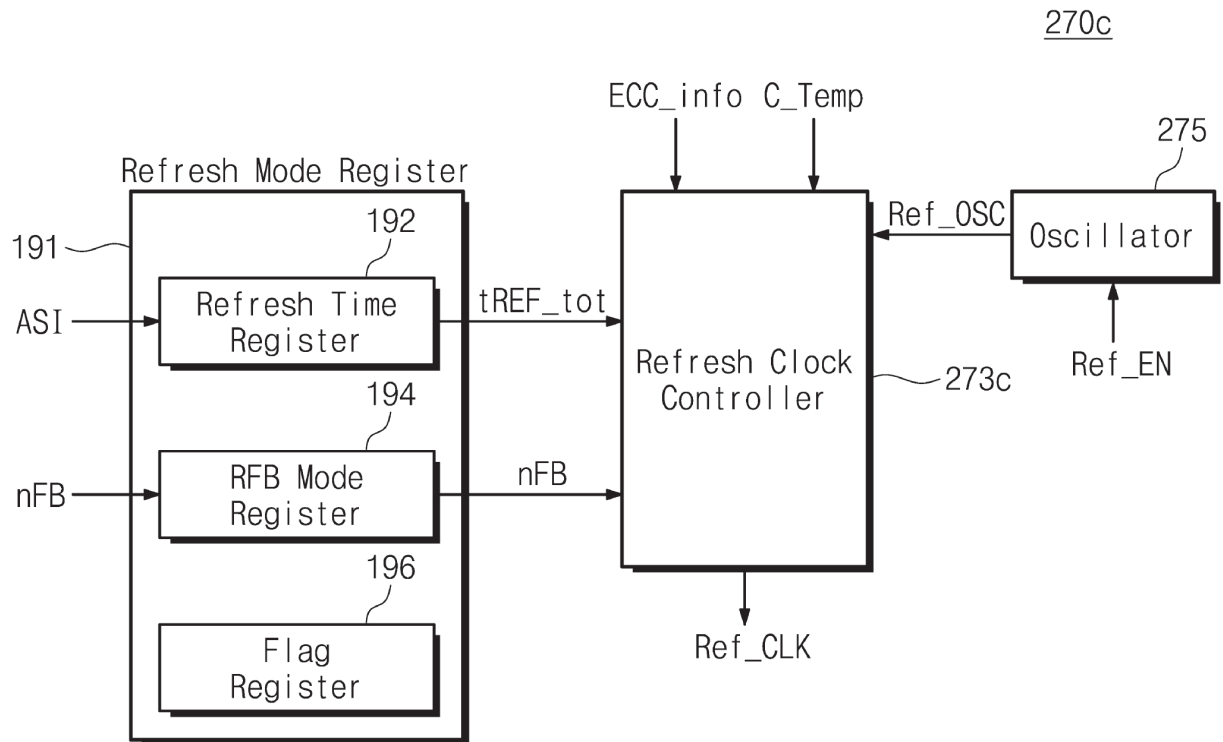


FIG. 13

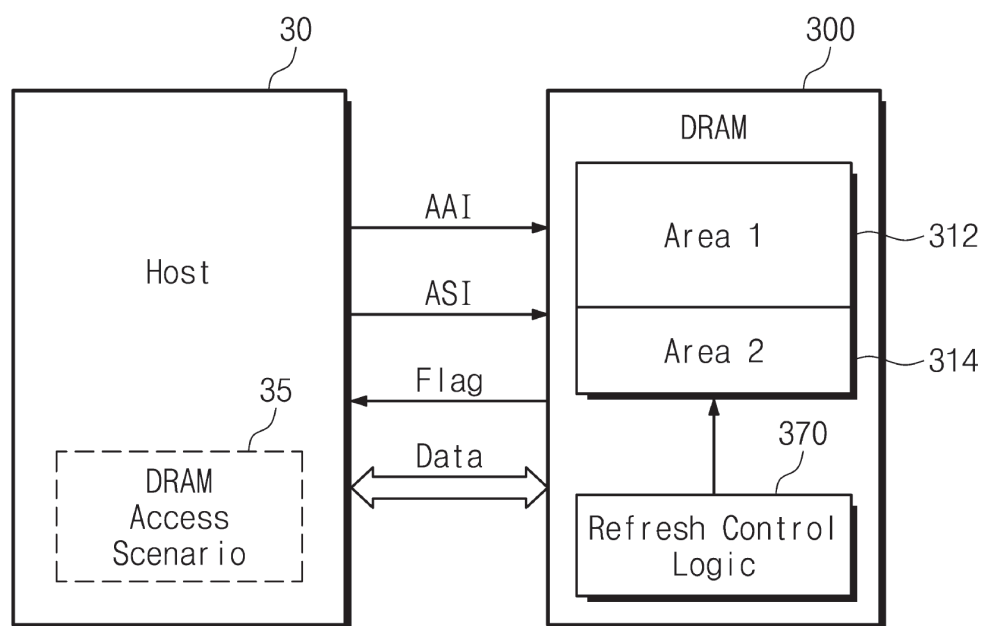


FIG. 14

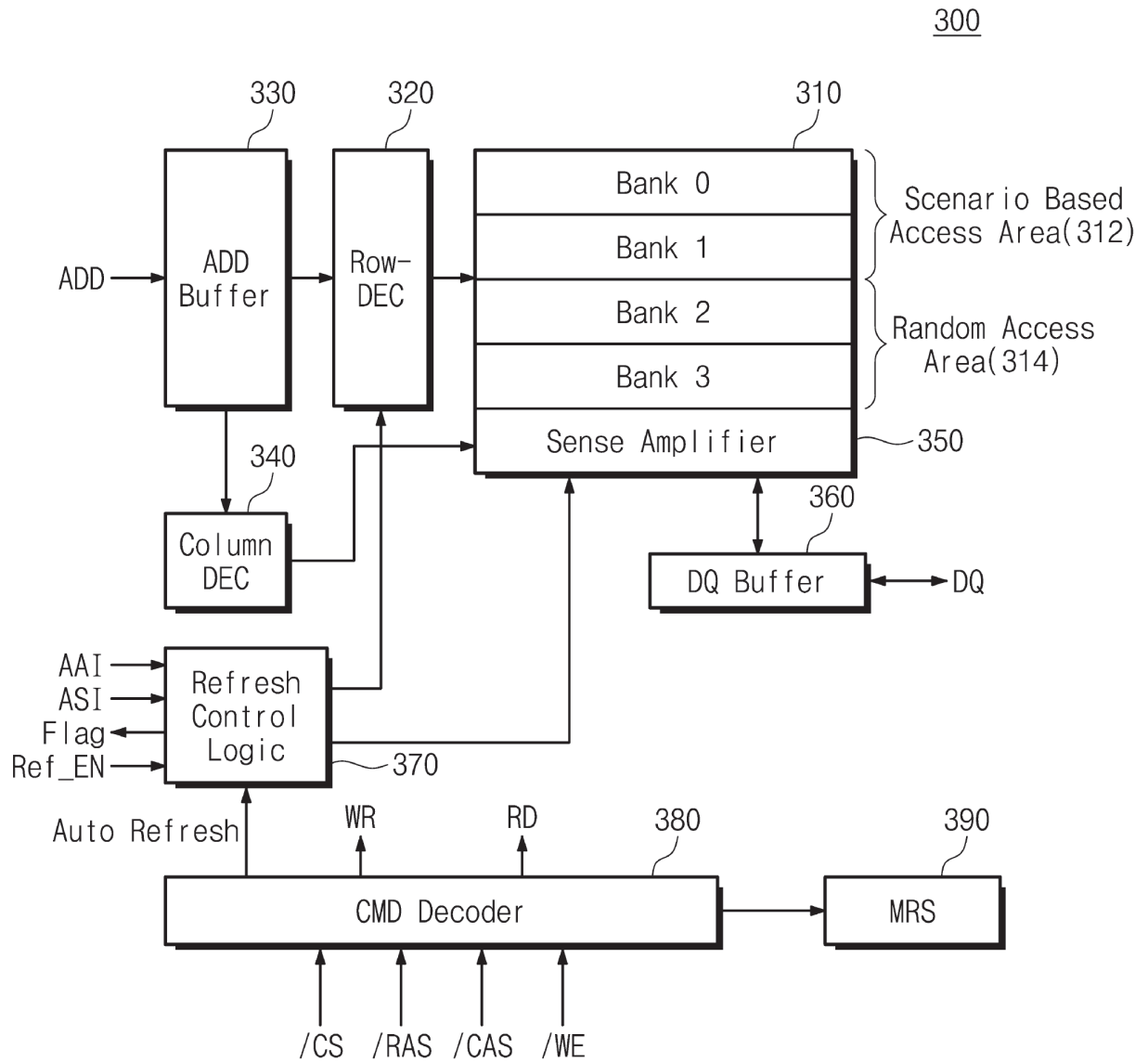


FIG. 15

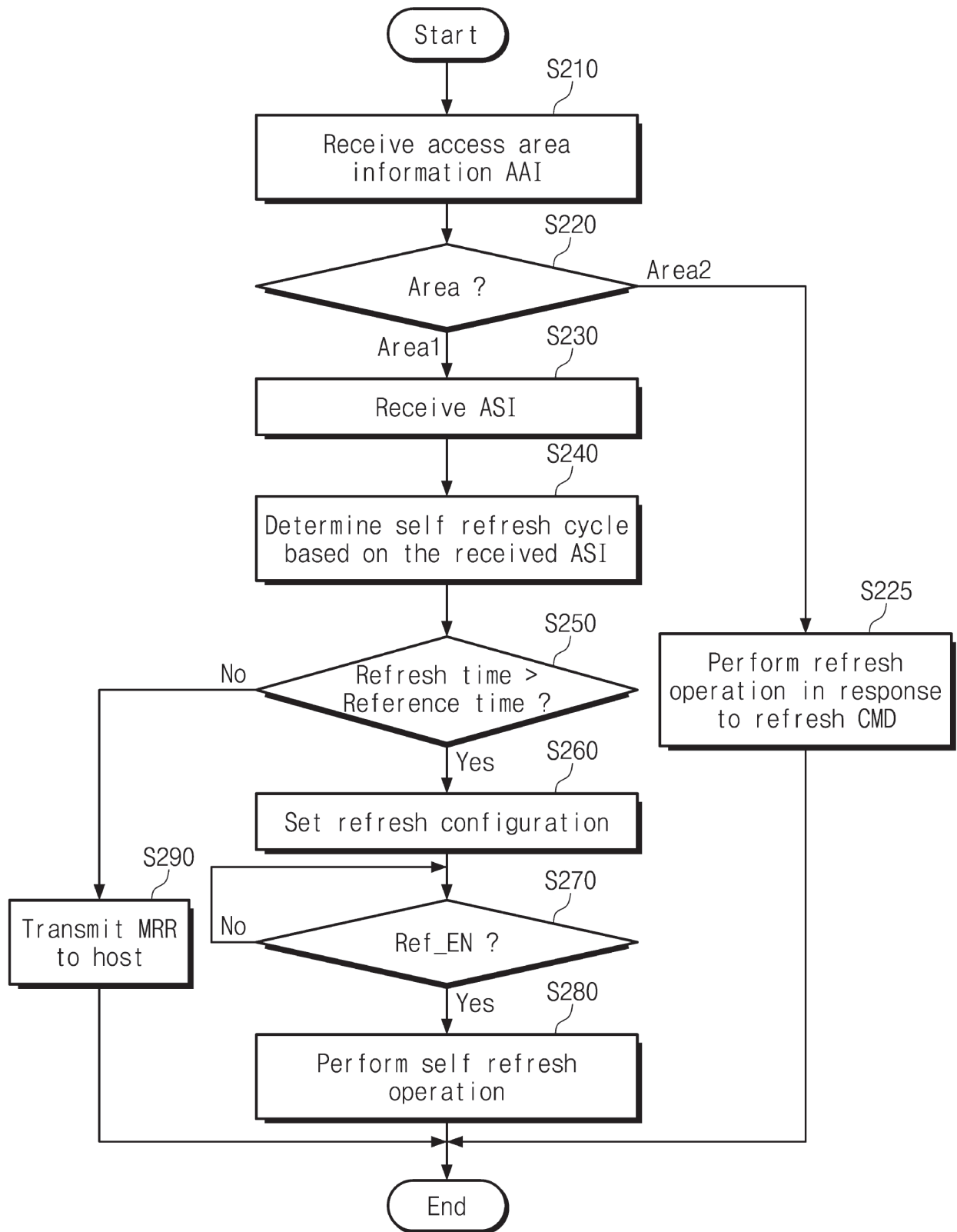


FIG. 16

