

DISPLAY APPARATUS**ABSTRACT OF THE DISCLOSURE**

The invention provide a display apparatus of a plural pixel simultaneous driving system wherein sample and hold noise included in a video signal is removed before the video signal is supplied to a display panel. A video signal processing circuit operates in response to a timing signal supplied from an external timing signal source, and delays an input video signal supplied thereto from an external video signal source to produce an output video signal. The video signal processing circuit includes a first sample and hold circuit, a second sample and hold circuit and a differential circuit. The first sample and hold circuit repetitively samples and holds the input video signal in response to the timing signal. The second sample and hold circuit repetitively samples and holds a predetermined reference signal in response to the same timing signal. The differential circuit differentially processes the input video signal after sampled and held and the reference signal after sampled and held to produce an output video signal from which sample and hold noise synchronized with the timing signal has been removed.

FIGURE 3