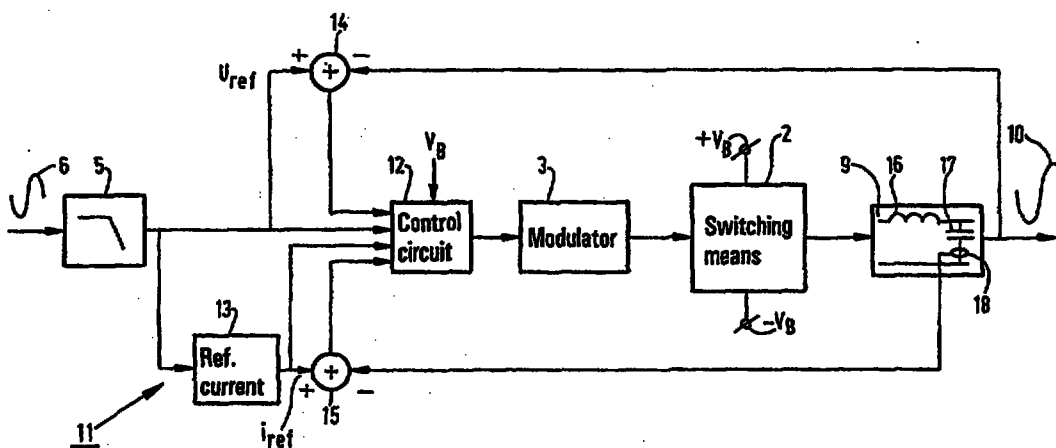




INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification ⁷: H03F 3/217	A1	(11) International Publication Number: WO 00/42702 (43) International Publication Date: 20 July 2000 (20.07.00)
(21) International Application Number: PCT/NL00/00016 (22) International Filing Date: 12 January 2000 (12.01.00) (30) Priority Data: 1011002 12 January 1999 (12.01.99) NL (71) Applicant (for all designated States except US): TECHNISCHE UNIVERSITEIT EINDHOVEN [NL/NL]; Den Dolech 2, NL-5612 AZ Eindhoven (NL). (72) Inventors; and (75) Inventors/Applicants (for US only): VELTMAN, André [NL/NL]; Markt 49, NL-4101 BW Culemborg (NL). DOMENSINO, Hendrikus, Johannes, Jacobus [NL/NL]; Minckelersplein 2, NL-5611 EE Eindhoven (NL). (74) Agents: VAN KAN, J., J., H. et al.; Algemeen Octrooibureau, World Trade Center, Pastoor Petersstraat 160, NL-5612 LV Eindhoven (NL).		(81) Designated States: AE, AL, AM, AT, AT (Utility model), AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CR, CU, CZ, CZ (Utility model), DE, DE (Utility model), DK, DK (Utility model), DM, EE, EE (Utility model), ES, FI, FI (Utility model), GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SK (Utility model), SL, TJ, TM, TR, TT, TZ, UA, UG, US, UZ, VN, YU, ZA, ZW, ARIPO patent (GH, GM, KE, LS, MW, SD, SL, SZ, TZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG). Published <i>With international search report.</i> <i>In English translation (filed in Dutch).</i>

(54) Title: AMPLIFIER CIRCUIT



(57) Abstract

An amplifier circuit (11) for amplifying electric signals, comprising controllable switching means (2) for generating a block wave signal whose amplitude varies between first and second supply voltage values during operation, filter means (9) for filtering the block wave signal so as to produce an output signal (10). The filter means (9) comprise a self-inductance (16) and a capacitance (17). The amplifier circuit (11) furthermore comprises modulating means (3) for pulse width modulation of the block wave signal by driving the switching means (2) in response to an input signal (16) to be amplified, and correction means (12) for providing from a reference value (U_{ref}) derived from the input signal and an output signal value proportional to the output signal (10) a correction signal for controlling the modulating means (3). In order to correct for interferences in the output signal (10), means (13) are provided for deriving a reference current (I_{ref}) from the input signal (6) as well as means (8) for providing a filter capacitance current proportional to the current through the filter capacitance (17). The correction means (12) are arranged for providing a current correction signal from the reference current (I_{ref}) and the filter capacitance current (18).