

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
29 January 2004 (29.01.2004)

PCT

(10) International Publication Number
WO 2004/010091 A1

(51) International Patent Classification⁷: **G01F 1/684**,
1/696, 1/698, 1/86

(21) International Application Number:
PCT/US2003/022337

(22) International Filing Date: 16 July 2003 (16.07.2003)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
60/397,139 19 July 2002 (19.07.2002) US
60/436,207 23 December 2002 (23.12.2002) US

(71) Applicant: **CELERITY GROUP INC.** [US/US]; 2805
Mission College Blvd., Santa Clara, CA 95054 (US).

(72) Inventor: **LULL, John, Michael**; P.O. Box 667, Clare-
mont, CA 91711 (US).

(74) Agent: **SKRIVANEK, Robert, A., Jr.**; Lowrie, Lando &
Anastasi, LLP, One Main Street, Cambridge, MA 02142
(US).

(81) Designated States (*national*): AE, AG, AL, AM, AT, AU,
AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU,
CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH,
GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC,
LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW,
MX, MZ, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC,
SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA,
UG, UZ, VC, VN, YU, ZA, ZM, ZW.

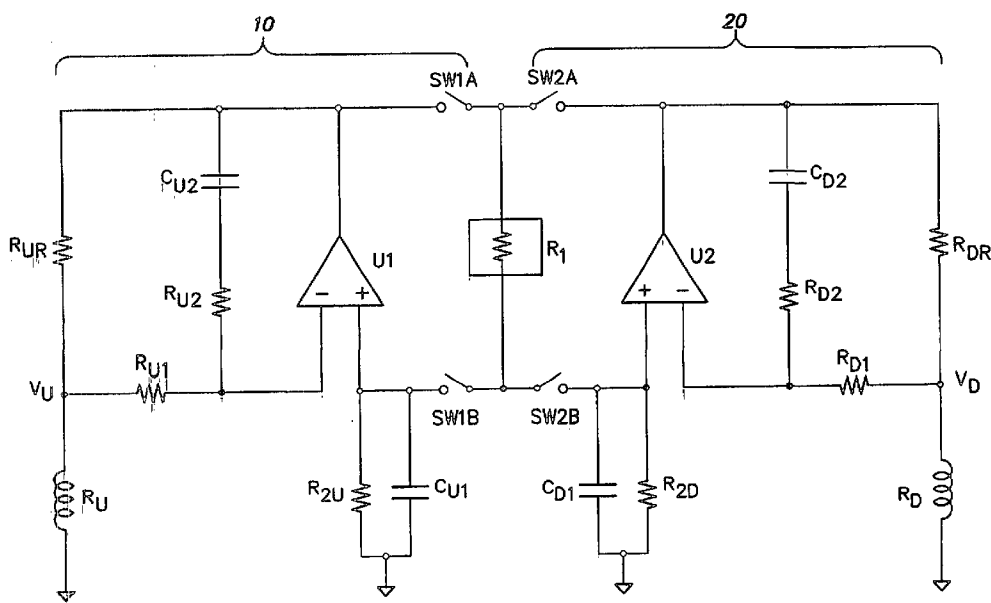
(84) Designated States (*regional*): ARIPO patent (GH, GM,
KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW),
Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM),
European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE,
ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO,
SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM,
GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

- with international search report
- before the expiration of the time limit for amending the
claims and to be republished in the event of receipt of
amendments

For two-letter codes and other abbreviations, refer to the "Guid-
ance Notes on Codes and Abbreviations" appearing at the begin-
ning of each regular issue of the PCT Gazette.

(54) Title: VARIABLE RESISTANCE SENSOR WITH COMMON REFERENCE LEG



(57) Abstract: A resistive bridge sensor circuit that includes a first resistive bridge circuit having a first variable resistance resistor and a second resistive bridge circuit having a second variable resistance resistor. The first and second resistive bridge circuits share at least a portion of a common reference leg that sets a resistance of a first and second variable resistors. The common reference leg or a portion of the common reference leg is alternately switchably connected to one of the first and second resistive bridge circuits.

WO 2004/010091 A1