

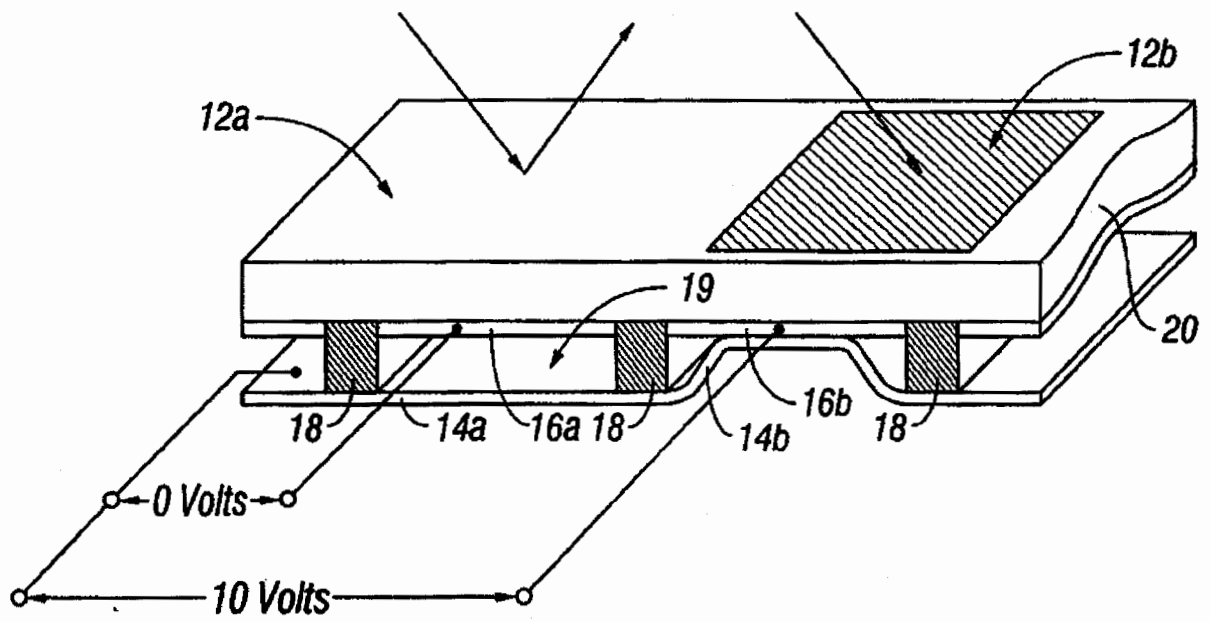


FIG. 1

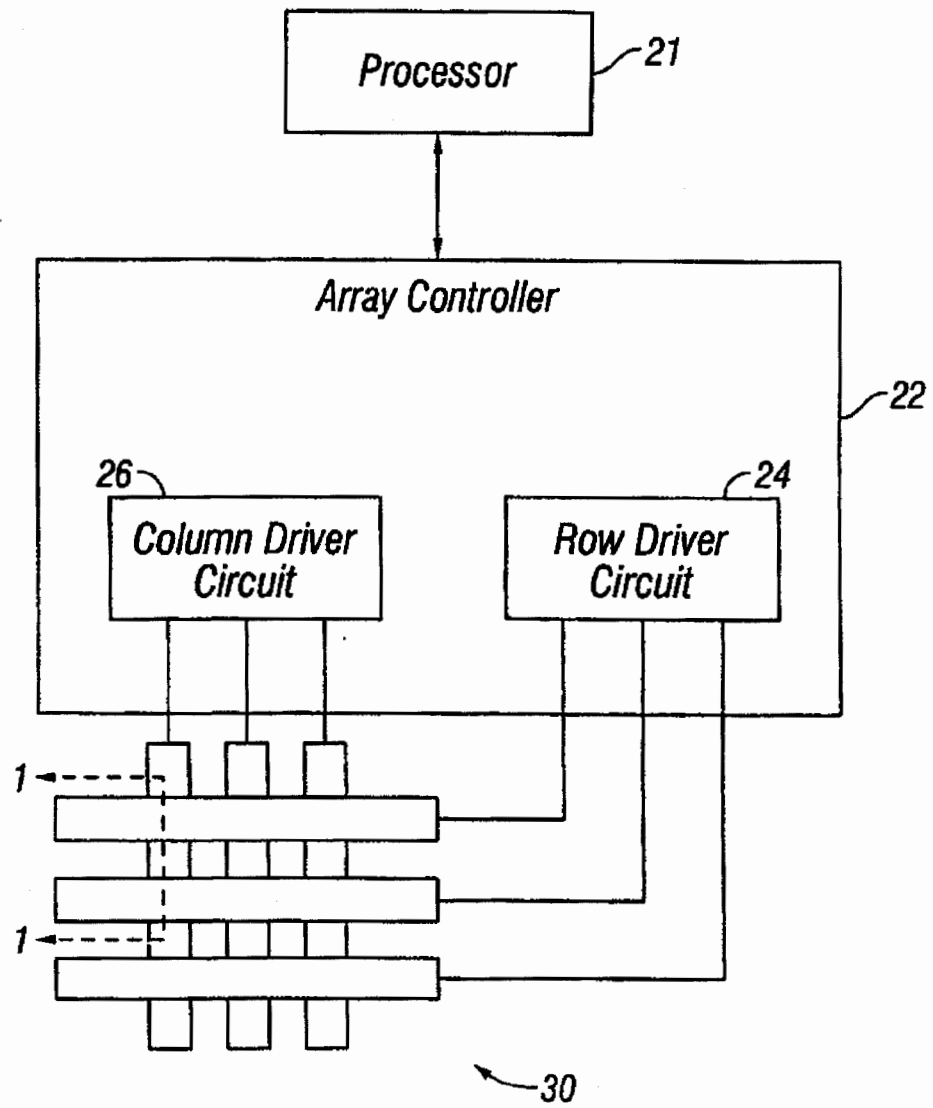


FIG. 2

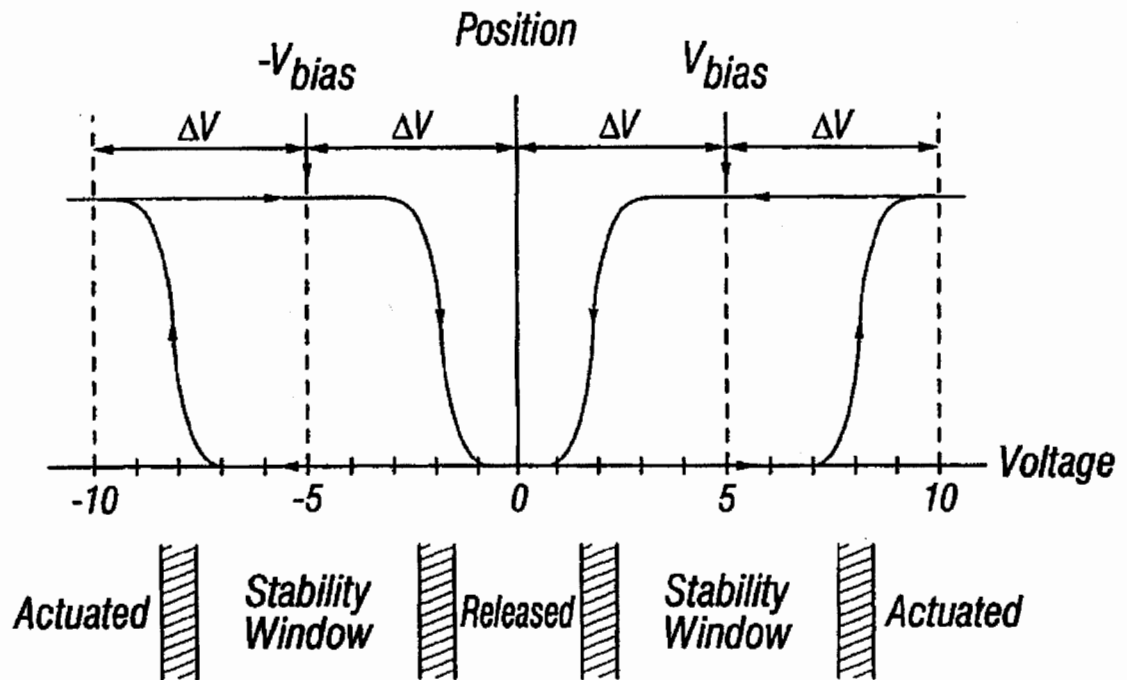


FIG. 3

	Column Output Signals	
	$+V_{bias}$	$-V_{bias}$
Row Output Signals		
0	Stable	Stable
$+\Delta V$	Release	Actuate
$-\Delta V$	Actuate	Release

FIG. 4

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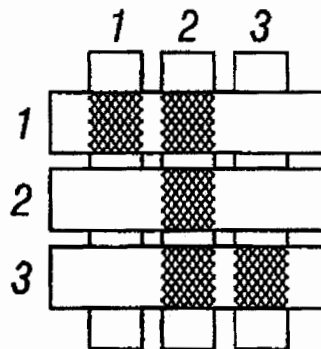


FIG. 5A

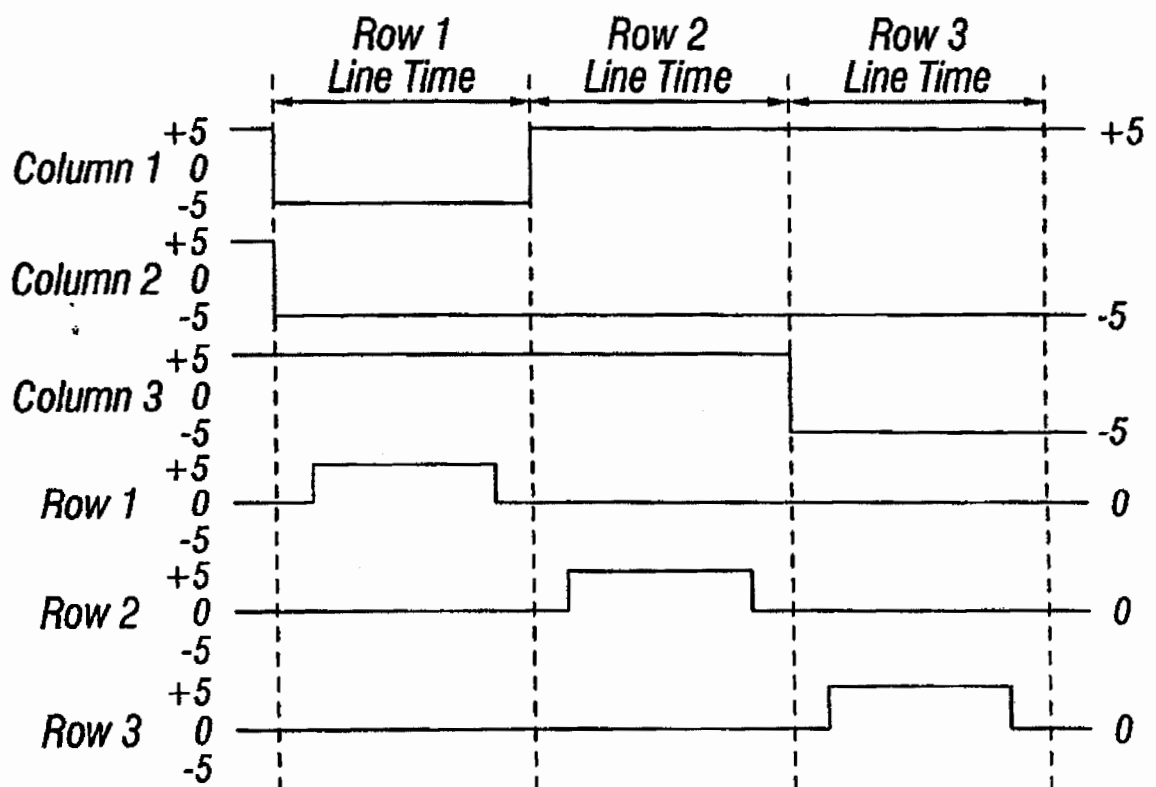


FIG. 5B

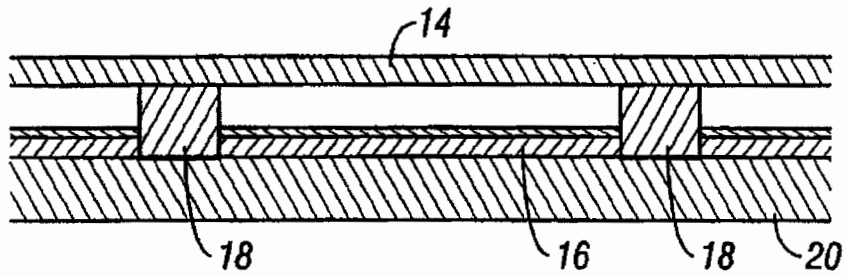


FIG. 6A

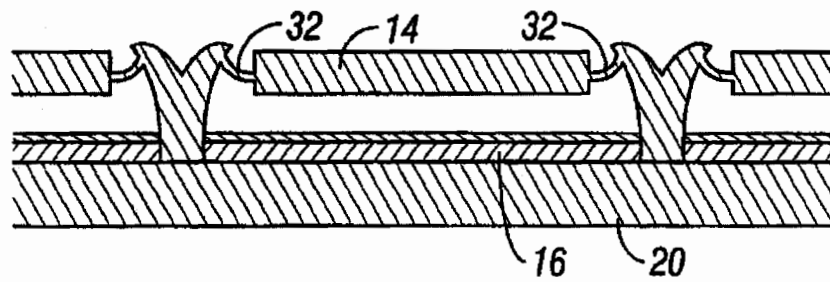


FIG. 6B

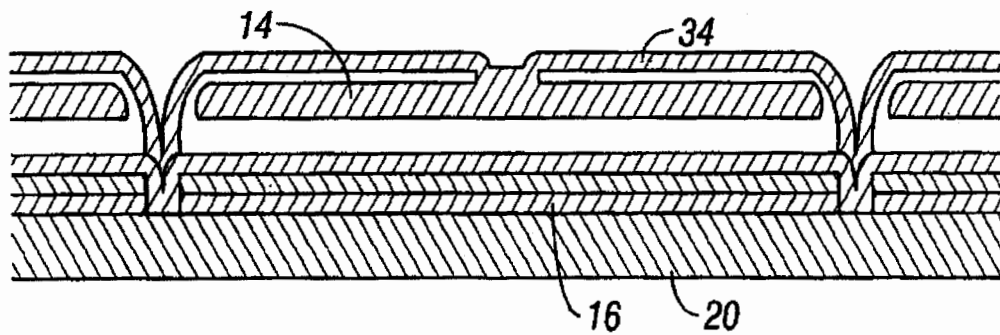


FIG. 6C

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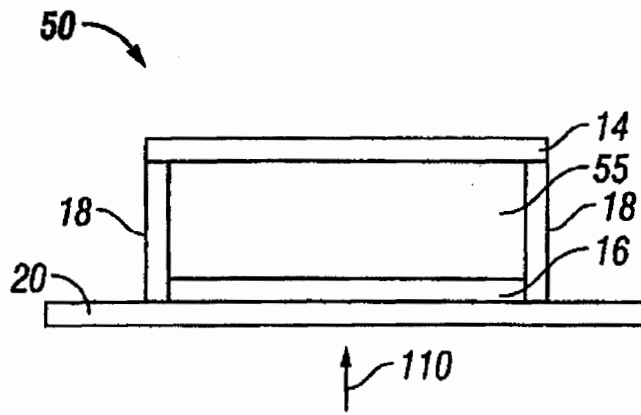


FIG. 7A

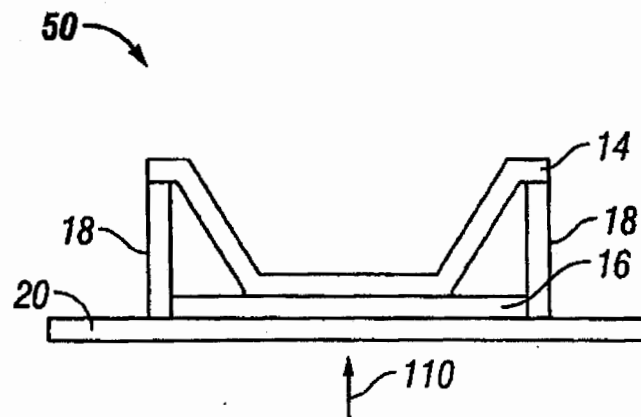


FIG. 7B

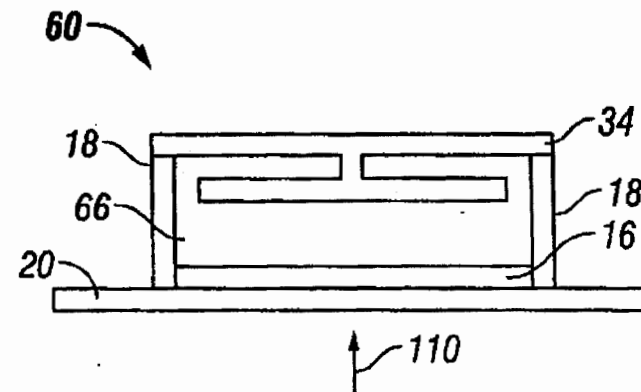


FIG. 7C

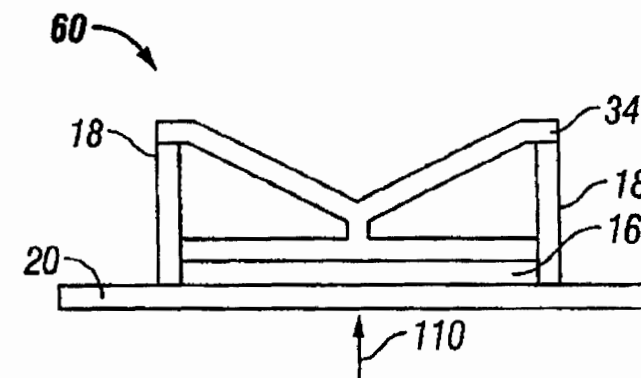


FIG. 7D

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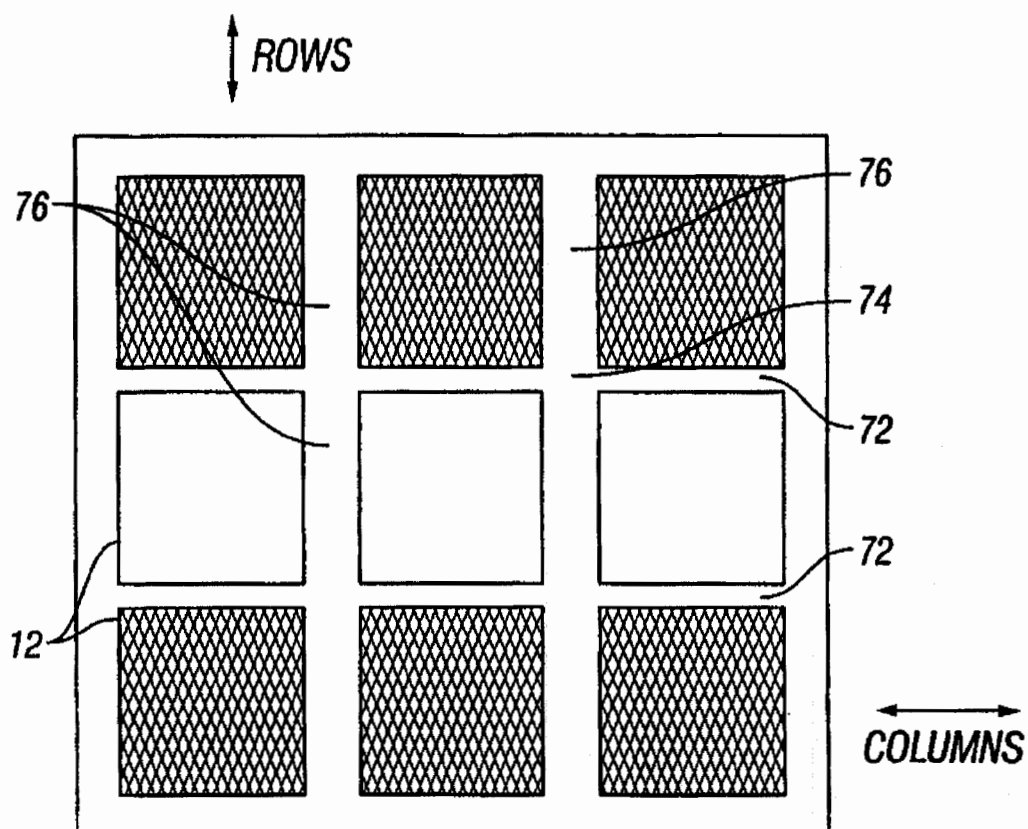


FIG. 8A

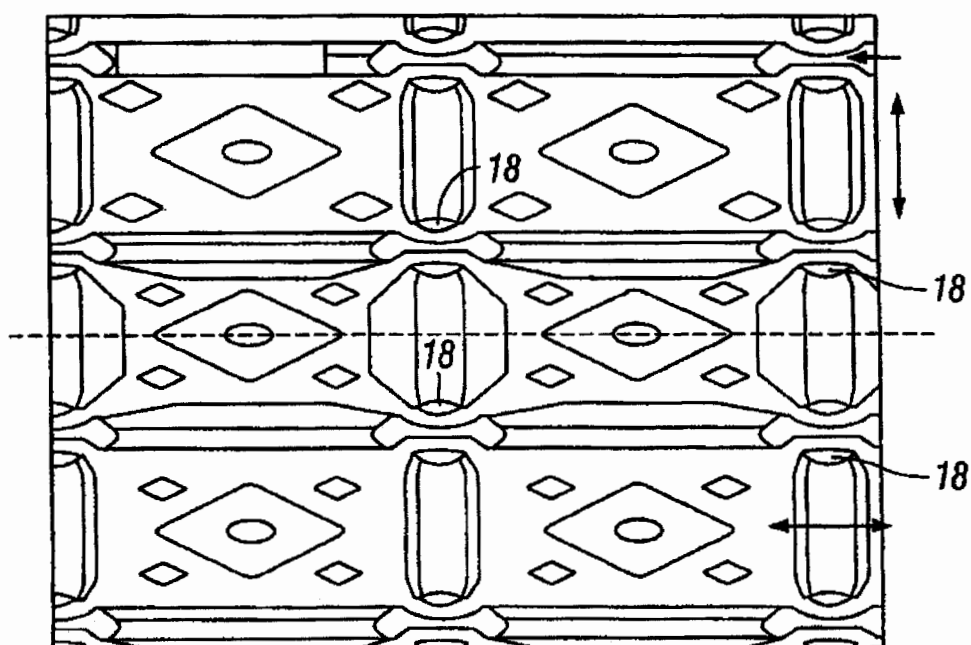


FIG. 8B

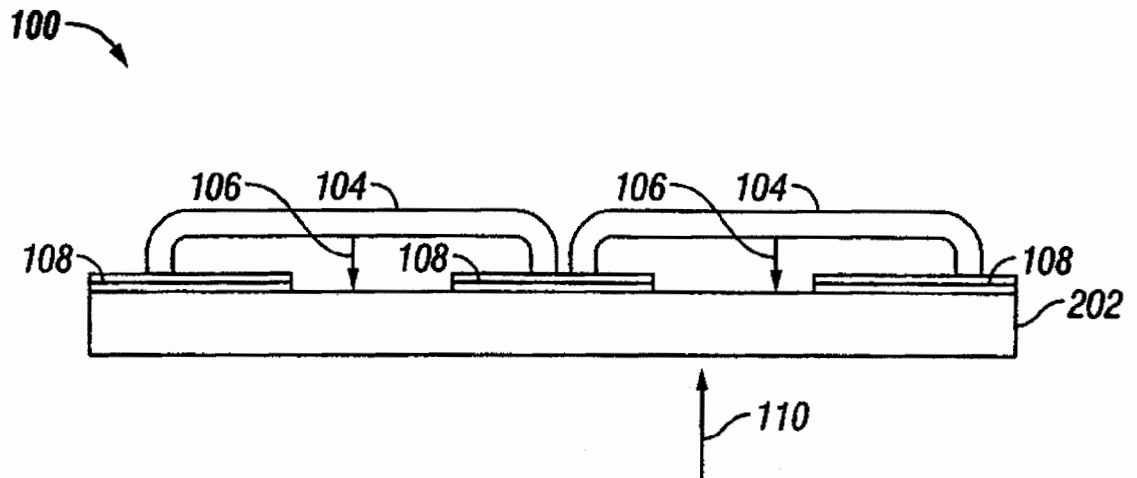


FIG. 9

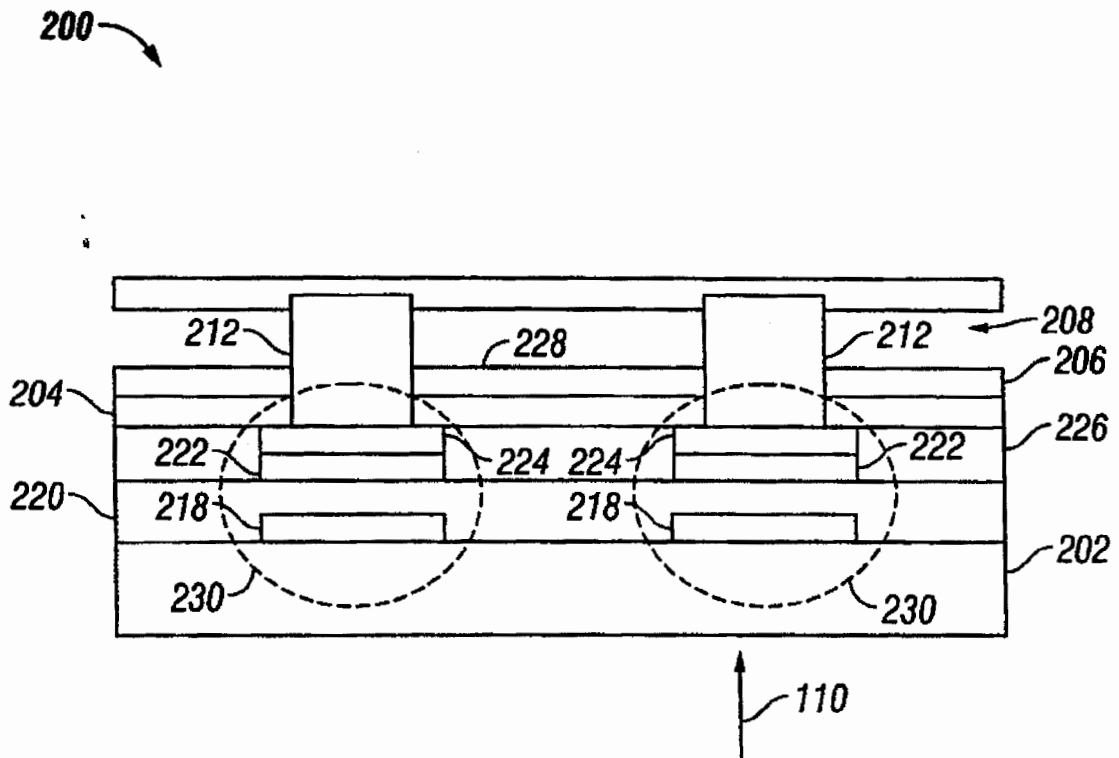


FIG. 10

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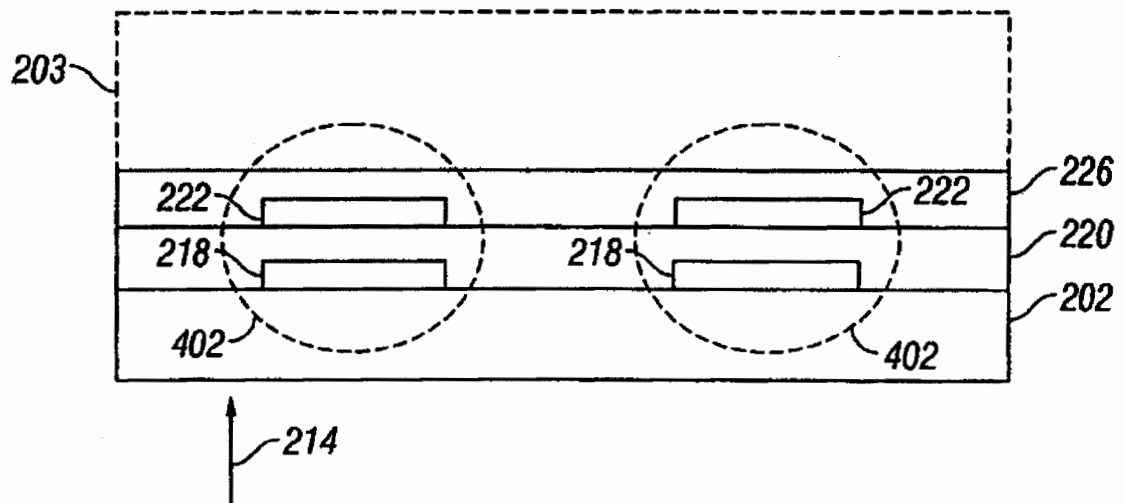


FIG. 11

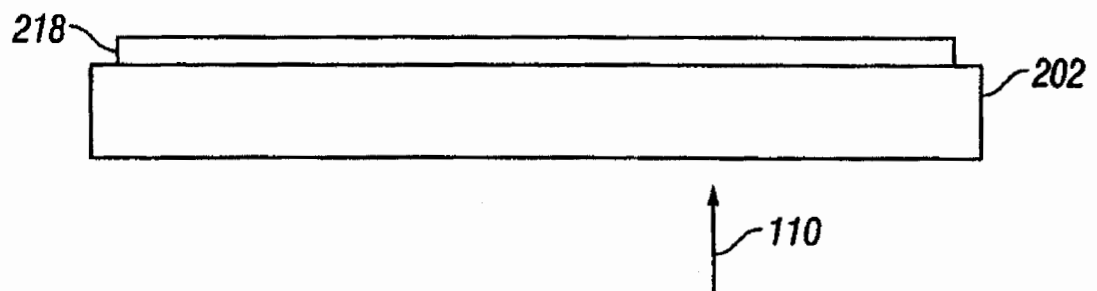


FIG. 12

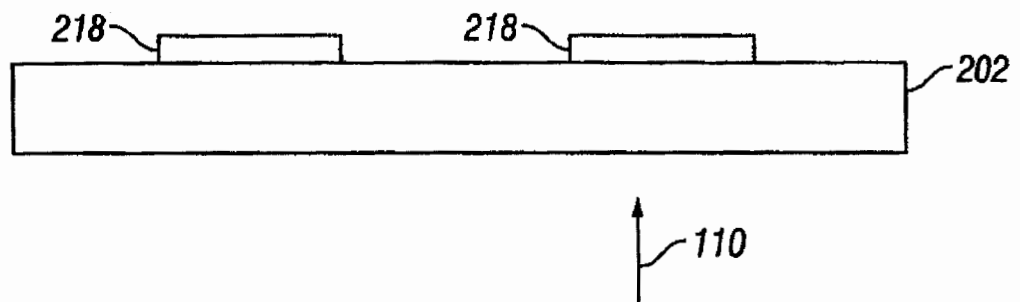


FIG. 13

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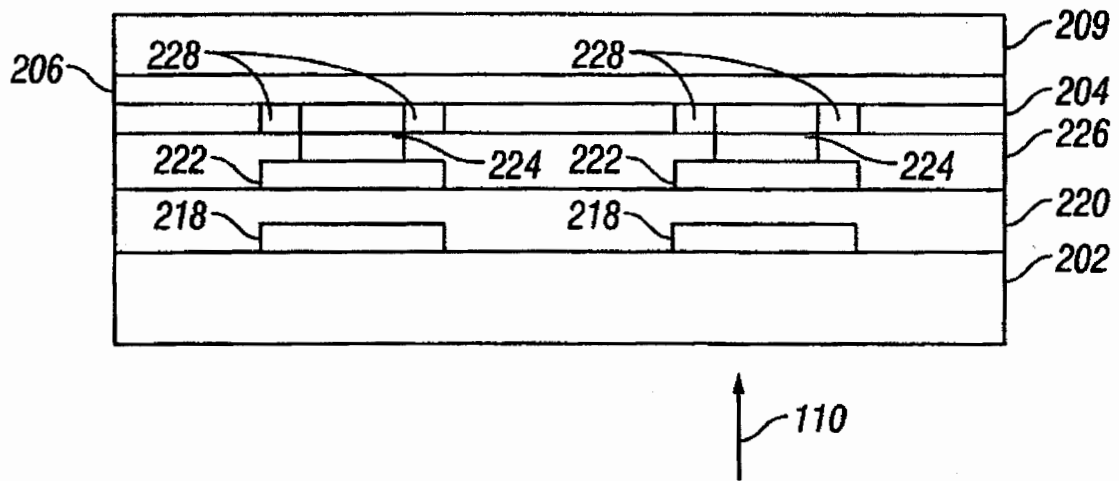


FIG. 14

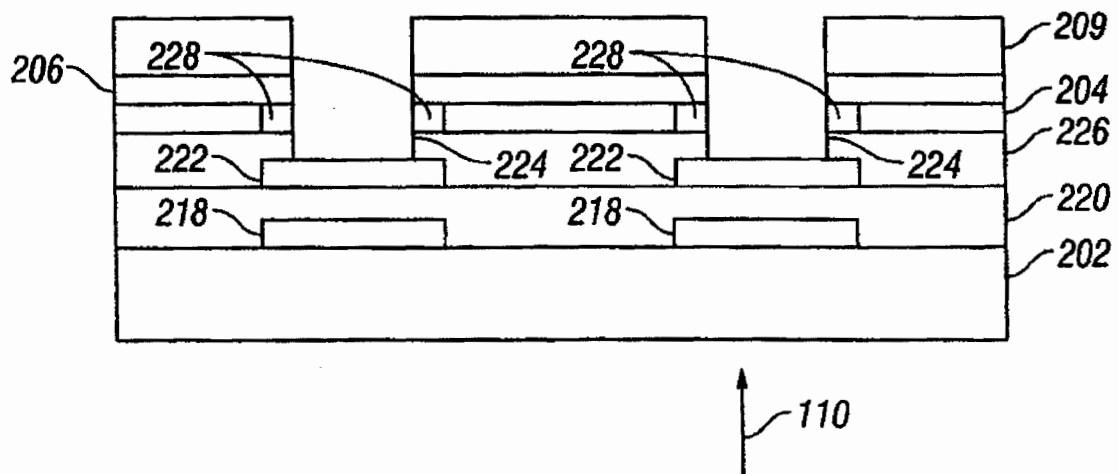


FIG. 15

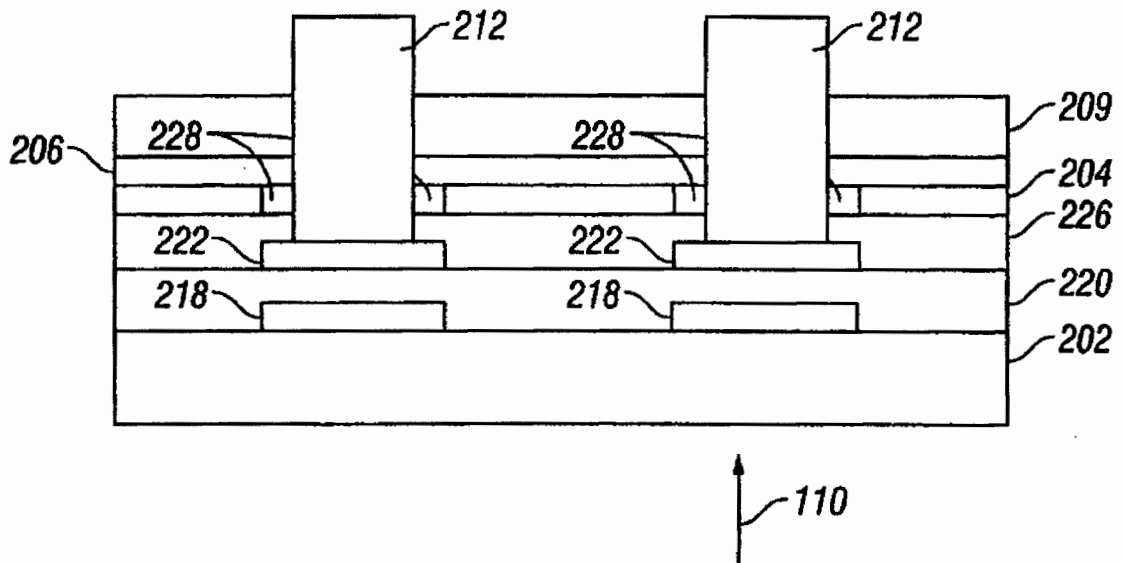


FIG. 16

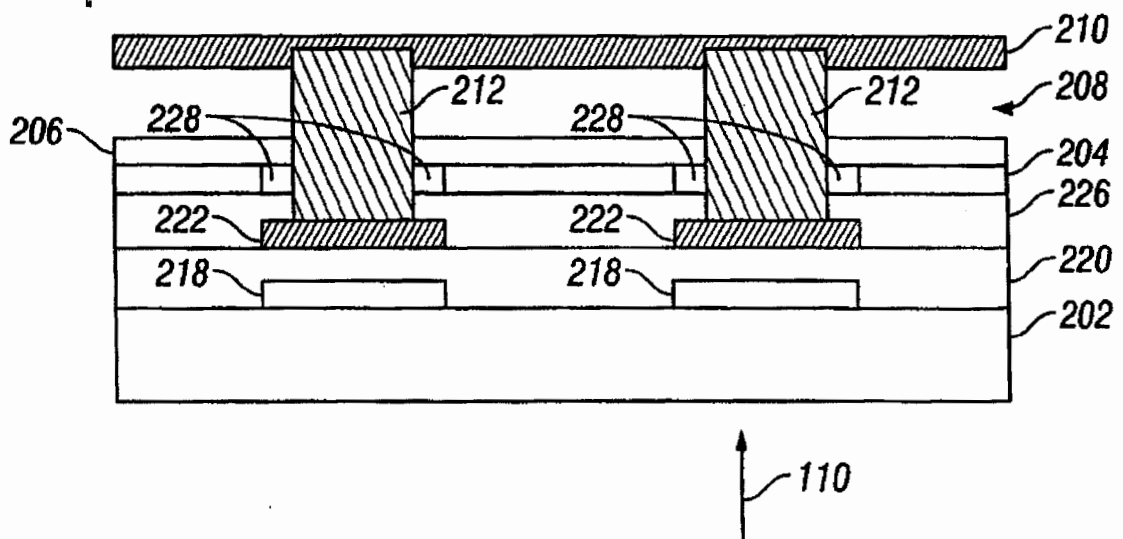


FIG. 17

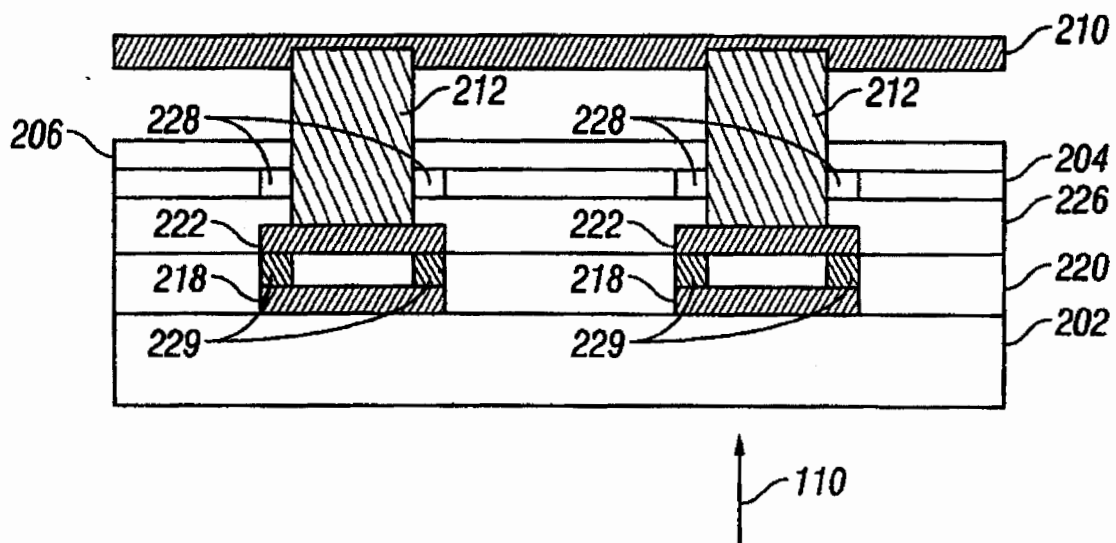


FIG. 18

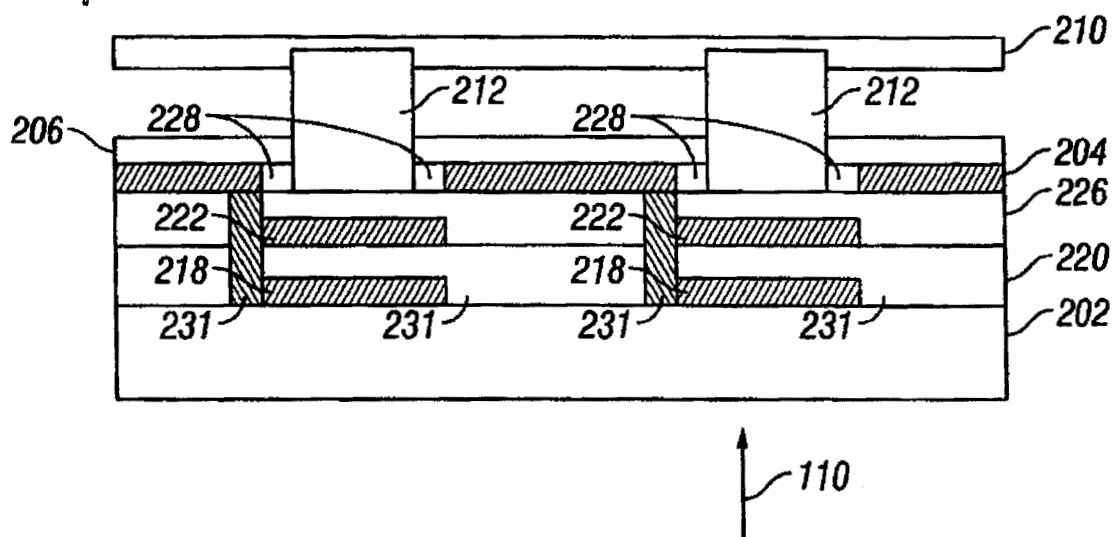


FIG. 19

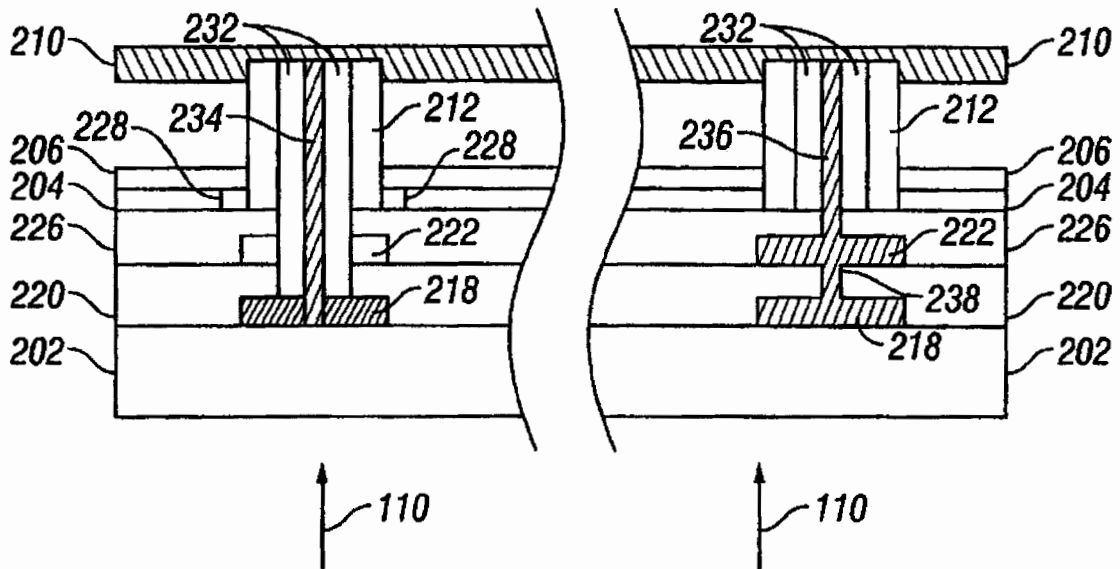


FIG. 20

FIG. 21

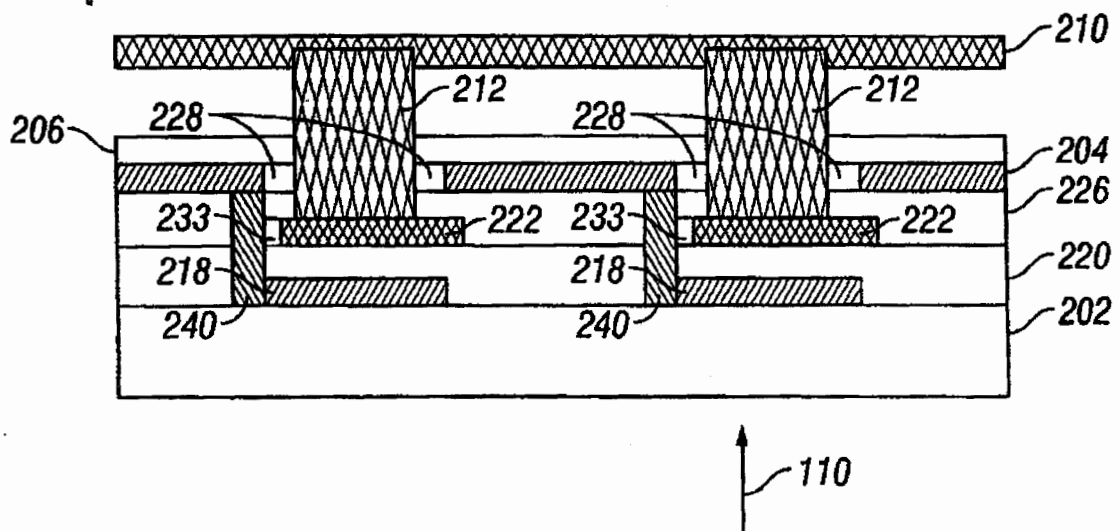


FIG. 22

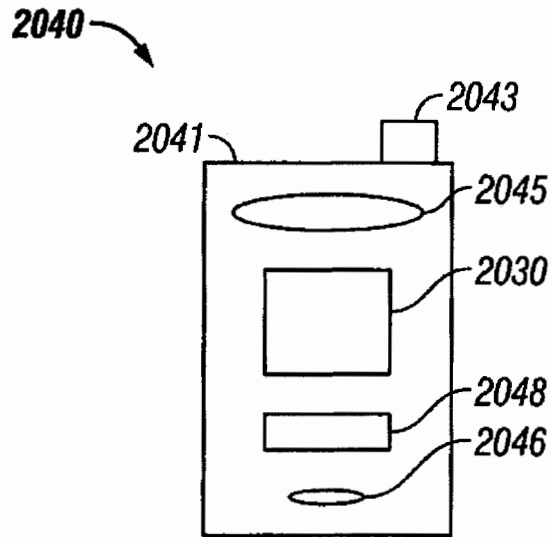


FIG. 23A

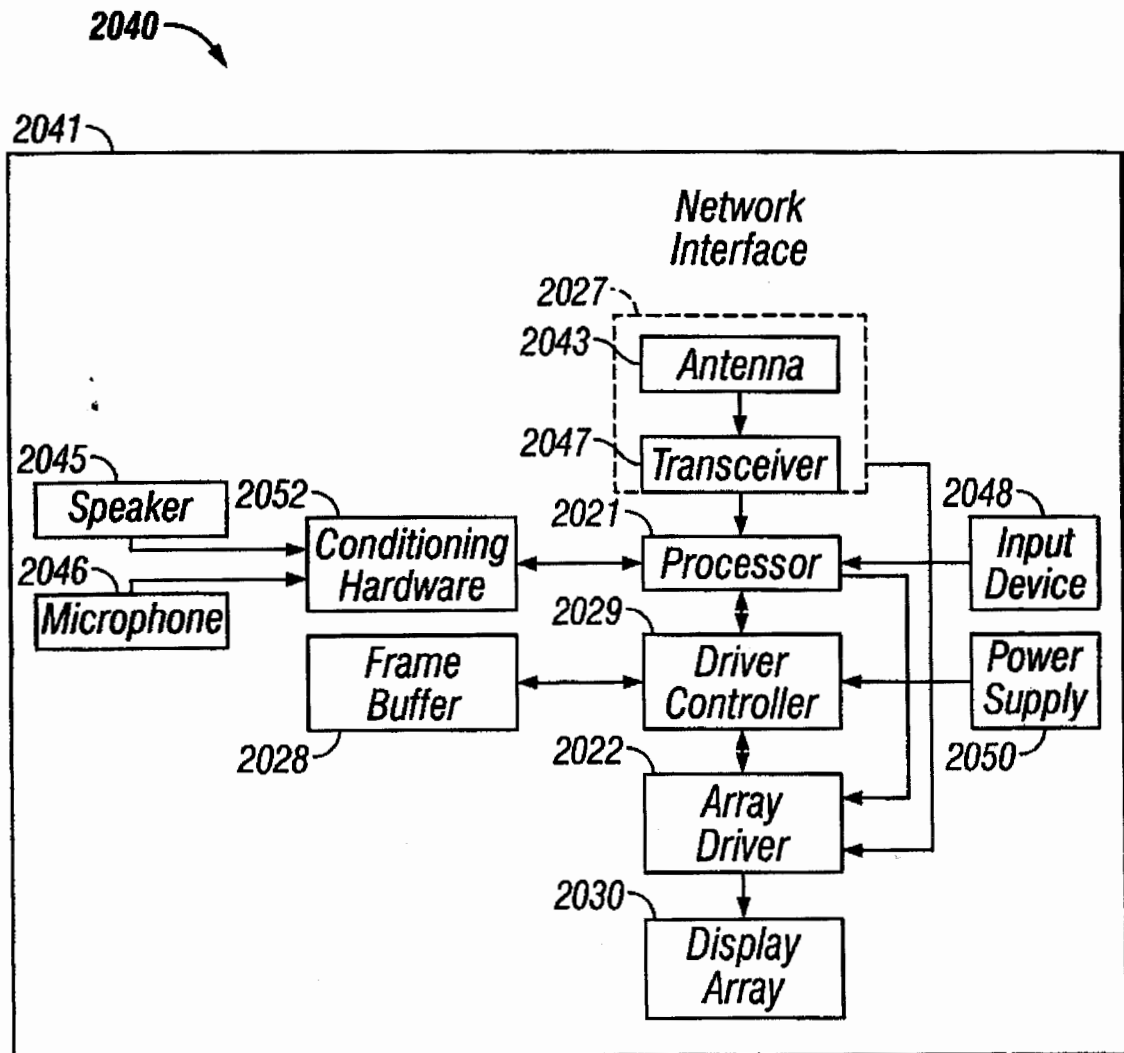


FIG. 23B