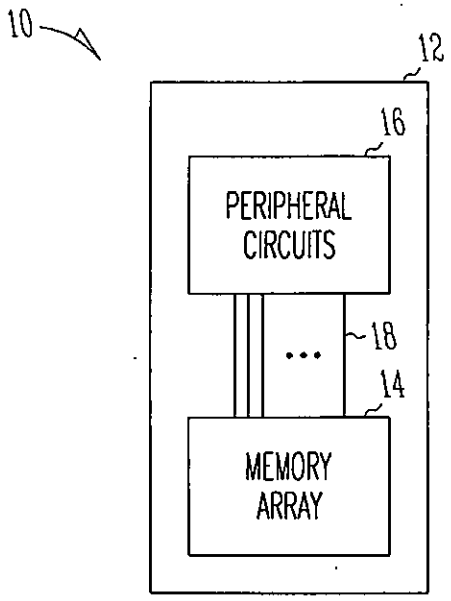


Fig. 1

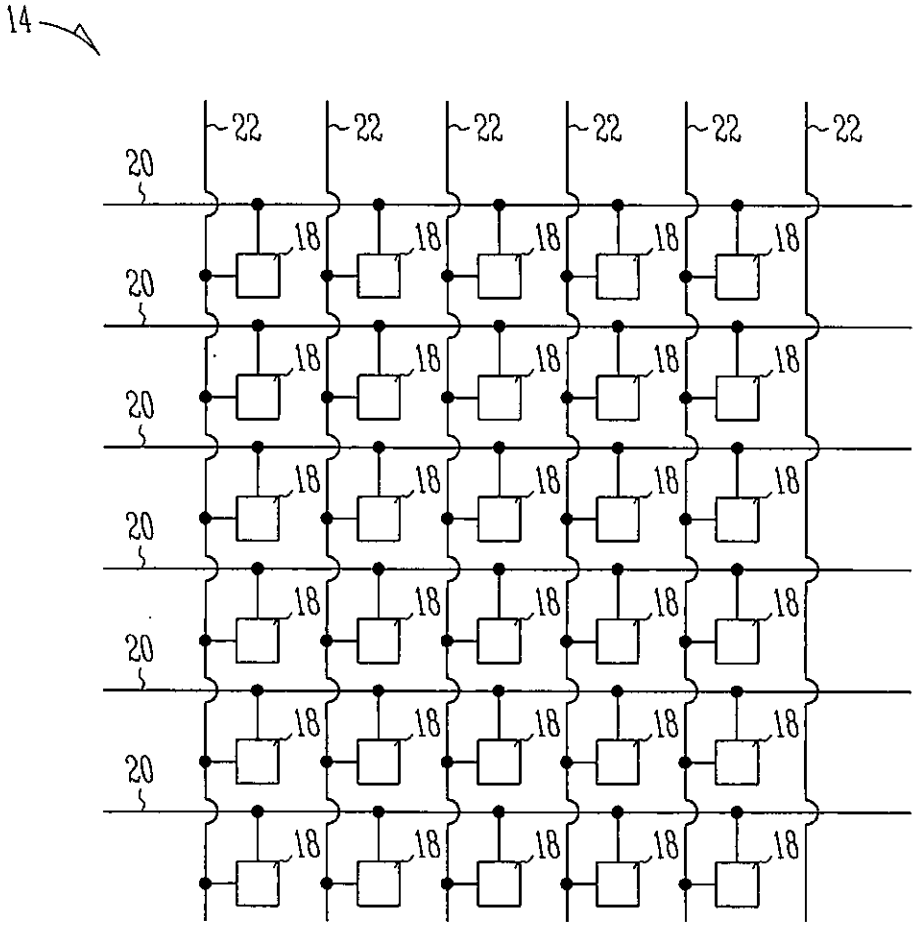


Fig. 2

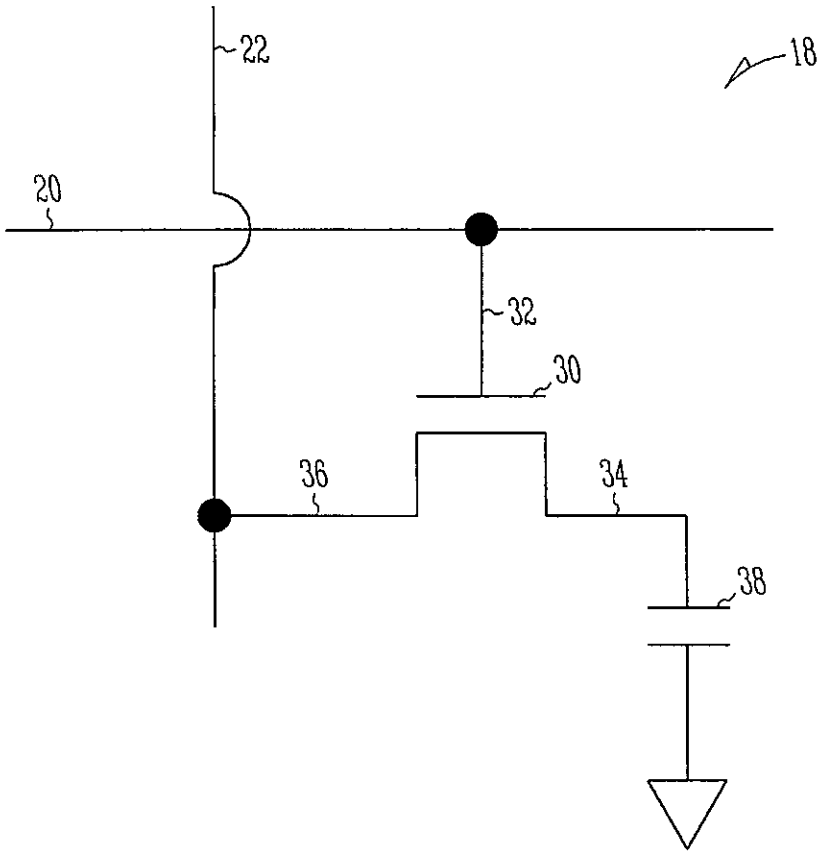


Fig. 3

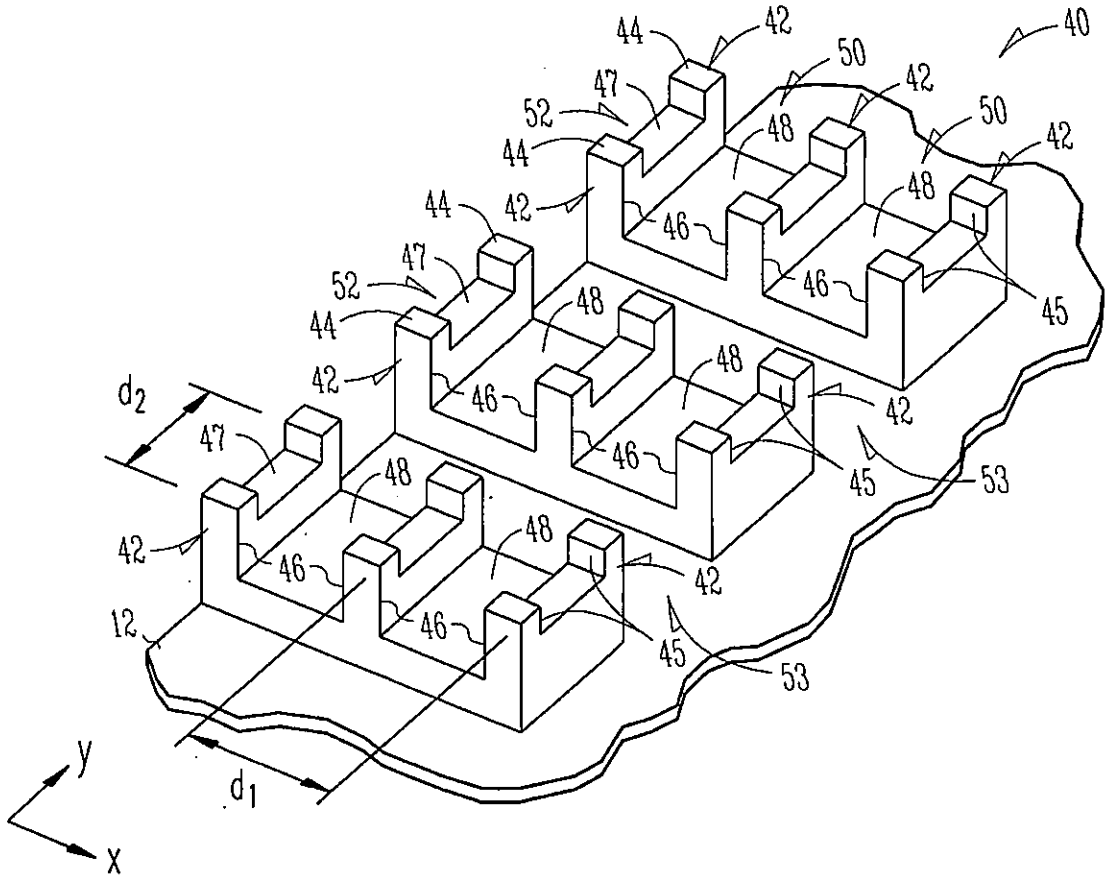


Fig. 4

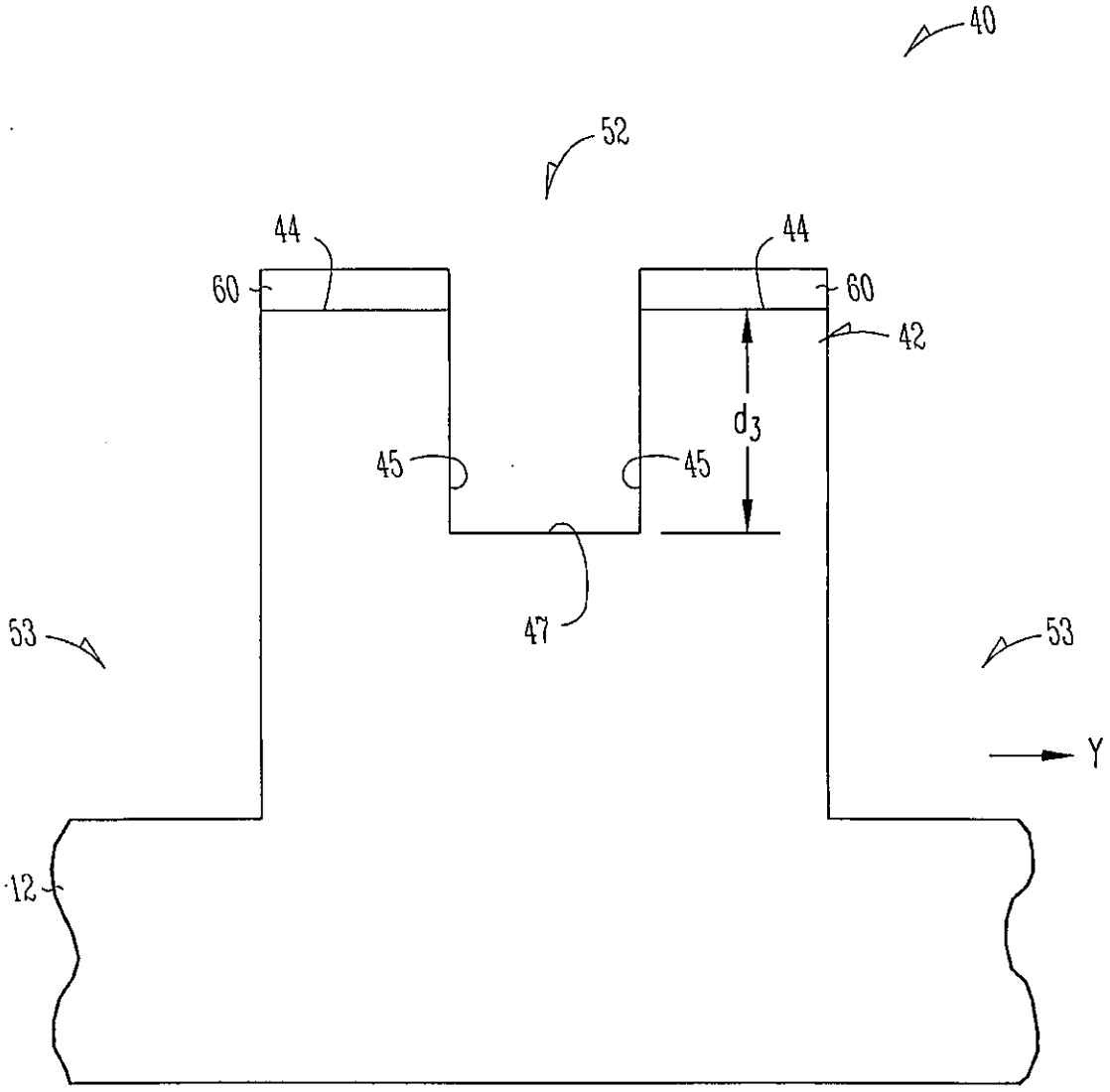


Fig. 5

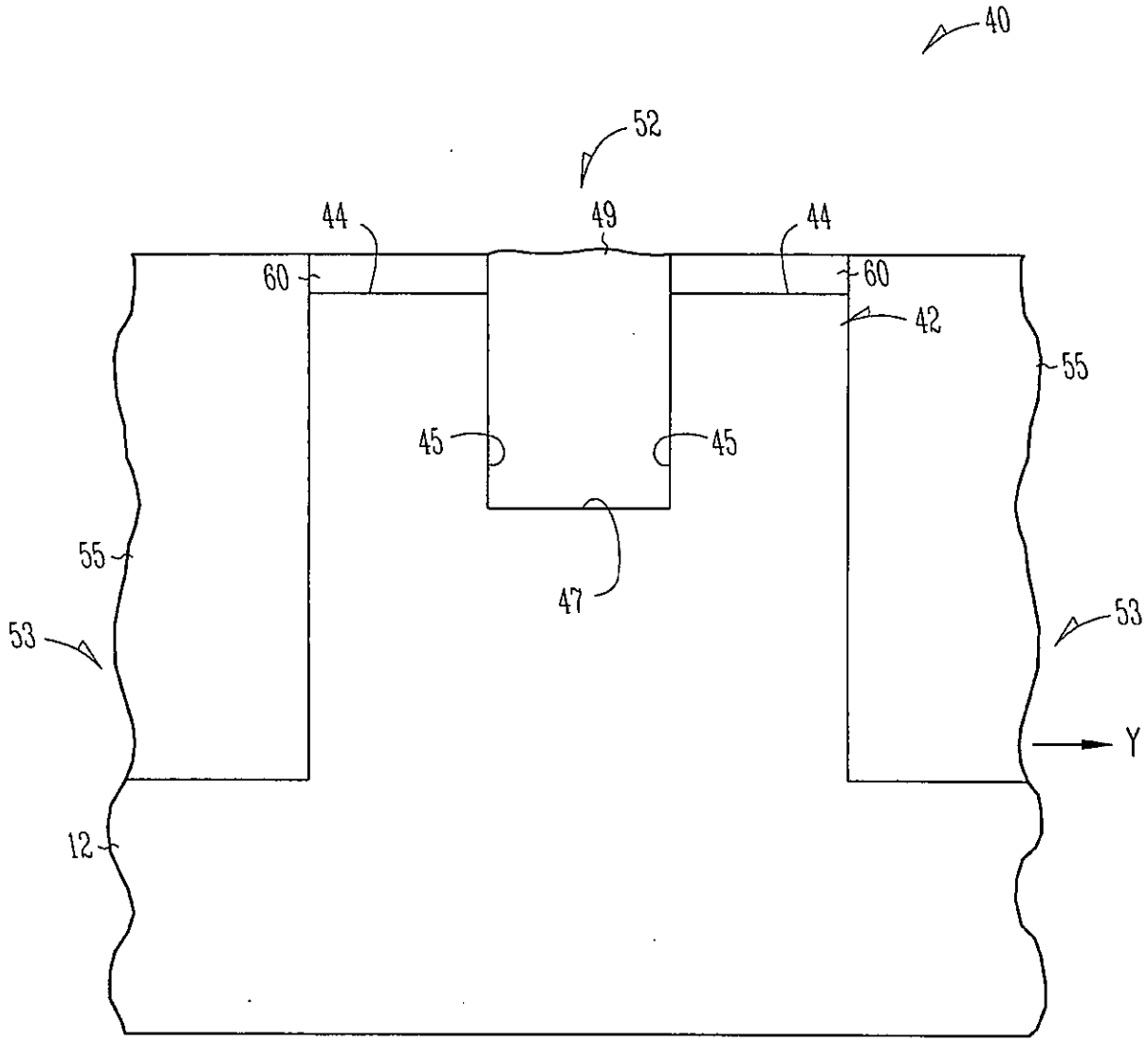


Fig. 6

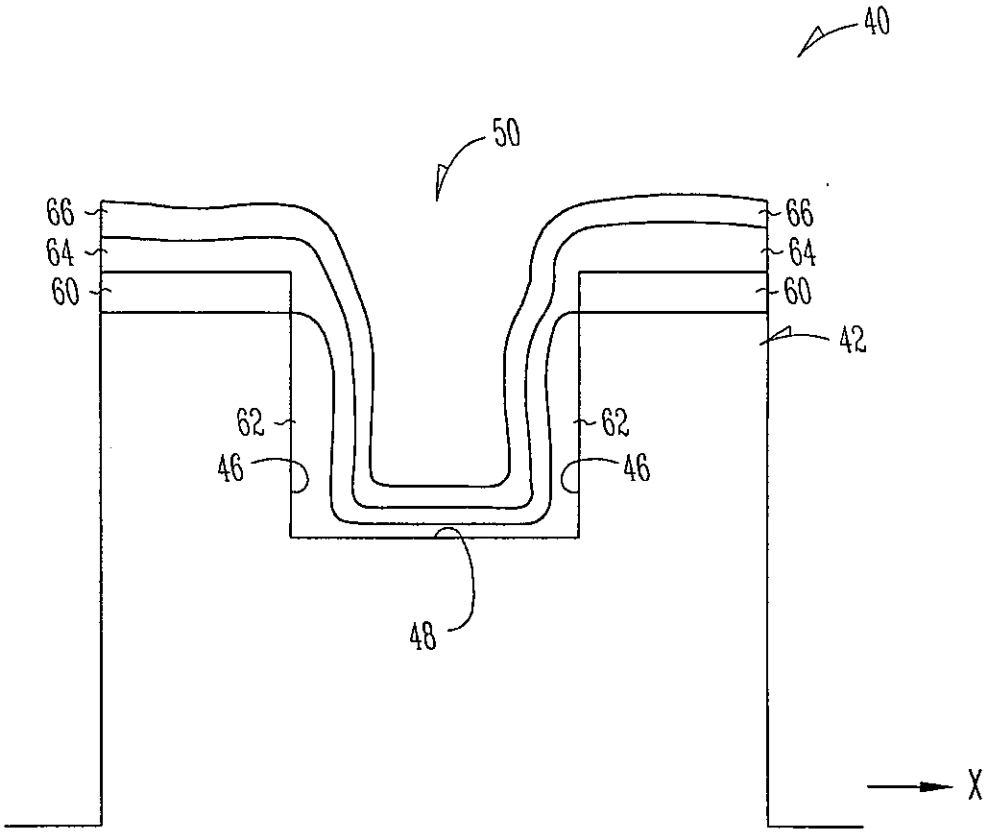


Fig. 7

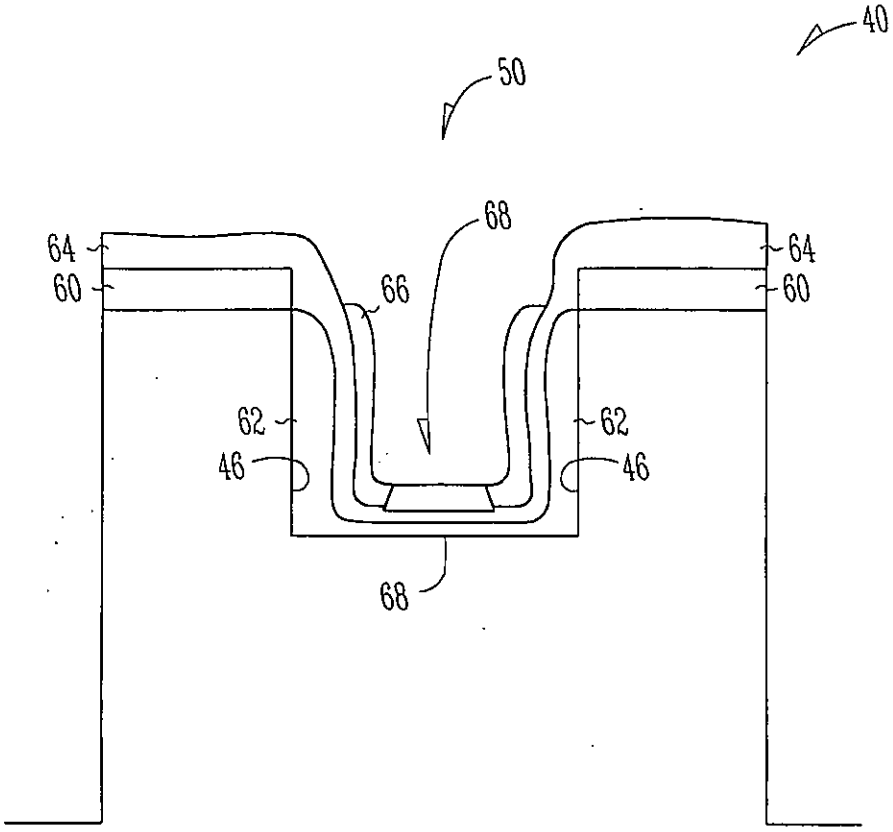


Fig. 8

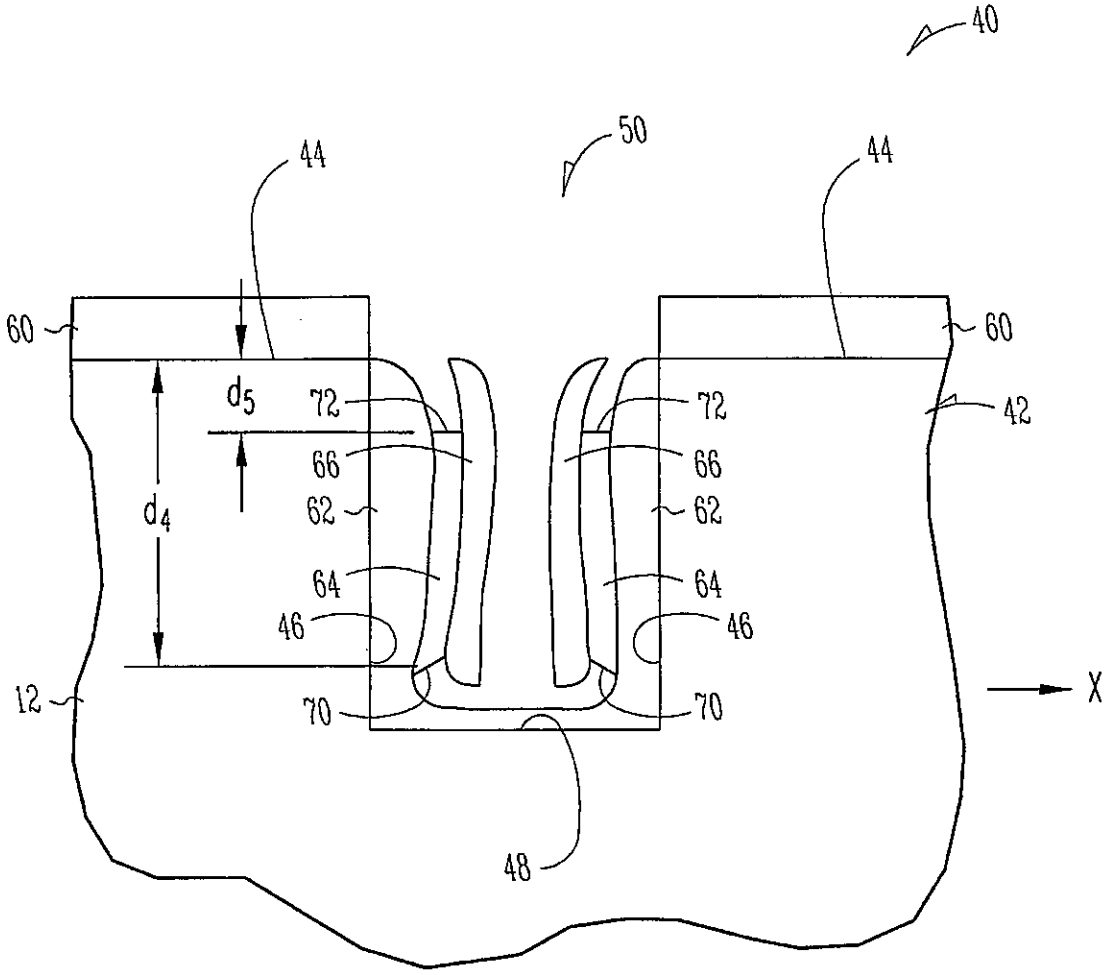


Fig. 9

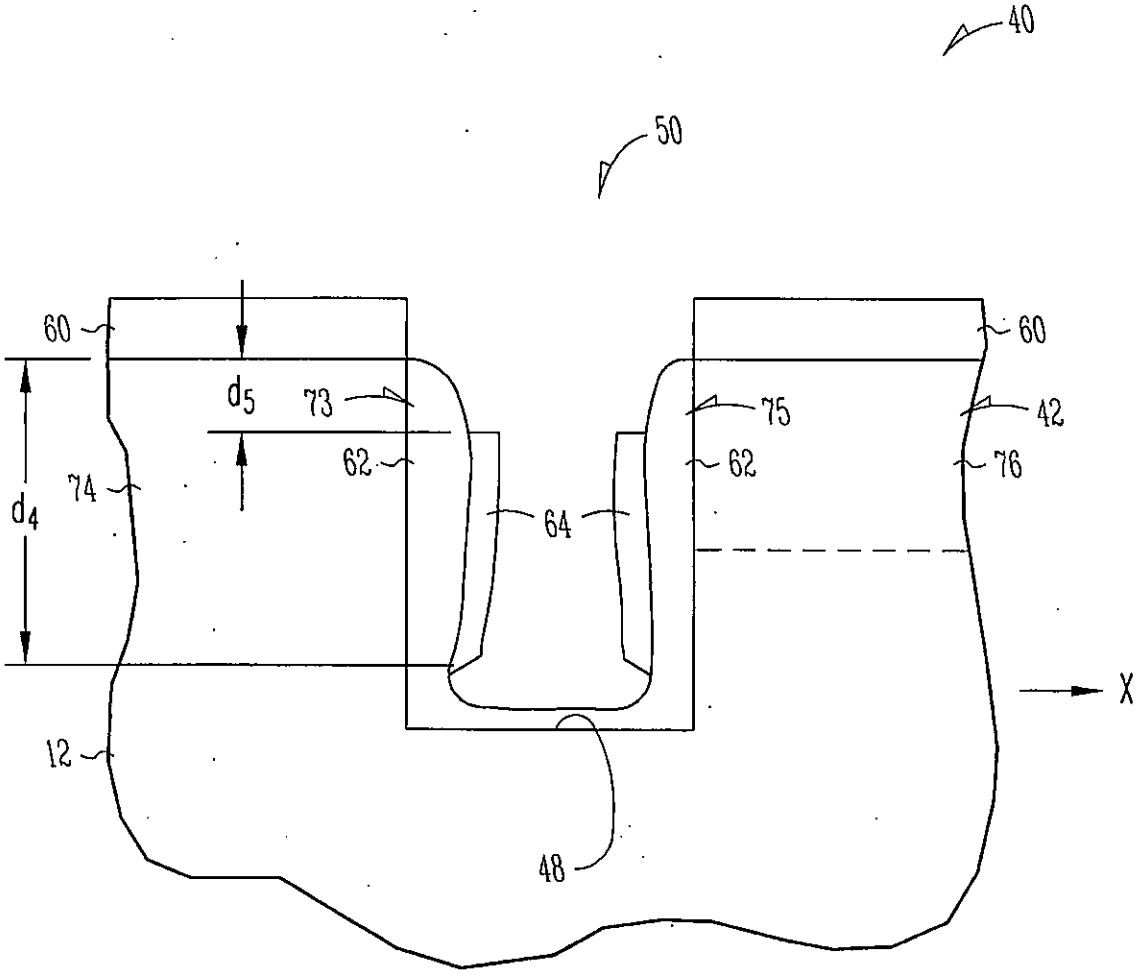


Fig. 10

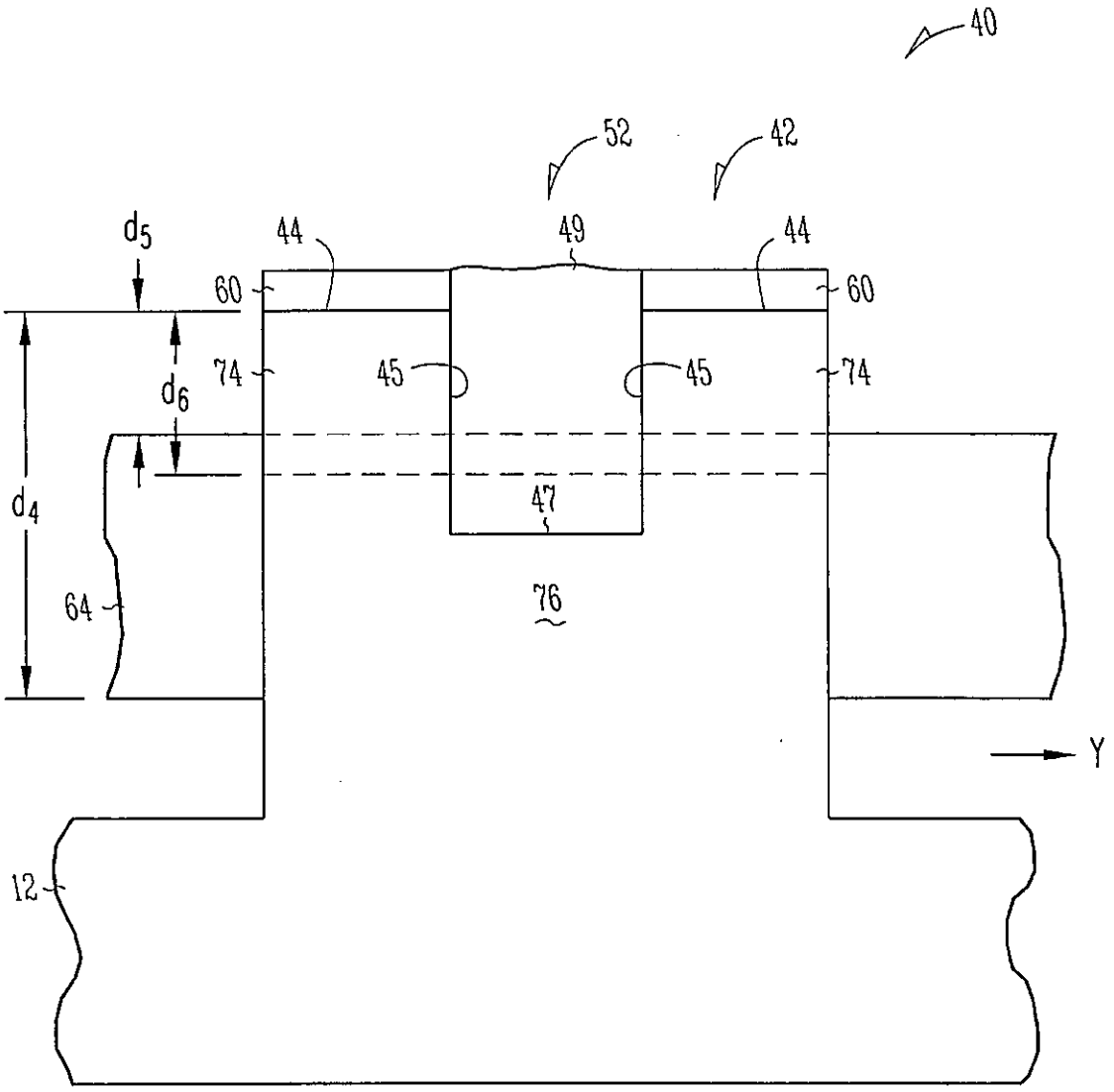


Fig. 11

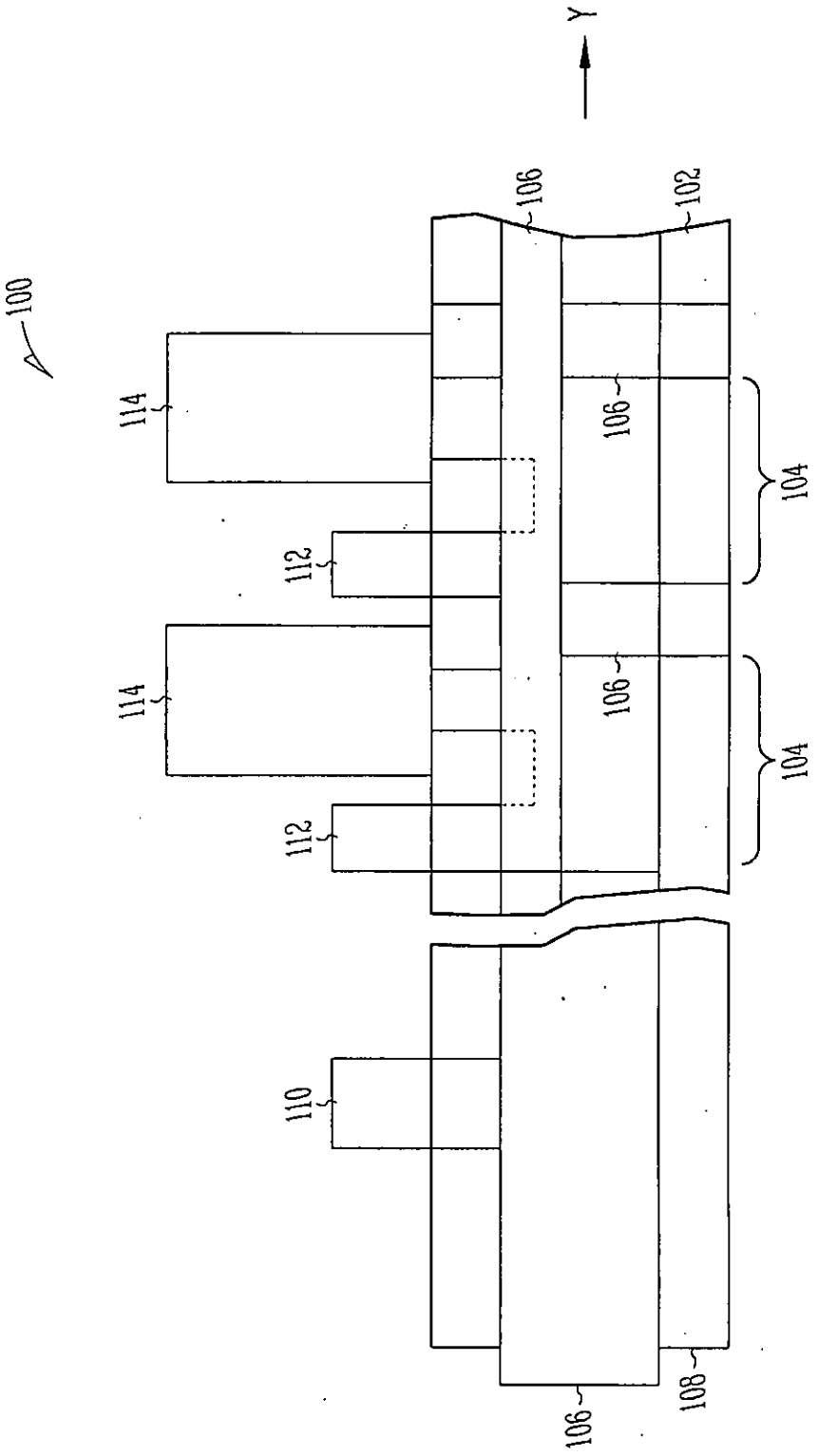


Fig. 12

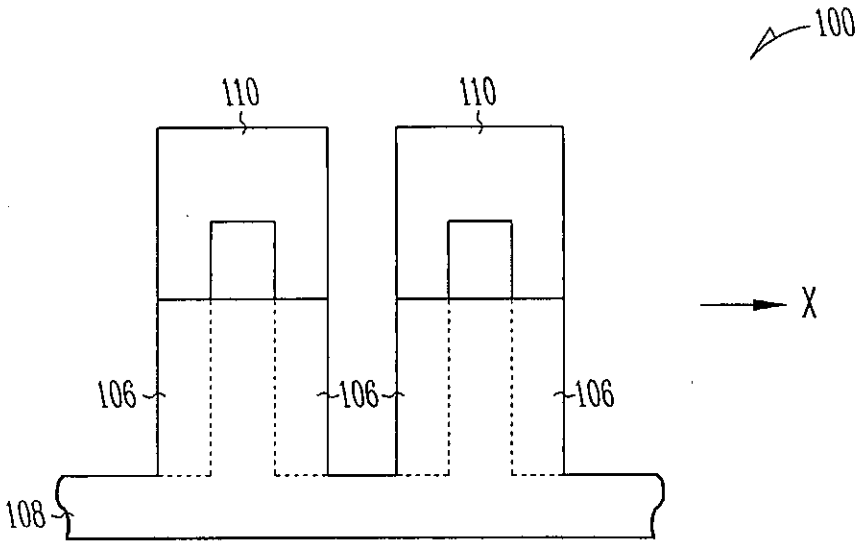

100
110
110
X
106
106
106
108

Fig. 13

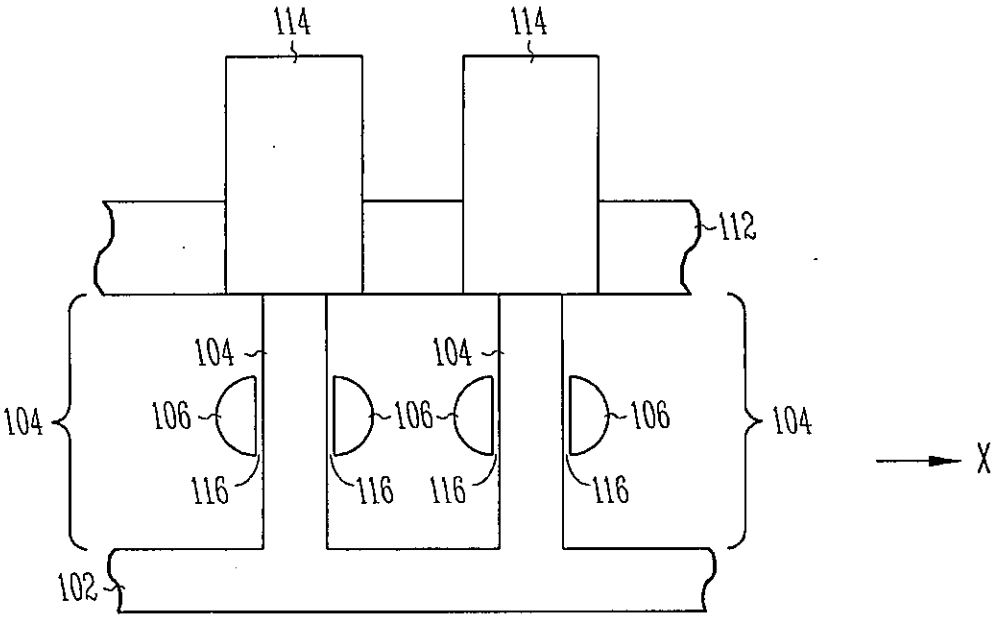

114
114
112
104
104
104
106
106
106
104
116
116
116
116
X
102

Fig. 14

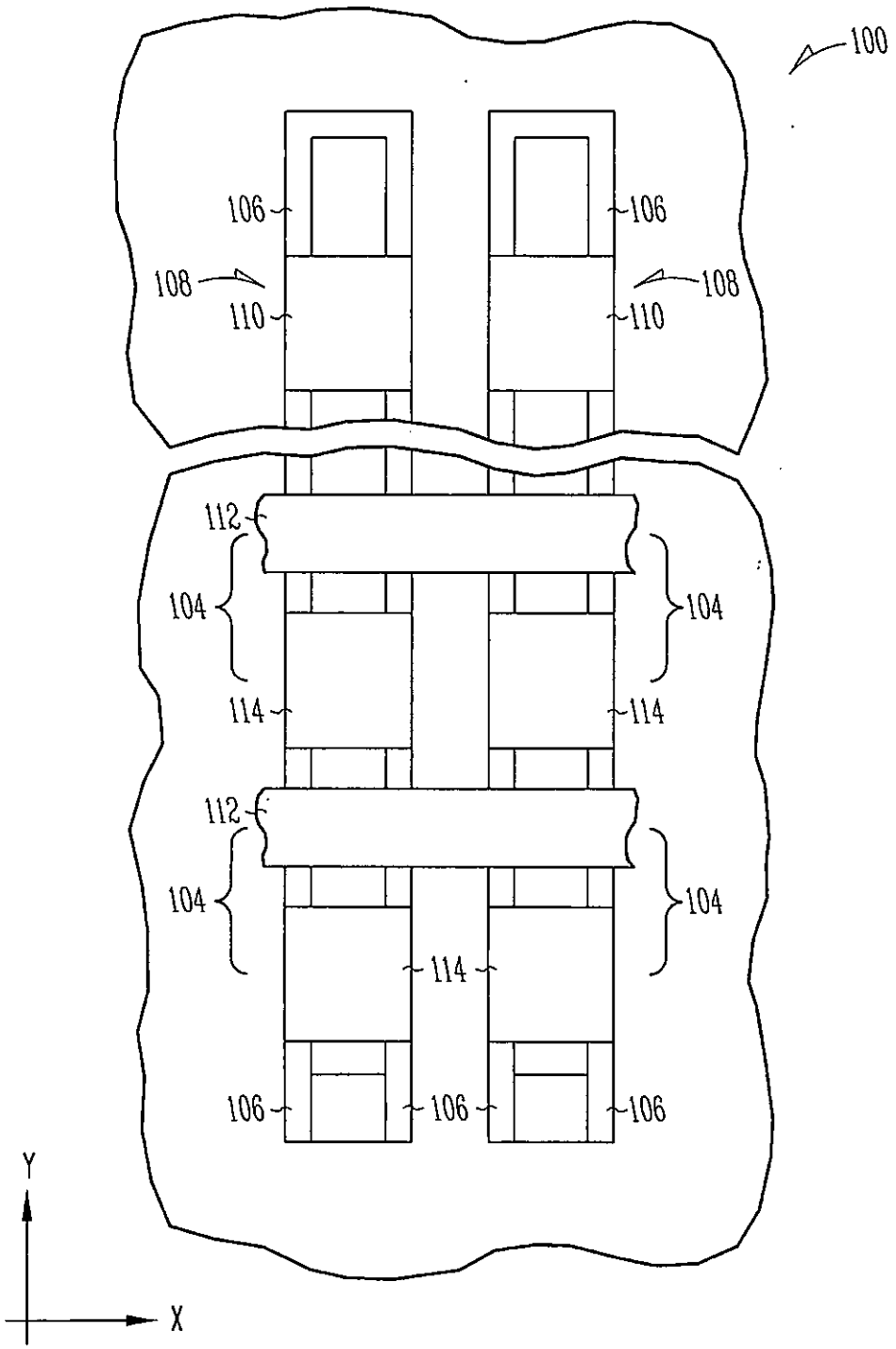

100
106
106
108
108
110
110
112
104
104
114
114
112
104
104
114
106
106
106
Y
X

Fig. 15

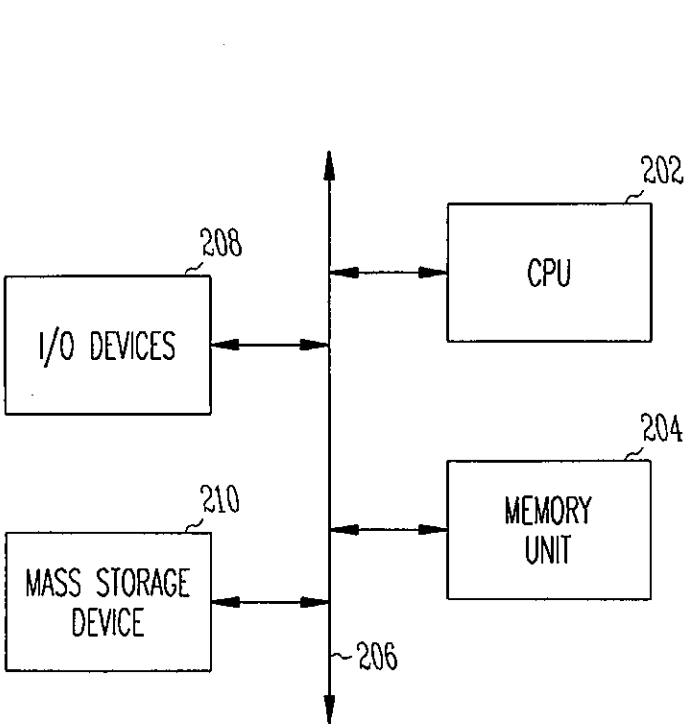

200
202
CPU
208
I/O DEVICES
204
MEMORY UNIT
210
MASS STORAGE DEVICE
206

Fig. 16