

A SYSTEM FOR ENCODING AN IMAGE CONTROL SIGNAL ONTO A PIXEL CLOCK SIGNAL

ABSTRACT

A system for encoding control data onto a clock signal includes at least one clock cycle in the clock signal; a first transition in the at least one clock cycle, the first transition is from a first voltage level to a second voltage level, the first transition is in a first location in the at least one clock cycle; a second transition in the at least one clock cycle, the second transition is from the second voltage level to the first voltage level, the second transition has a variable location in the clock cycle; and an encoder circuit 144 for positioning the second transition in the variable location in response to the control data.

Figure 1