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(54) Title: A CONTROLLER FOR PROVIDING TIMING SIGNALS FOR VIDEO DATA																					
(57) Abstract																					
A controller for synchronising video signals for display on a TV screen comprising: a horizontal input storage means for receiving a horizontal synchronising signal of a first sequence for computer generated image data; a vertical input storage means for receiving a vertical synchronising signal of a second sequence for computer generated image data; a first processor means for generating horizontal synchronising signals of a third sequence and a vertical synchronising signal of a fourth sequence; and a second processing means for combining the horizontal synchronising signal of the first sequence and the horizontal synchronising of the third sequence to generate a horizontal synchronising signal of a fifth sequence and for combining the vertical synchronising signal of the second sequence with the vertical synchronising signal of the fourth sequence to generate a vertical synchronising signal of a sixth sequence; wherein the horizontal synchronising signal of the fifth sequence and the vertical synchronising signal of the sixth sequence generated by the second processor means are arranged to provide timing sequences for images data generated by a computer whereby the image data can be displayed on a TV screen.																					
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