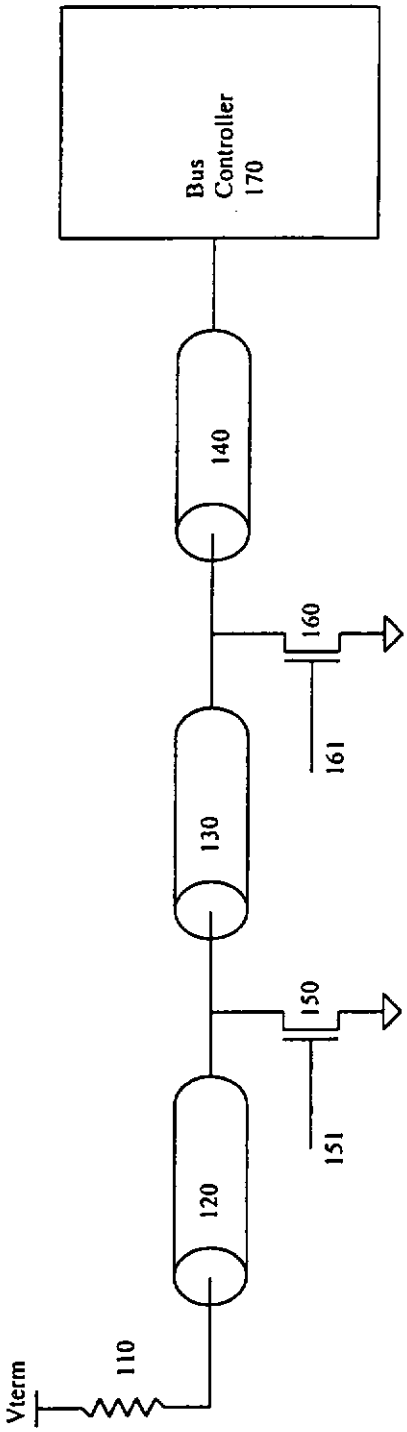


Figure 1 – Prior Art

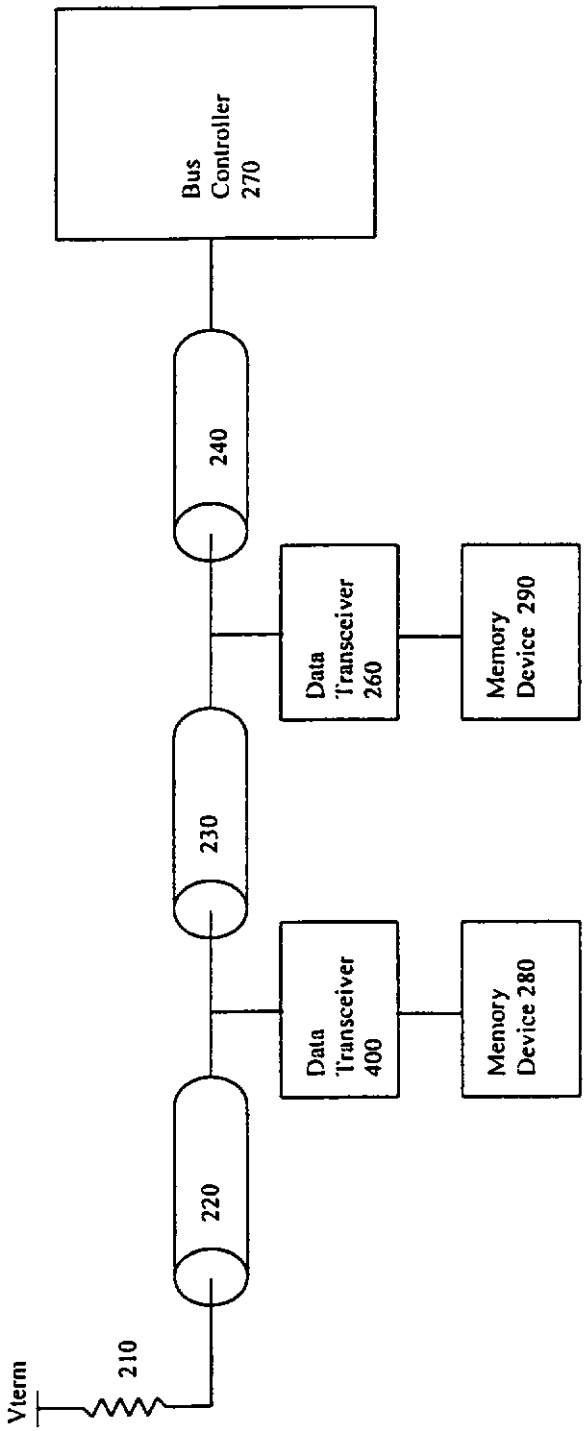

Bus Controller 270
240
Data Transceiver 260
Memory Device 290
230
Data Transceiver 400
Memory Device 280
220
210
Vterm

Figure 2

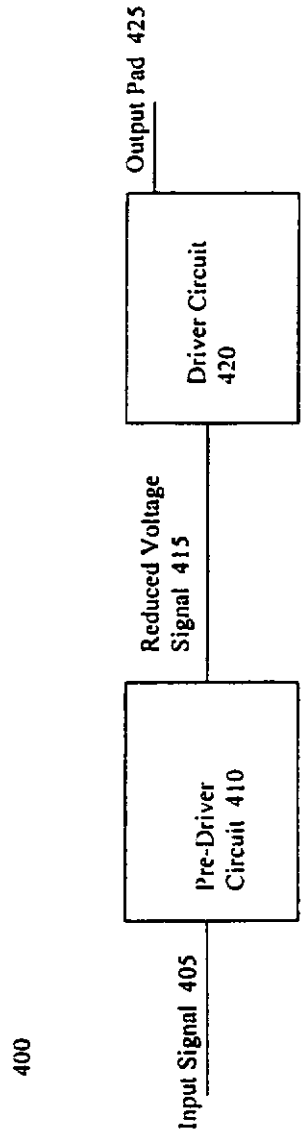

Output Pad 425
Driver Circuit 420
Reduced Voltage Signal 415
Pre-Driver Circuit 410
Input Signal 405
400

Figure 3

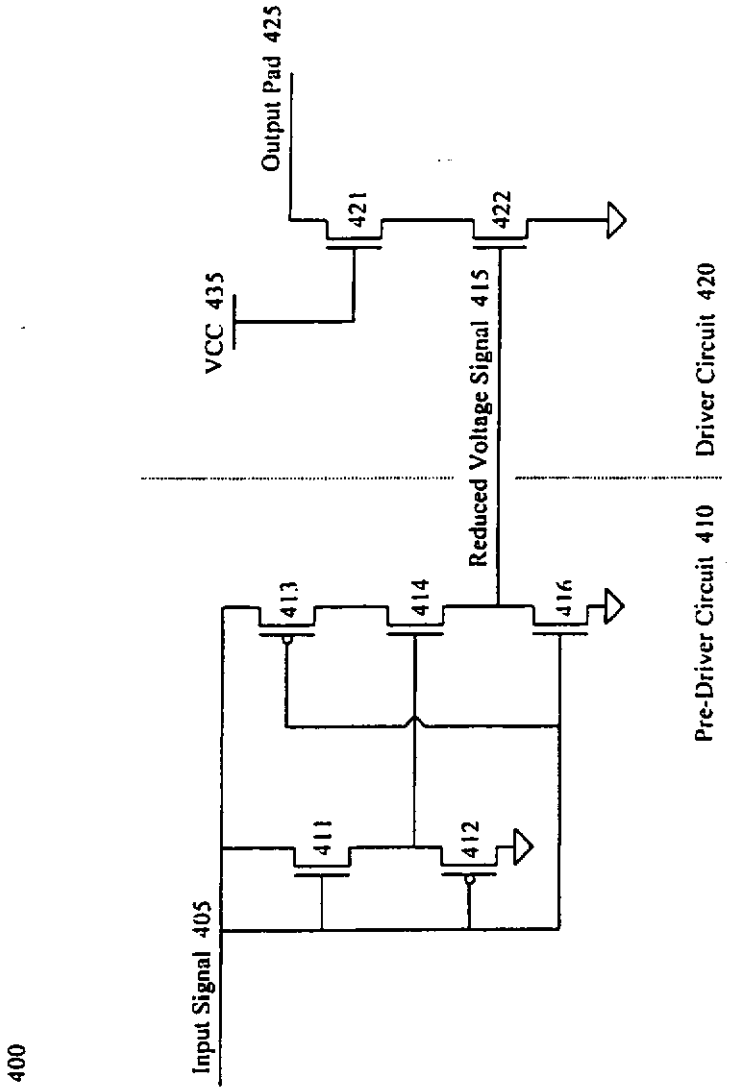

Output Pad 425
421
422
VCC 435
Reduced Voltage Signal 415
Driver Circuit 420
Pre-Driver Circuit 410
413
414
416
411
412
Input Signal 405
400

Figure 4

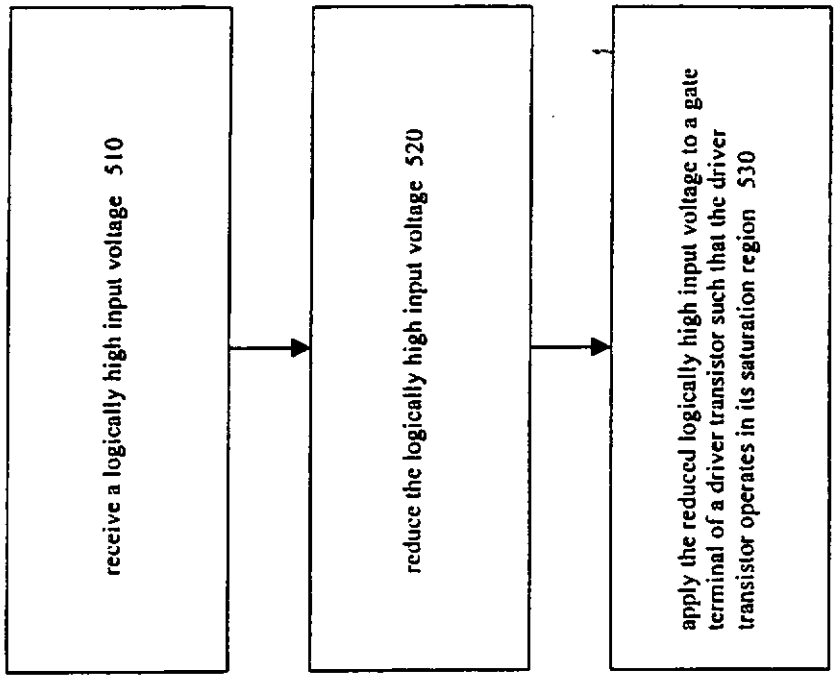

receive a logically high input voltage 510
reduce the logically high input voltage 520
apply the reduced logically high input voltage to a gate terminal of a driver transistor such that the driver transistor operates in its saturation region 530

Figure 5