

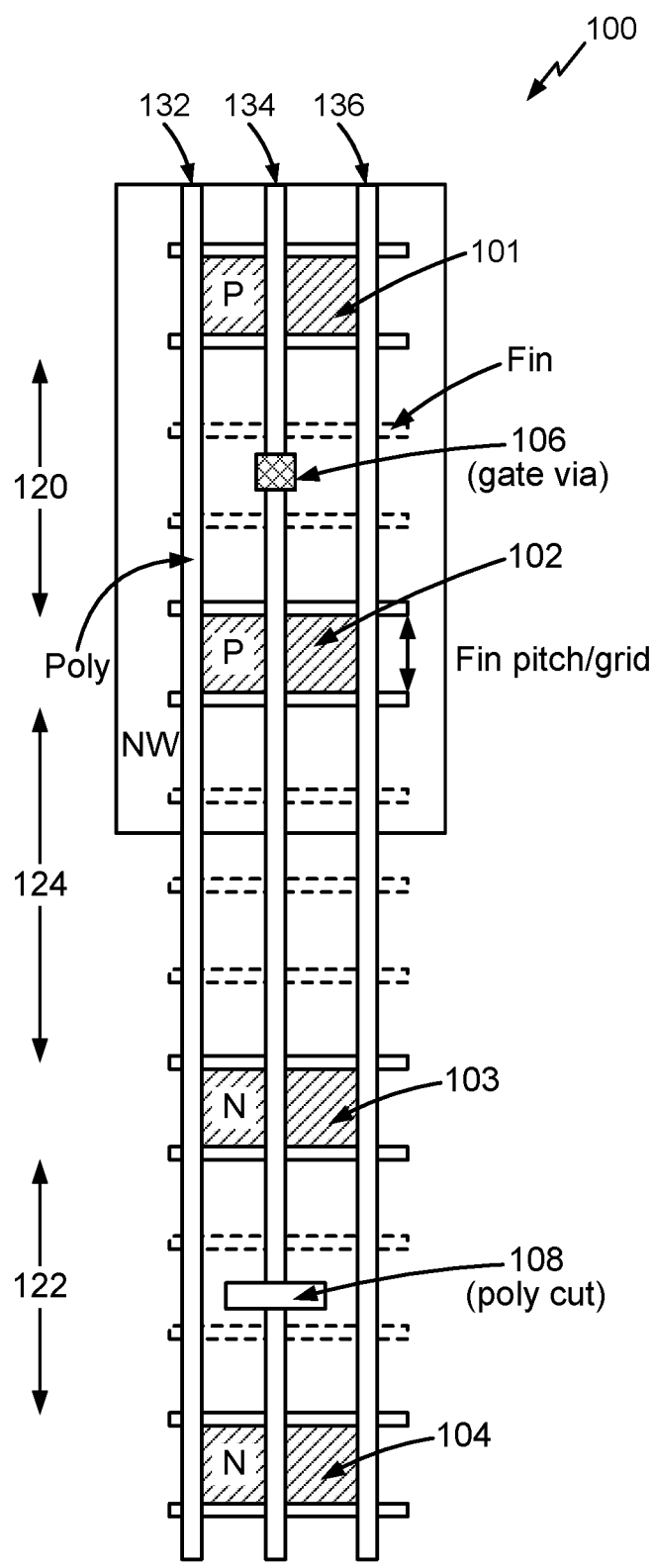


FIG. 1

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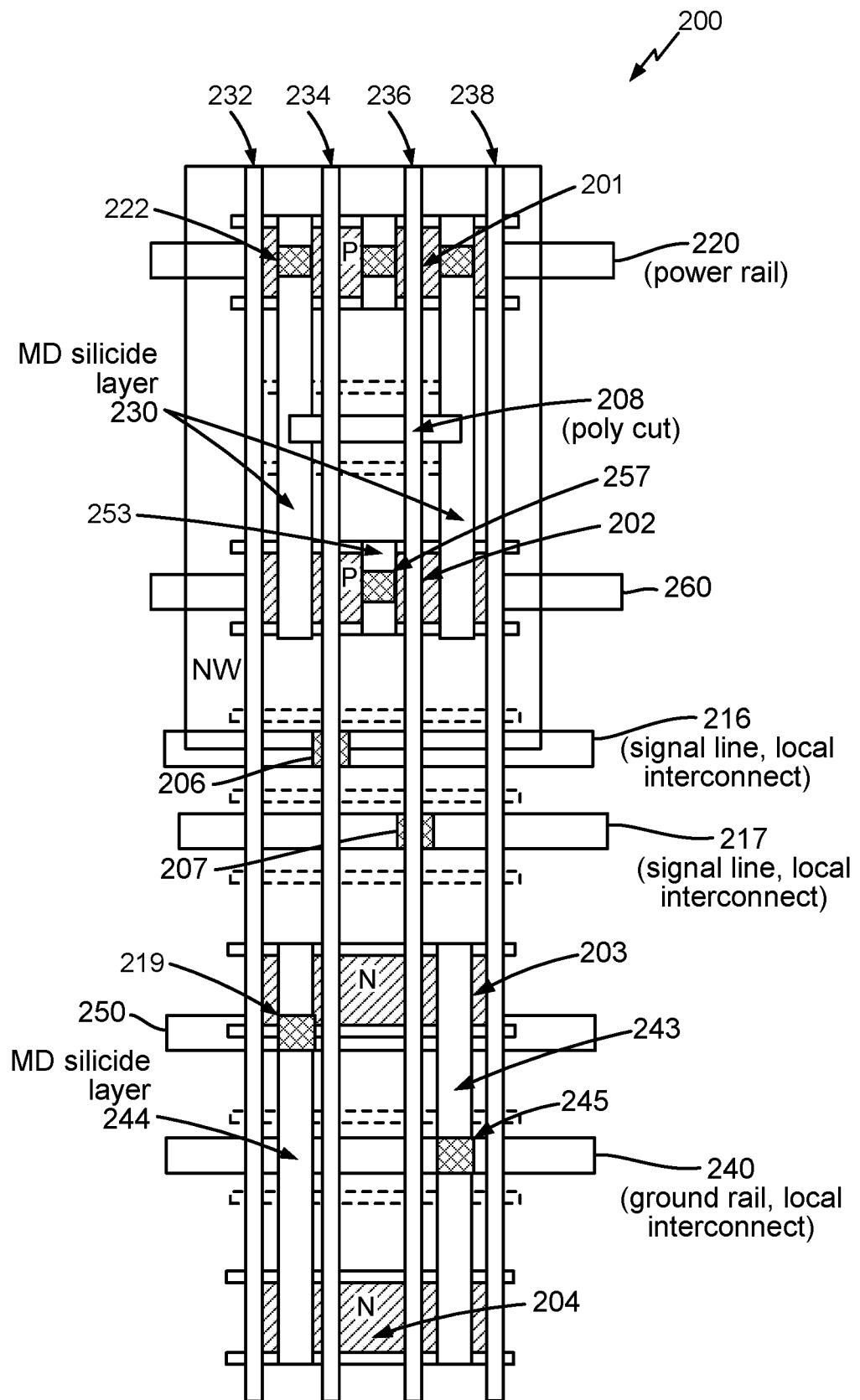


FIG. 2

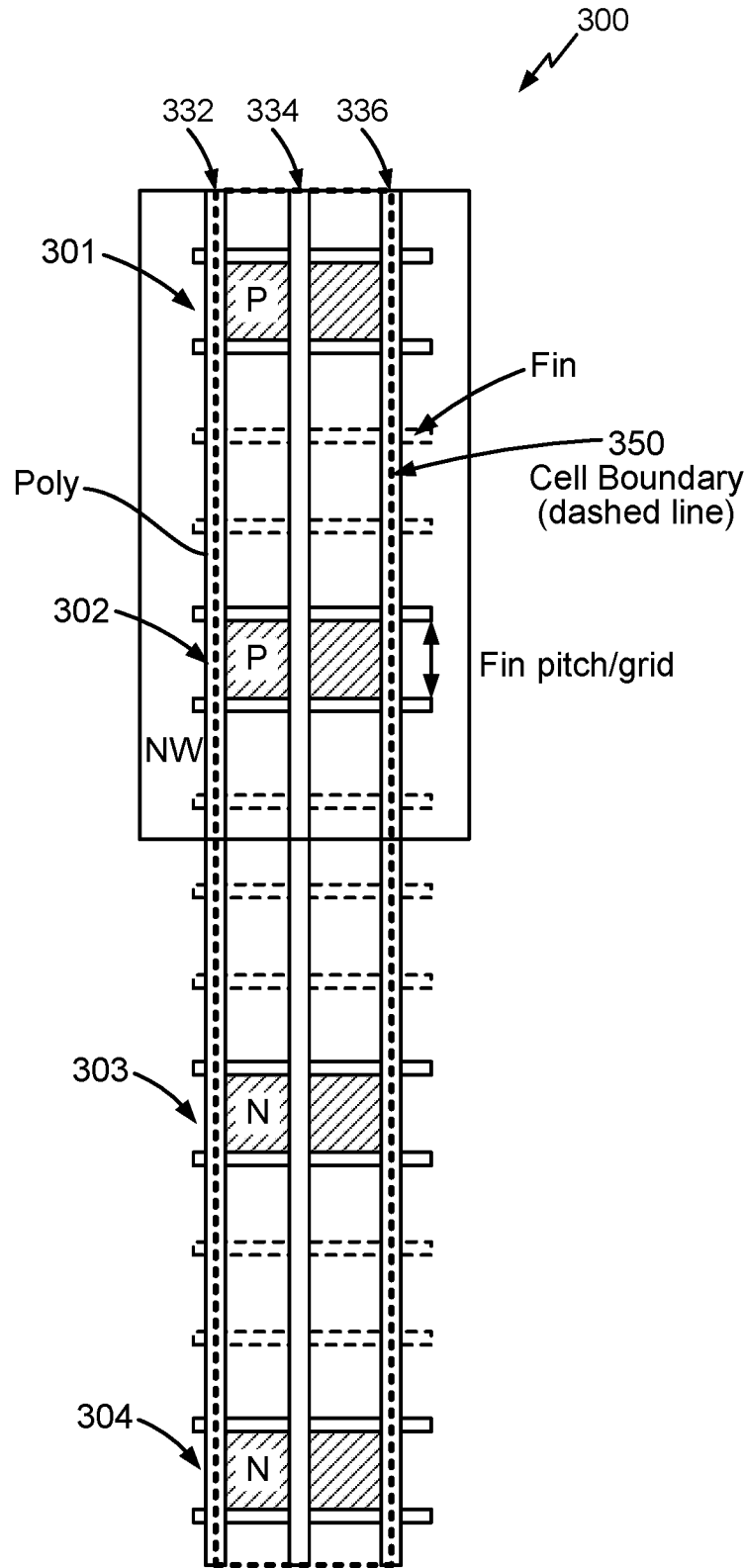


FIG. 3

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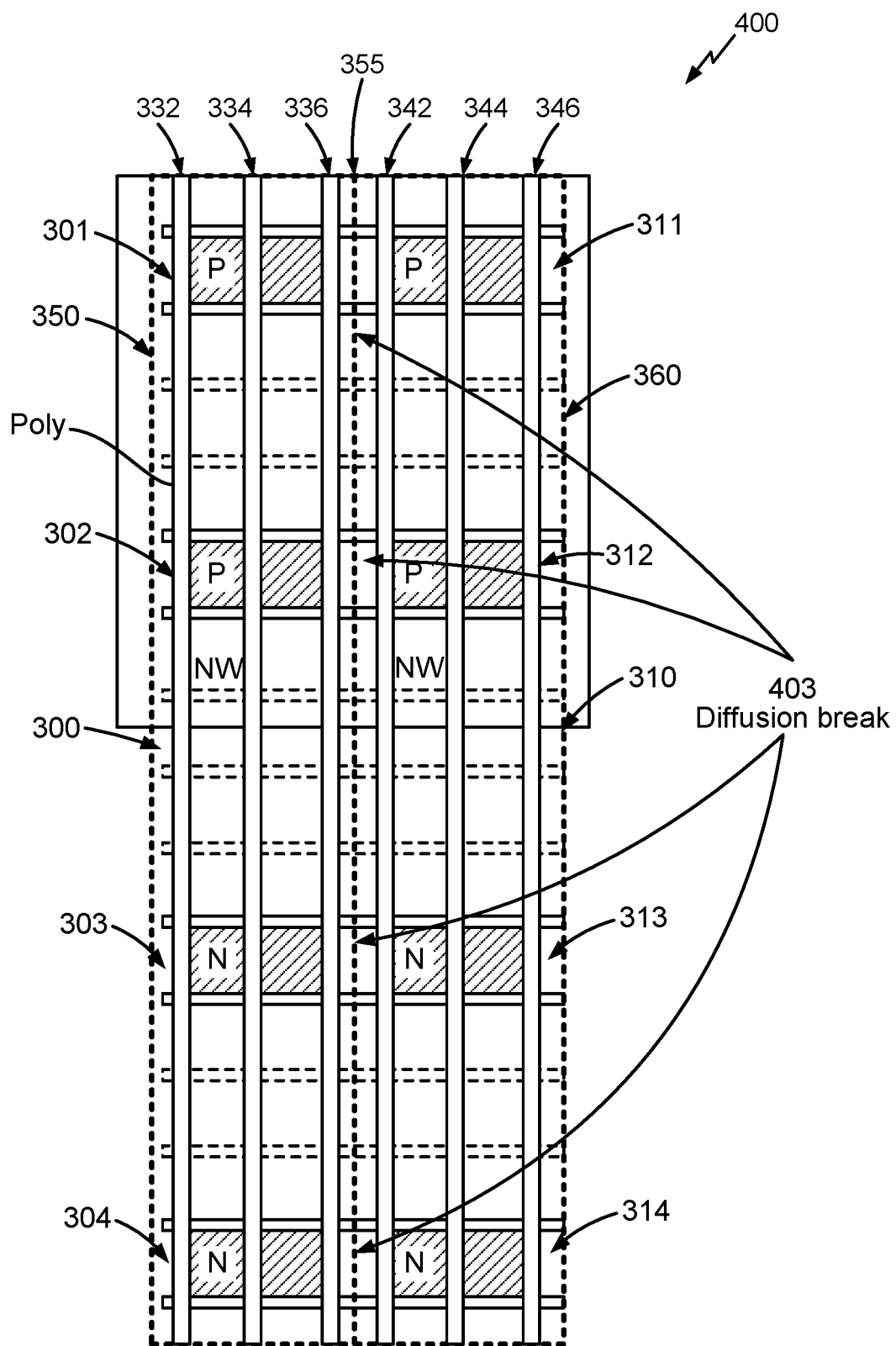


FIG. 4

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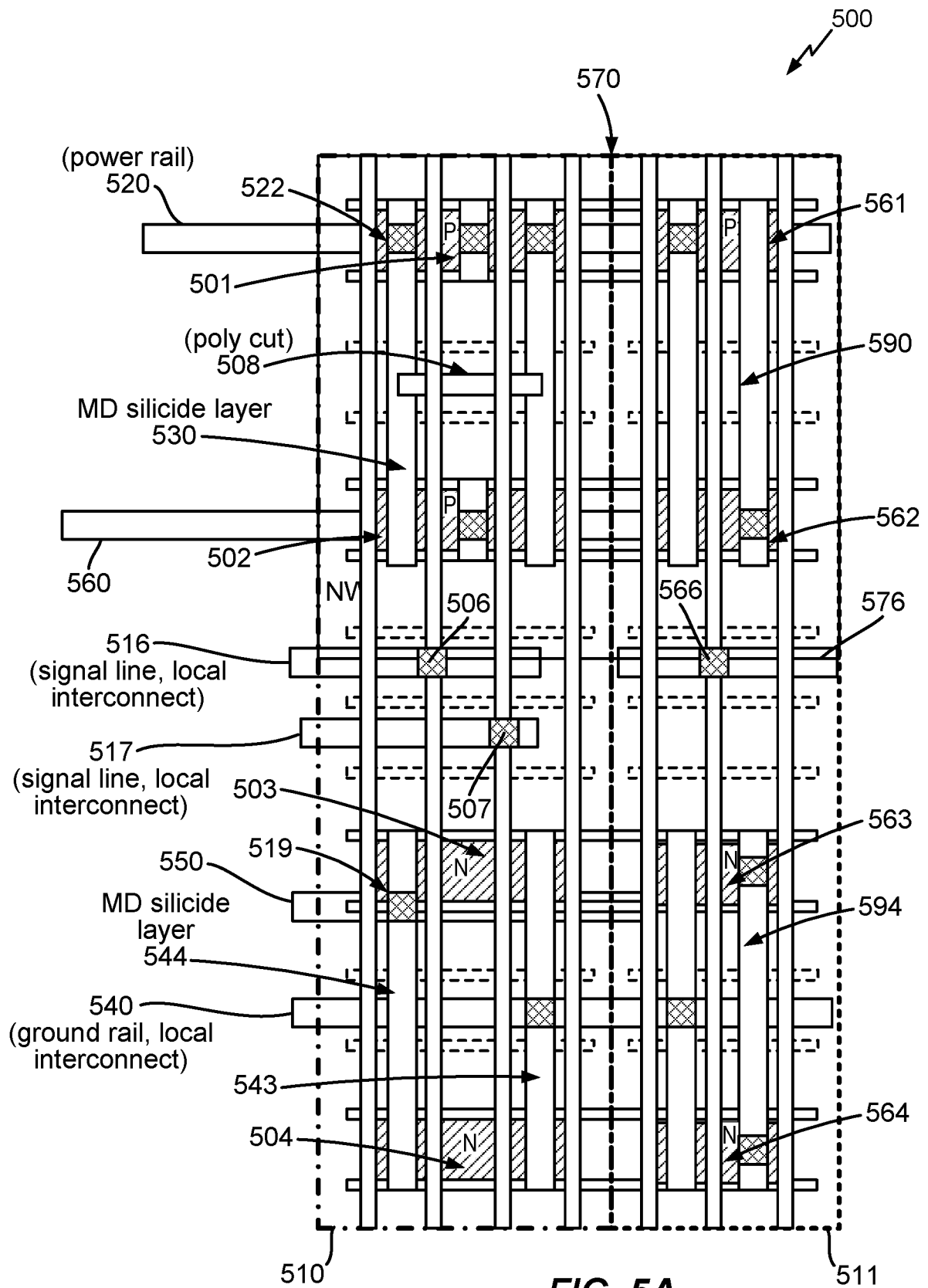


FIG. 5A

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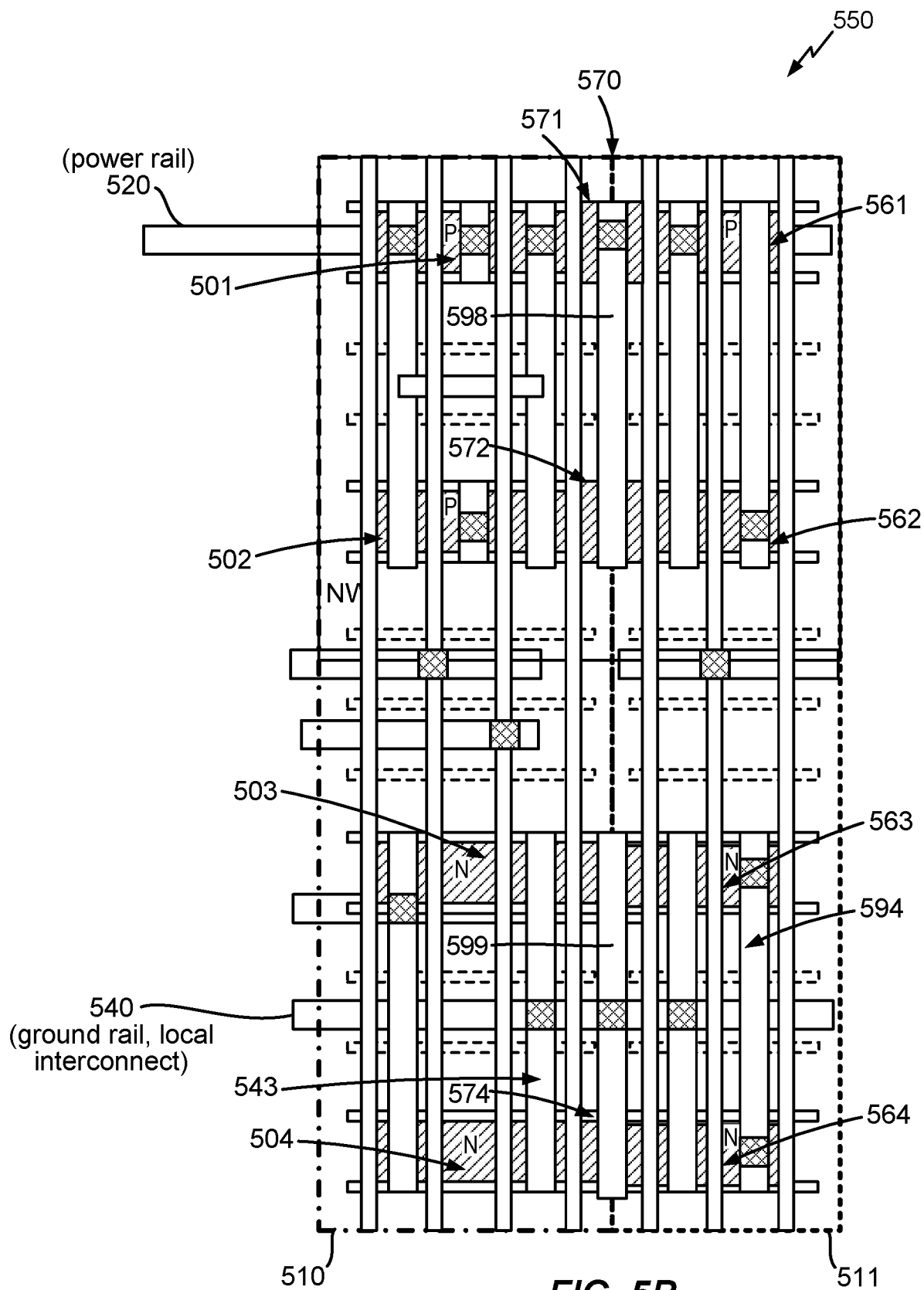


FIG. 5B

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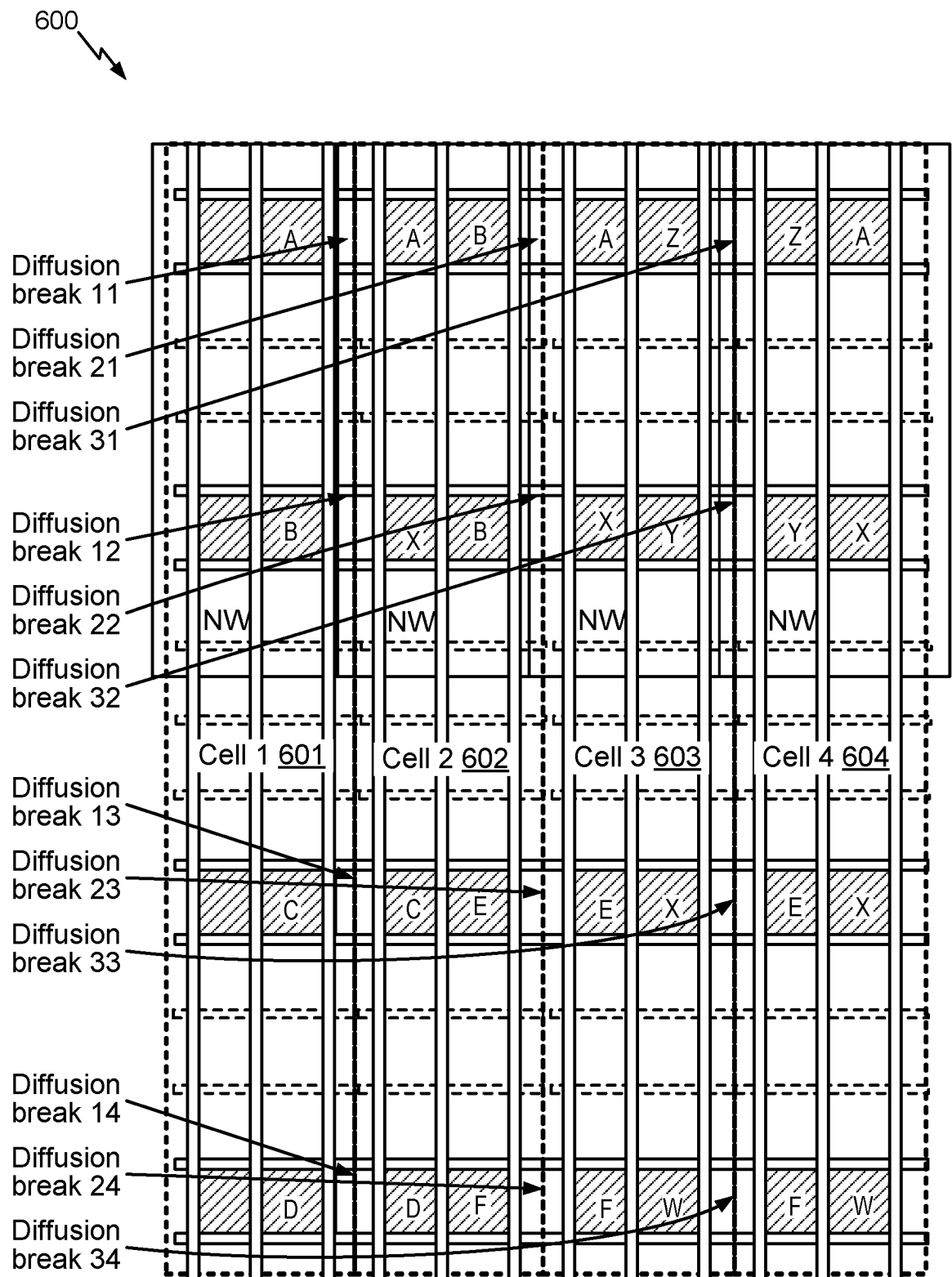
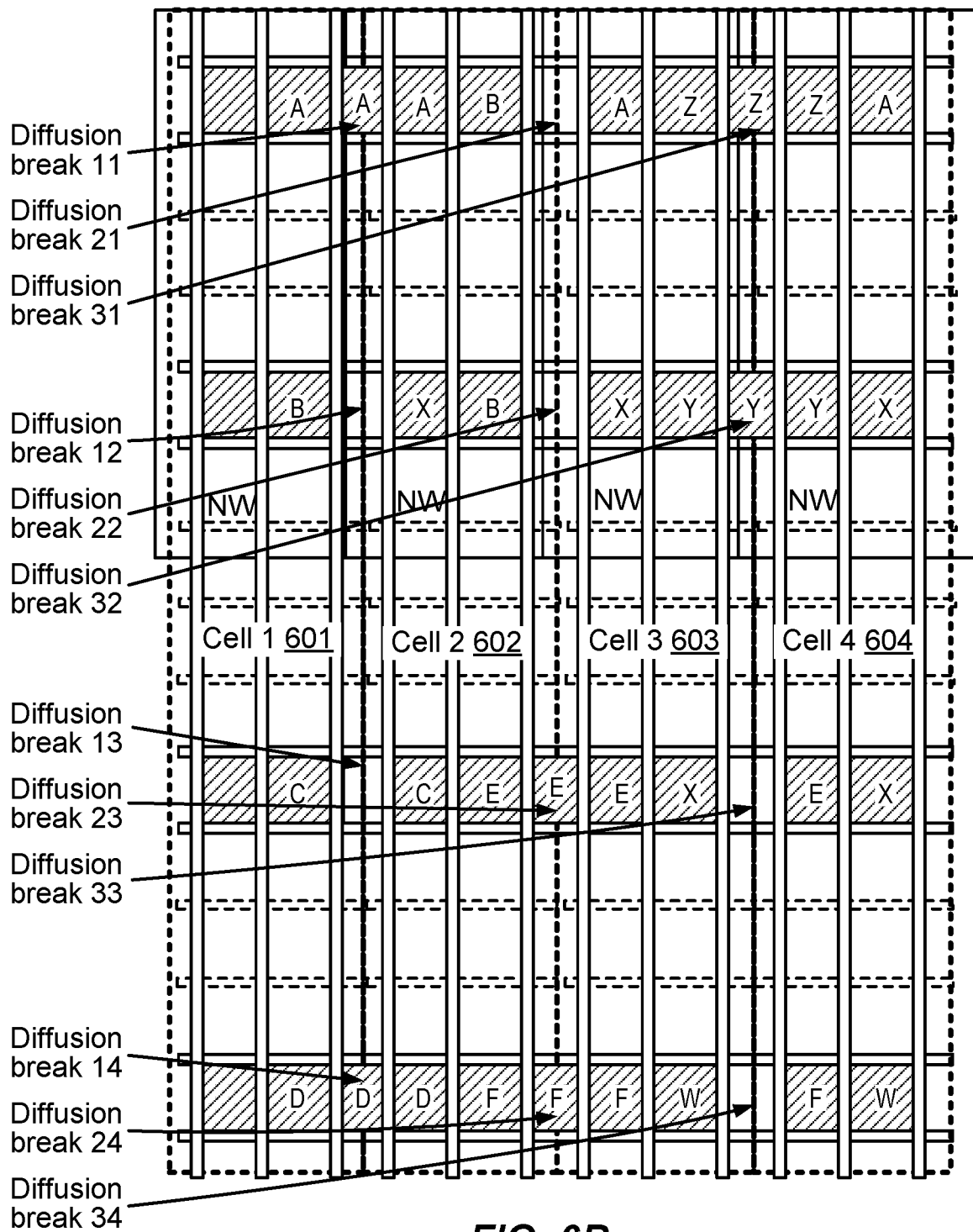


FIG. 6A

650



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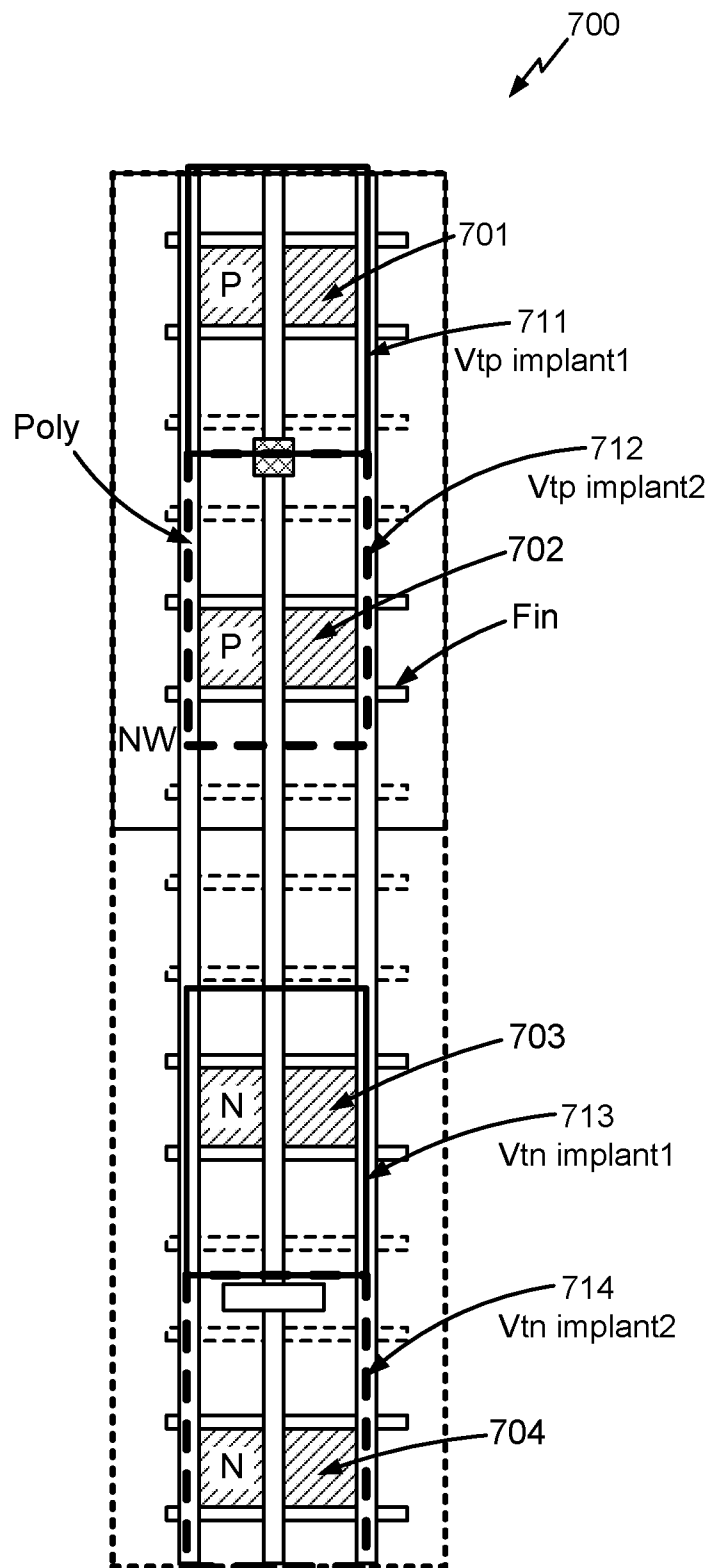


FIG. 7

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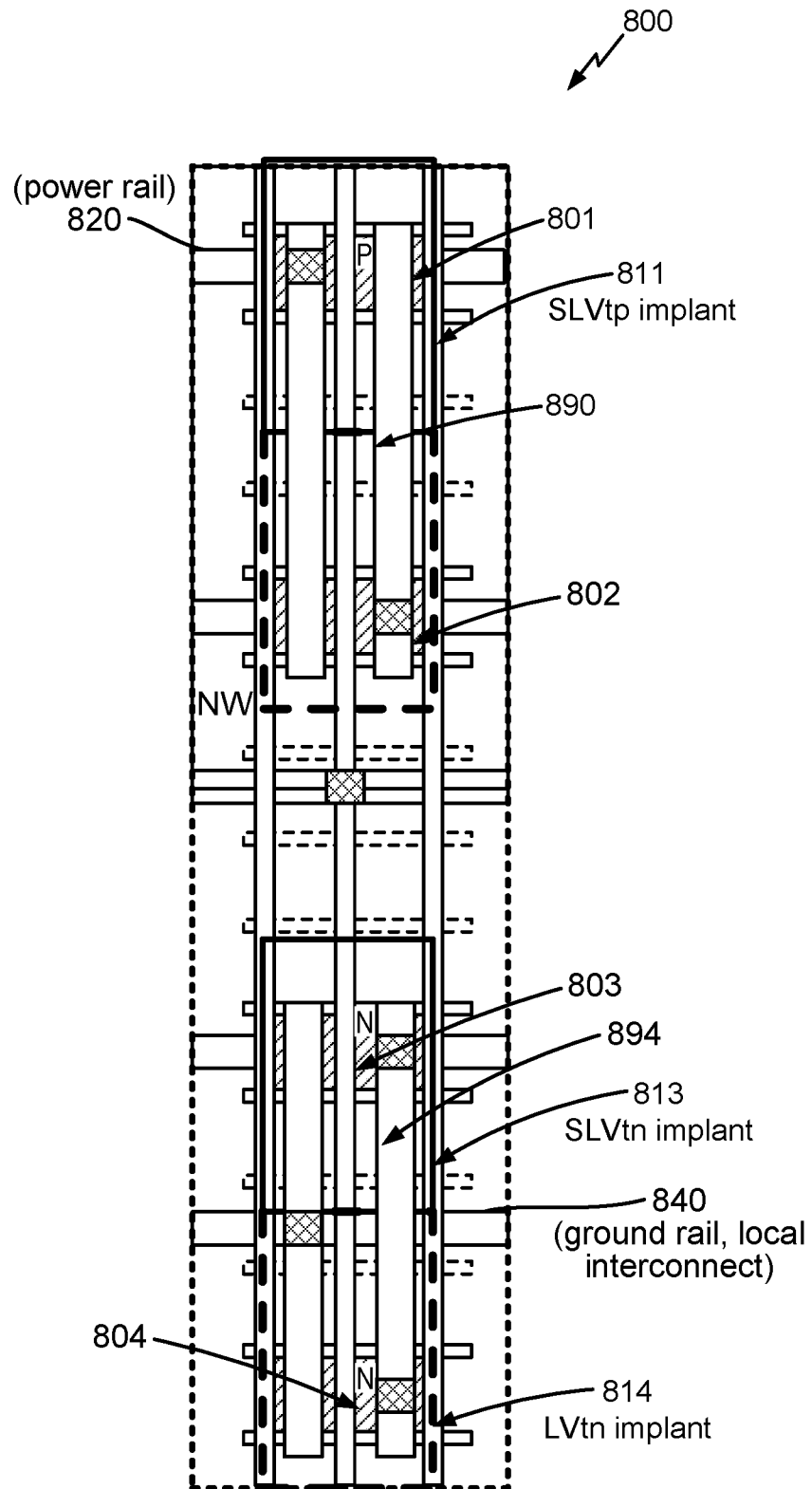


FIG. 8

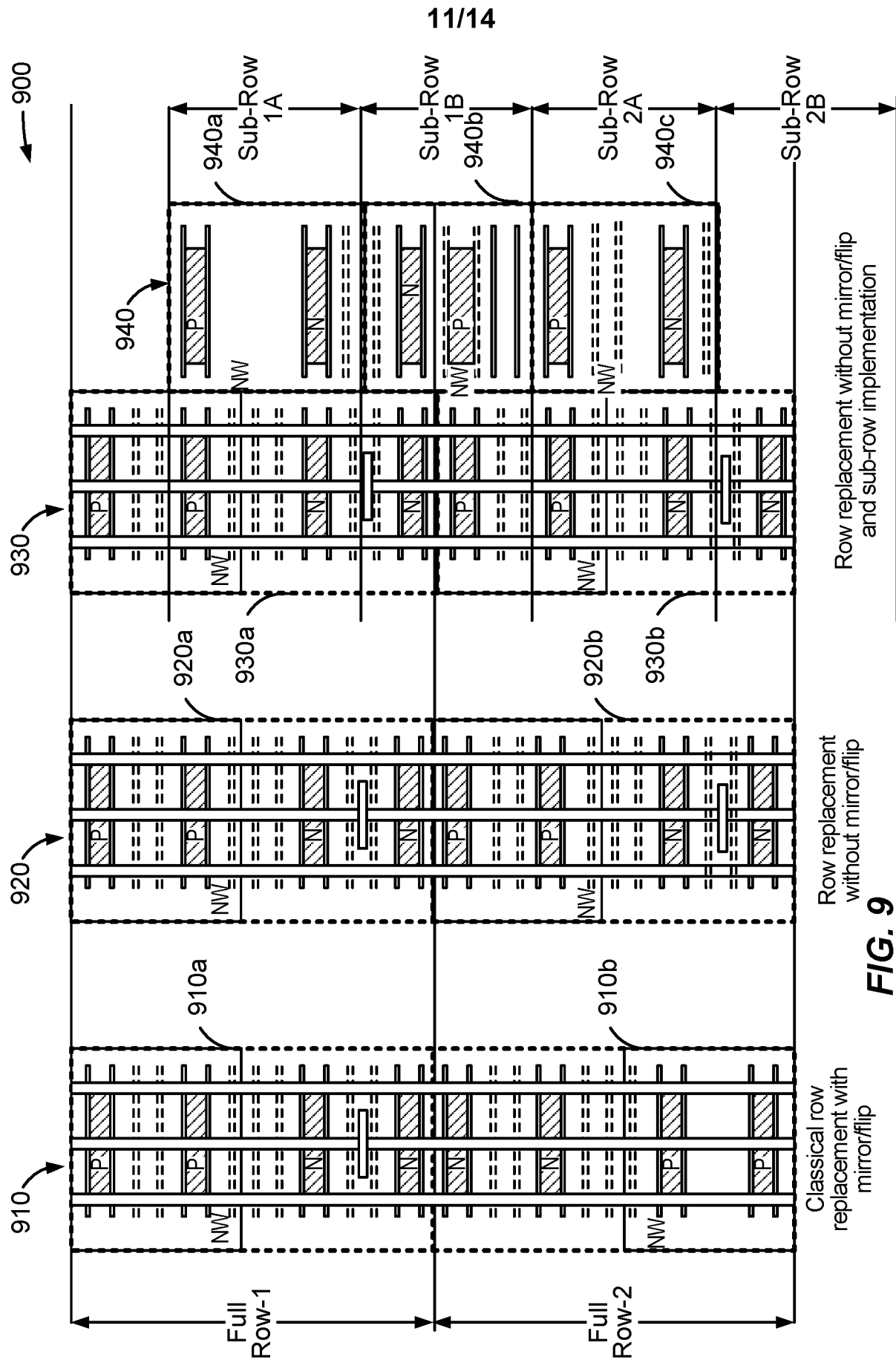
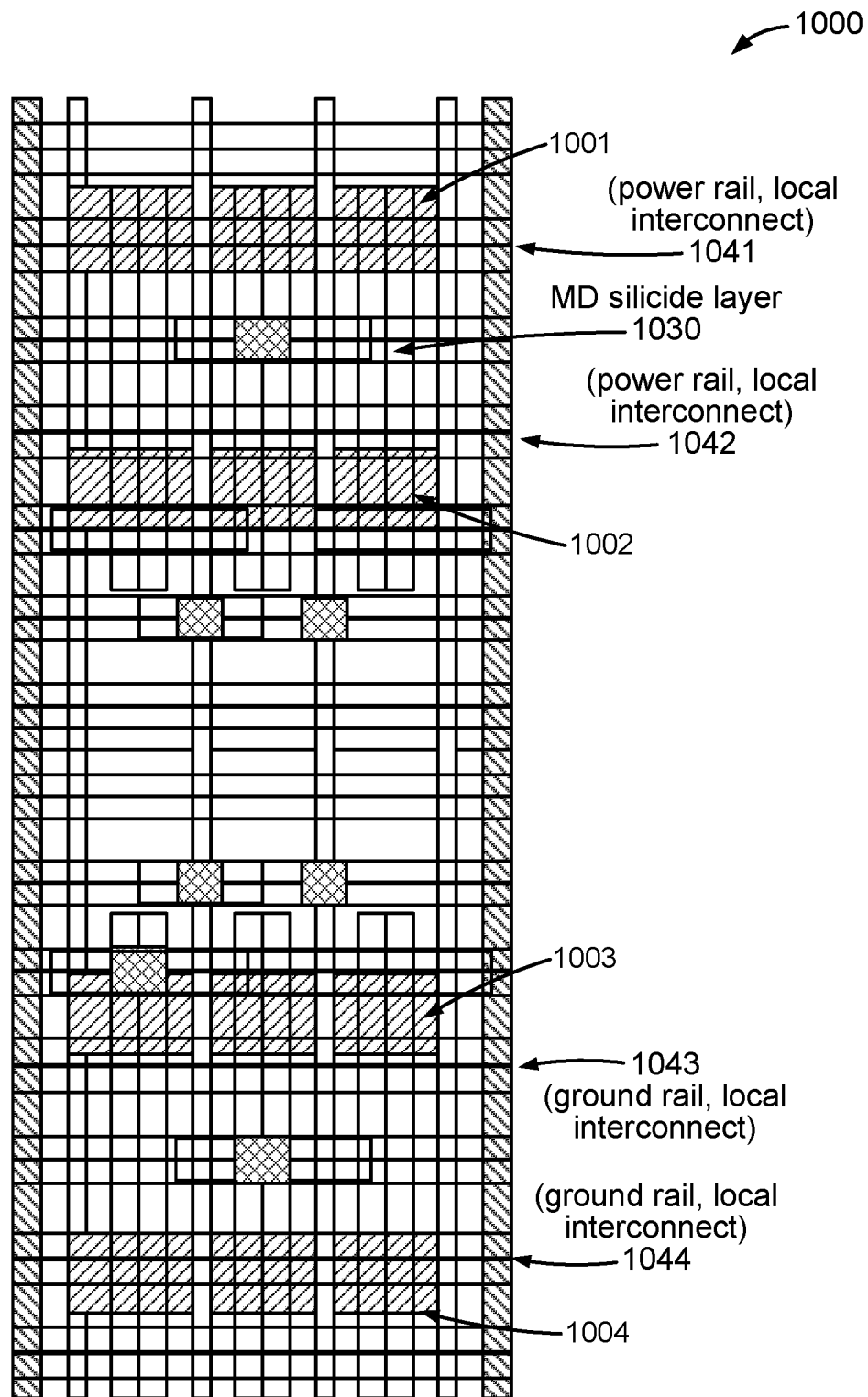


FIG. 9

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Standard cell architecture illustrating distributed power and ground rails

FIG. 10

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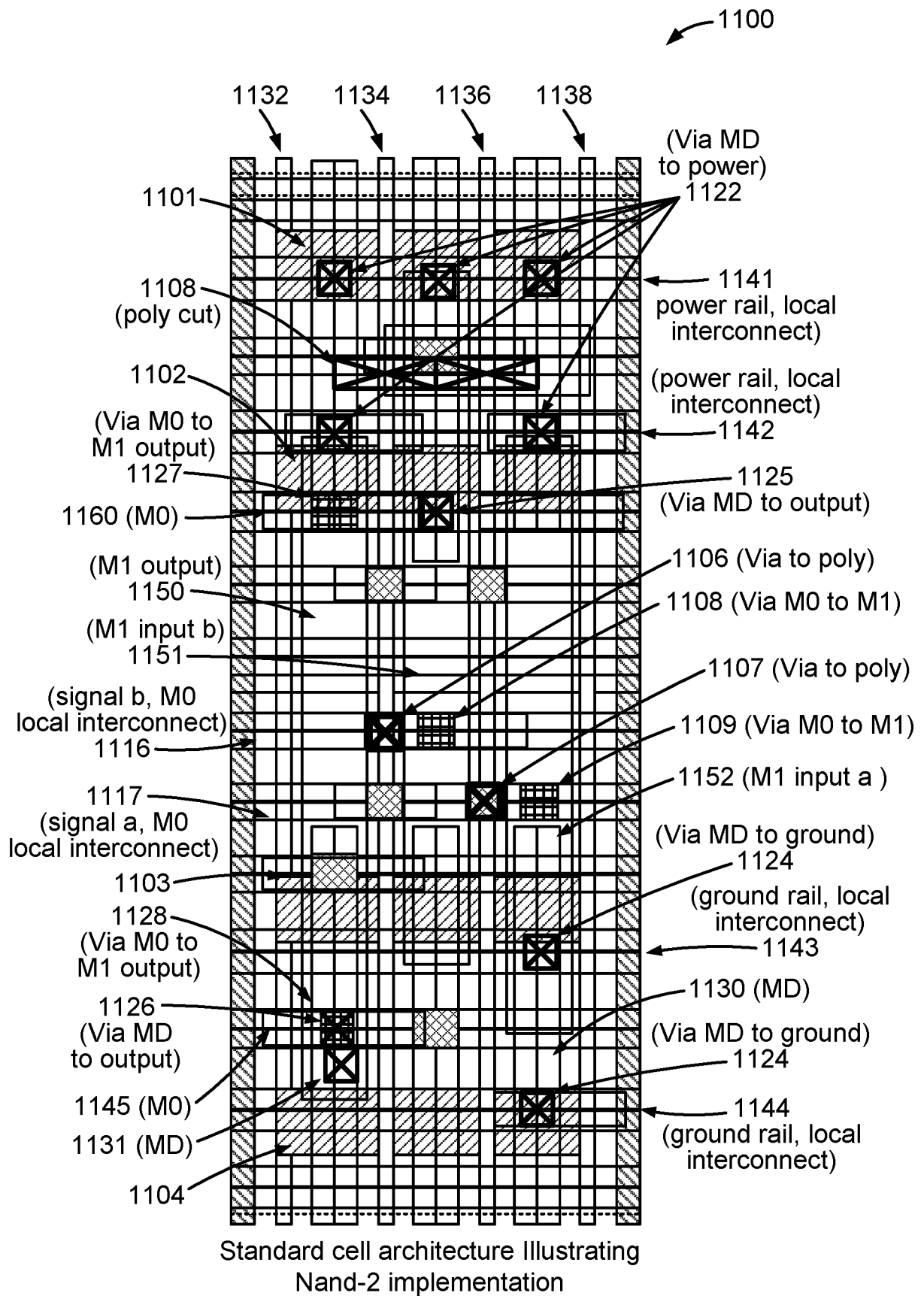
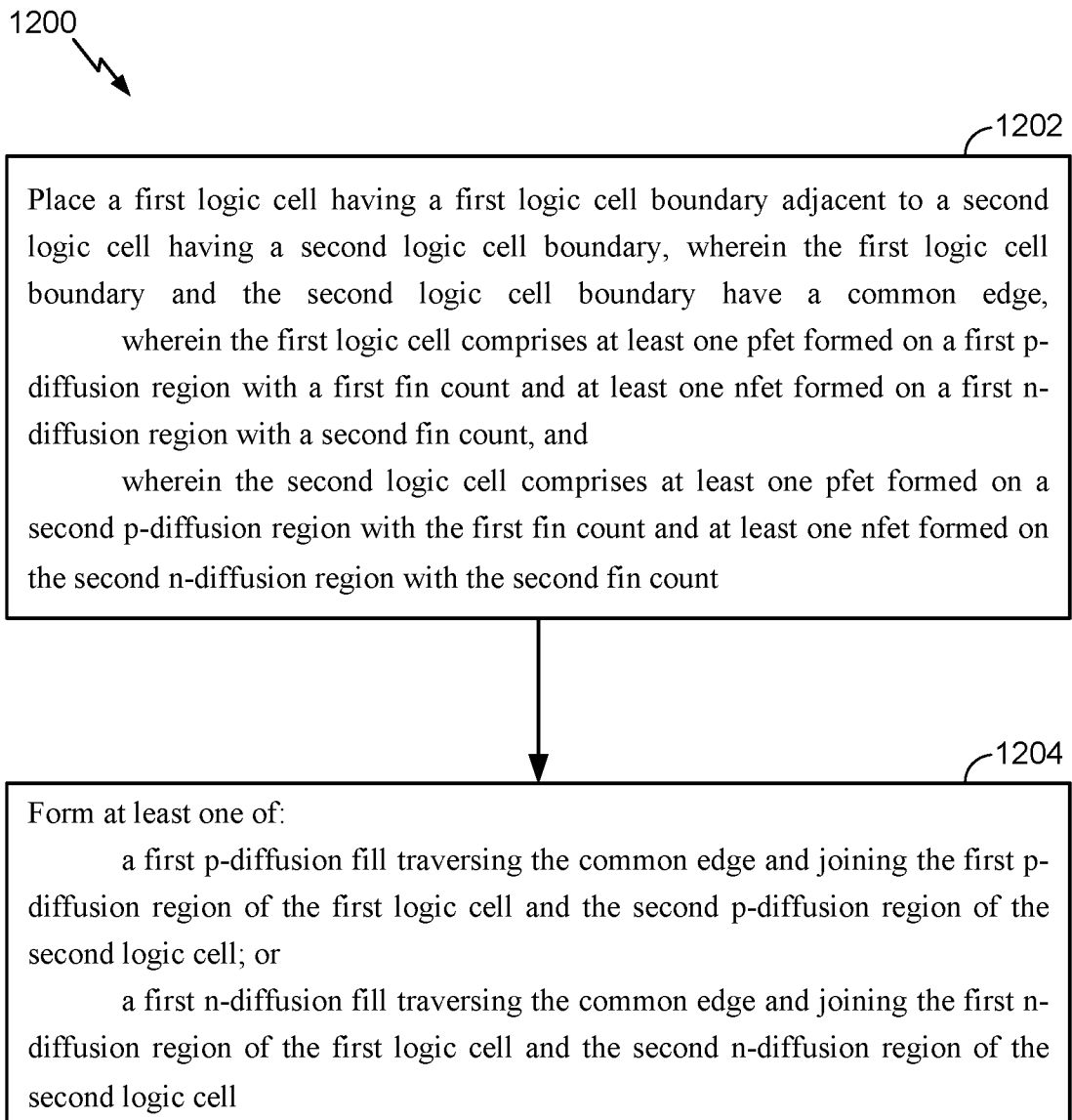


FIG. 11

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**FIG. 12**